Medium-Power Complementary Silicon Transistors

... for use as output devices in complementary general purpose amplifier applications.

WWW.DZSC.COM

- High DC Current Gain hFE = 6000 (Typ) @ IC = 3.0 Adc
- Monolithic Construction with Built-in Base-Emitter Shunt Resistors

NPN MJ1000 MJ1001*

*Motorola Preferred Device

10 AMPERE
DARLINGTON
POWER TRANSISTORS
COMPLEMENTARY
SILICON
60-80 VOLTS
90 WATTS



CASE 1-07 TO-204AA (TO-3)

MAXIMUM RATINGS

Rating	Symbol	MJ1000	MJ1001	Unit
Collector–Emitter Voltage	VCEO	60	80	Vdc
Collector–Base Voltage	VCB	60	80	Vdc
Emitter–Base Voltage	V _{EB}	5.0		Vdc
Collector Current	lC	10		Adc
Base Current	IB	0.1		Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	90 0.515		Watts W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +200		°C

THERMAL CHARACTERISTICS

Characteristic		Symbol	Max	Unit
Thermal Resistance, Junction to Case		$R_{ heta JC}$	1.94	°C/W
DVD	COLLECTOR	A LAST	COLLECTOR	

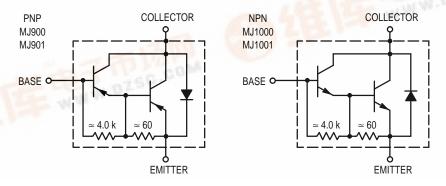


Figure 1. Darlington Circuit Schematic



MJ1000 MJ1001

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage ⁽¹⁾ (I _C = 100 mAdc, I _B = 0) MJ1000 MJ1001		V(BR)CEO	60 80	_ _	Vdc
Collector Emitter Leakage Current (V _{CB} = 60 Vdc, R _{BE} = 1.0k ohm) (V _{CB} = 80 Vdc, R _{BE} = 1.0k ohm) (V _{CB} = 60 Vdc, R _{BE} = 1.0k ohm, T _C = 150°C) (V _{CB} = 80 Vdc, R _{BE} = 1.0k ohm, T _C = 150°C)	MJ1000 MJ1001 MJ1000 MJ1001	ICER	_ _ _	1.0 1.0 5.0 5.0	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}	_	2.0	mAdc
Collector Emitter Leakage Current (V _{CE} = 30 Vdc, I _B = 0) (V _{CE} = 40 Vdc, I _B = 0)		ICEO	_ _	500 500	μAdc
ON CHARACTERISTICS					
DC Current Gain ⁽¹⁾ (I _C = 3.0 Adc, V_{CE} = 3.0 Vdc) (I _C = 4.0 Adc, V_{CE} = 3.0 Vdc)		hFE	1000 750	_	
Collector Emitter Saturation Voltage(1) (I _C = 30 Adc, I _B = 12 mAdc) (I _C = 8.0 Adc, I _B = 40 mAdc)		VCE(sat)	_	2.0 4.0	Vdc
Base Emitter Voltage(1) (I _C = 3.0 Adc, V _{CE} = 3.0 Vdc)		V _{BE(on)}	_	2.5	Vdc

(1) Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

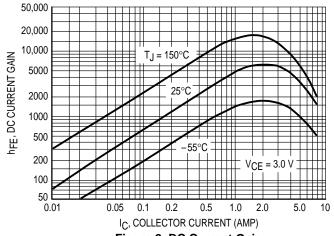


Figure 2. DC Current Gain

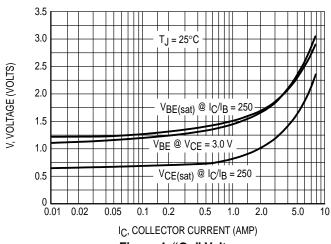


Figure 4. "On" Voltages

There we two limitations on the power handling ability of a transistor: average junction temperature and secondary breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; e.g., the transistor must not be subjected to greater

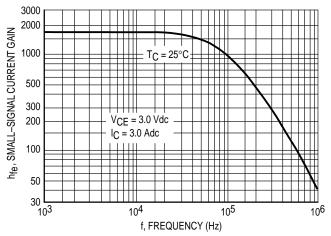


Figure 3. Small-Signal Current Gain

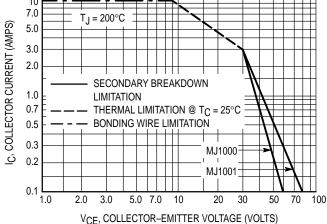
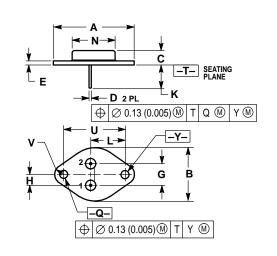


Figure 5. DC Safe Operating Area

dissipation than the curves indicate.

At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by secondary breakdown.

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	1.550 REF		39.37	REF	
В		1.050		26.67	
С	0.250	0.335	6.35	8.51	
D	0.038	0.043	0.97	1.09	
E	0.055	0.070	1.40	1.77	
G	0.430 BSC		10.92 BSC		
Н	0.215 BSC		5.46 BSC		
K	0.440	0.480	11.18	12.19	
L	0.665	0.665 BSC		BSC	
N		0.830		21.08	
Q	0.151	0.165	3.84	4.19	
U	1.187 BSC		30.15 BSC		
٧	0.131	0.188	3.33	4.77	

STYLE 1: PIN 1. BASE 2. EMITTER CASE: COLLECTOR

CASE 1-07 TO-204AA (TO-3) ISSUE Z

MJ1000 MJ1001

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

How to reach us: USA/EUROPE: Motorola Literature Distribution;

P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, Toshikatsu Otsuki, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–3521–8315

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE (602) 244–6609 INTERNET: http://Design=NET.com

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

