

IC for Control of Lithium-ion Batteries Charging Monolithic IC MM1433

Outline

This IC is used to control charging of lithium-ion batteries. This one IC incorporates functions for constant-current and constant-voltage charging and for precharging, for an overcharge timer, battery temperature detection, and other protective functions. It was developed by adding to the previous MM1332 and 1333 the above-described functions.

Features

1. Charging voltage accuracy $\pm 30\text{mV/cell}$.
2. Consumption current 5mA typ.
3. Precharge function.
4. Recharge function.
5. Overcharge timer.
6. Battery temperature detection function.
7. We can supply type for one and two cells.

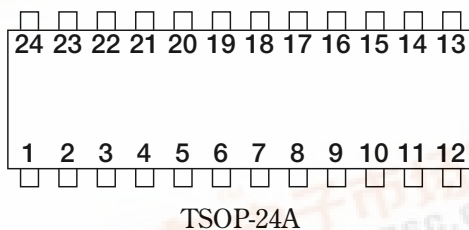
Package

TSOP-24A

Applications

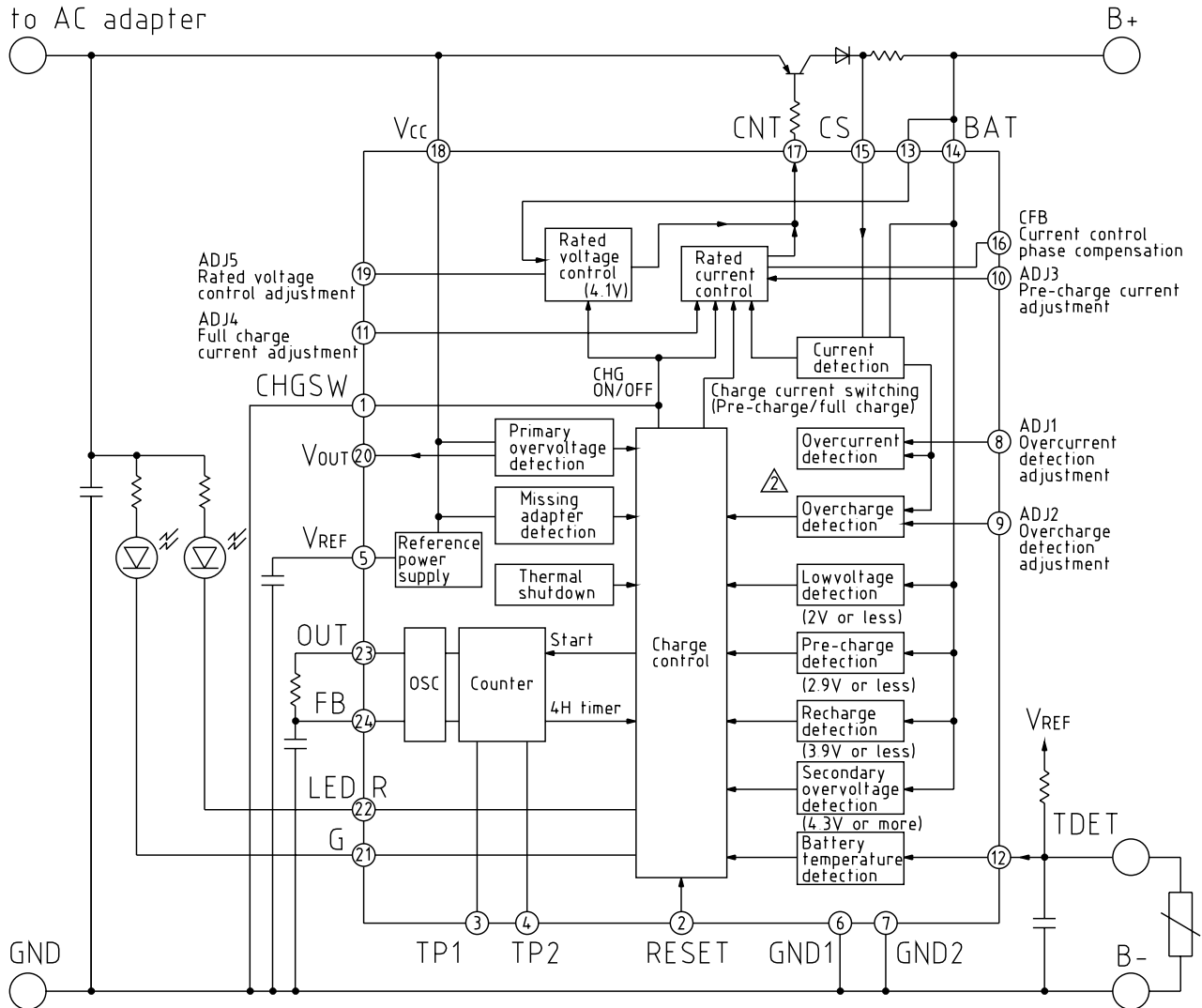
IC for control of lithium-ion batteries charging.

Pin Assignment



1	CHGSW	13	BAT1
2	RESET	14	BAT2
3	TP1	15	CS
4	TP2	16	CFB
5	VREF	17	CNT
6	GND1	18	V _{CC}
7	GND2	19	ADJ5
8	ADJ1	20	V _{OUT}
9	ADJ2	21	LED G
10	ADJ3	22	LED R
11	ADJ4	23	OSC OUT
12	TDET	24	OSC FB-

Block Diagram

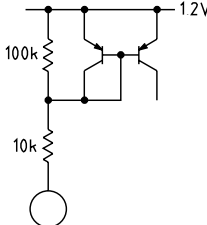
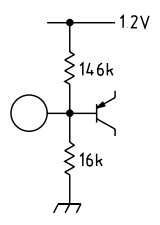
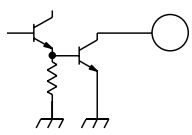
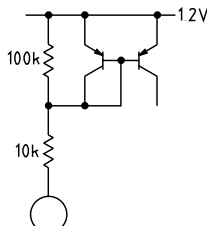
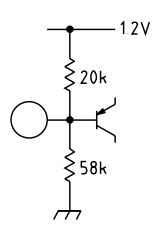
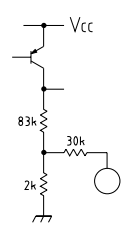
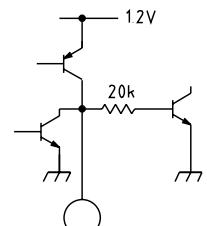
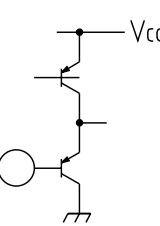
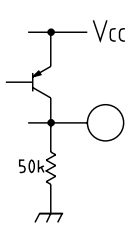
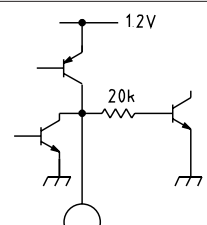
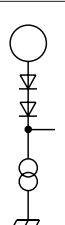
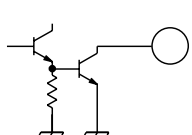
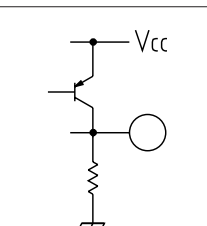

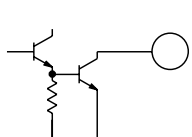
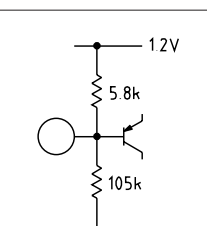
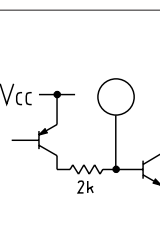
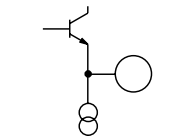
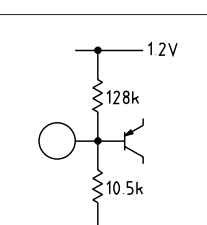
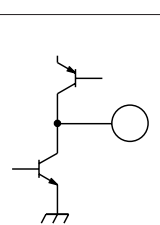
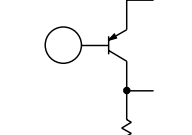


Pin Description

Pin No.	Pin name	I/O	Functions
1	CHGSW	Input	Forced charging OFF pin L: Forced charging circuit ON (OFF for reset) H: Charging stop is forced
2	RESET	Input	Logic reset pin L: Forced charging circuit ON (start) H: Forced charging circuit OFF
3	TP1	Input/ Output	Test pin 1 Pre-charge timer test pin Inverts while counting (the middle stage of the several FF stages) and output to TP1, to permit monitoring. Also, TP1 output signal is inverted again inside the IC and inputs to the next stage FF. (Timer setting is done by binary counter.)
4	TP2	Input/ Output	Test pin 2 Full charge timer test pin Same structure as TP1
5	VREF	Output	Reference power supply output pin Outputs 1.2V typ. reference voltage. Used for temperature detection reference power supply and ADJ1 - ADJ4 adjustment.
6	GND1	Input	Ground pin.
7	GND2	Input	Ground pin.
8	ADJ1	Input	Overcurrent detection adjustment pin Set so that overcurrent detection does not function. Pin voltage is 1.16V typ.
9	ADJ2	Input	Full charge detection adjustment pin Pin voltage is set at 93mV typ. Full charge detection value can be changed by adjusting pin voltage with an external resistor, etc. Full charge detection is done by comparing ADJ2 pin voltage and 12dB voltage drop value between CS and BAT.
10	ADJ3	Input	Pre-charge current adjustment pin Pin voltage is set at 120mV typ. Pre-charge current can be changed by adjusting pin voltage with an external resistor, etc. Pre-charge current control is done by comparing ADJ3 pin voltage and 12dB voltage drop value between CS and BAT.
11	ADJ4	Input	Full charge current adjustment pin Pin voltage is set at 0.89mV typ. Full charge current can be changed by adjusting pin voltage with an external resistor, etc. Full charge current control is done by comparing ADJ4 pin voltage and 12dB voltage drop value between CS and BAT. When full charge current is controlled to rated current by an adapter, short ADJ4 pin and VREF pin so that rated current control does not function in the IC.

Pin No.	Pin name	I/O	Functions
12	TDET	Input	Temperature detection input pin Apply potential resistance divided by external resistor and thermistor from reference voltage when using. Reset state will exist if TDET pin does not reach the specified potential.
13	BAT1	Input	Battery voltage input pins Detect battery voltage and control charging.
14	BAT2	Input	
15	CS	Input	Current detection pin Detects current by external resistor (between CS and BAT) voltage drop and controls charging current.
16	CFB	Input	Rated current control phase compensation pin Oscillation is improved by connecting an external capacitor (around 100pf) between CFB and CNT for phase compensation.
17	CNT	Output	Charging control output pin Controls external PNP-Tr base for rated current rated voltage charging.
18	V _{CC}	Input	Power supply input pin
19	ADJ5	Input	Rated voltage control adjustment pin Allows fine adjustment of rated voltage value. For example, rated voltage value rises by around 15mV (at 4.1V typ.) when ADJ5-GND is shorted.
20	V _{OUT}	Output	Overvoltage detection output pin For V _{CC} overvoltage input: L For V _{CC} recommended operating voltage: H
21	LED G	Output	LED C control output pin NPN-Tr open collector output. Refer to the flow chart for ON/OFF.
22	LED R	Output	LED R control output pin NPN-Tr open collector output. Refer to the flow chart for ON/OFF.
23	OSC OUT	Output	Oscillator output pin Timer setting time changes according to oscillation frequency. Oscillation frequency is determined by an external resistor (connected between OSC OUT and OSC FB) and capacitor (connected between OSC FB and GND). For example, the full charge timer setting is 4H for external resistor of 130kΩ and capacitor of 0.01μF.
24	OSC FB-	Input	Oscillator inverted input pin

Pin Description (The values below are average values)

Pin No.	Pin name	Equivalent circuit diagram	Pin No.	Pin name	Equivalent circuit diagram	Pin No.	Pin name	Equivalent circuit diagram
1	CHGSW		10	ADJ3		17	CNT	
2	RESET		11	ADJ4		19	ADJ5	
3	TP1		12	TDET		20	VOUT	
4	TP2		13	BAT1		21	LED G	
5	VREF		14	BAT2		22	LED R	
8	ADJ1		15	CS		23	OSC OUT	
9	ADJ2		16	CFB		24	OSC FB-	

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Unit
Storage temperature	T _{STG}	-40~+125	°C
Operating temperature	T _{OPR}	-20~+70	°C
Power supply voltage	V _{CC} max.	-0.3~+15	V
Allowable loss	P _d	250	mW

Recommended Operating Conditions

Item	Symbol	Ratings	Unit
Operating temperature	T _{OPR}	-20~+70	°C
Charging control operating voltage	V _{OPR}	2.7~5.9	V

Electrical Characteristics (Except where noted otherwise, Ta=25°C, V_{CC}=5V)

Item	Symbol	Conditions	Measurement circuit	Min.	Typ.	Max.	Unit
Consumption current	I _{CC}		18		5.0	7.0	mA
Reference voltage	V _{REF}		5		1.207		V
ADP detection voltage L	V _{ADPL}	V _{CC} : H→L	20	2.35	2.45	2.55	V
ADP detection voltage L Hysteresis voltage width	V _{ADPLW}		20	50	100	150	mV
ADP detection voltage H	V _{ADPH}	V _{CC} : L→H	20	6.1	6.3	6.5	V
ADP detection voltage H Hysteresis voltage width	V _{ADPHW}		20	50	100	150	mV
Impedance for ADP detection output L	Z _{ADPL}		20		30		kΩ
BAT pin leak current	I _{BAT}		13, 14, 15			1	μA
BAT pin output voltage	V _{BAT}	Ta=0~+50°C	13	4.070	4.100	4.130	V
CNT pin output voltage	V _{CNT}	I _{CNT} =20mA	17			0.5	V
CHGSW pin input current	I _{SW}		1	40	60	80	μA
CHGSW pin input voltage H	V _{SWH}	CHGSW : OFF	1	0.6		1.20	V
CHGSW pin input voltage L	V _{SWL}	CHGSW : ON	1			0.25	V
RESET pin input current	I _{RE}		2	40	60	80	μA
RESET pin input voltage H	V _{REH}	Charging control circuit: OFF	2	0.6		1.20	V
RESET pin input voltage L	V _{REL}	Charging control circuit: ON	2			0.25	V
Current limit 1	V _{L1}	Quick charge	14, 15	0.20	0.22	0.24	V
Current limit 2	V _{L2}	Pre-charge	14, 15	21	26	31	mV
Full charge detection	V _F		14, 15	13	18	23	mV
Low voltage detection voltage	V _{LV}	V _{BAT} : L→H	13	1.90	2.00	2.10	V

Item	Symbol	Conditions	Measurement circuit	Min.	Typ.	Max.	Unit
Low voltage detection voltage Hysteresis voltage width	V_{LVW}		13	25	50	100	mV
Pre-charge detection voltage	V_P	$V_{BAT} : L \rightarrow H$	13	2.80	2.90	3.00	V
Pre-charge detection voltage Hysteresis voltage width	V_{PW}		13	25	50	100	mV
Re-charge detection voltage	V_R	$V_{BAT} : H \rightarrow L$	13	3.85	3.90	3.95	V
Overvoltage detection voltage	V_{OV}	$V_{BAT} : L \rightarrow H$	13	4.30	4.35	4.40	V
Battery temperature detection voltage H	V_{TH}	Low temperature $3^{\circ}\text{C} \pm 3^{\circ}\text{C}$ detection	12	0.835	0.860	0.885	V
Battery temperature detection voltage L1	V_{TL1}	High temperature $43^{\circ}\text{C} \pm 3^{\circ}\text{C}$ detection (charging start)	12	0.390	0.413	0.435	V
Battery temperature detection voltage L2	V_{TL2}	High temperature $50^{\circ}\text{C} \pm 3^{\circ}\text{C}$ detection (during charging)	12	0.335	0.353	0.370	V
TDET input bias current	I_T		12		30	150	nA
LED R pin output voltage	V_{LEDR}	$I_{LEDR}=10\text{mA}$	22			0.4	V
LED G pin output voltage	V_{LEDG}	$I_{LEDG}=10\text{mA}$	21			0.4	V
Timer error time	ΔT	Not including external deviation	21, 22	-10		10	%

Note 1: Current limits 1 and 2 and full charge detection are specified at current detection resistor voltage drop.

Note 2: If the IC is damaged and control is no longer possible, its safety can not be guaranteed. Please protect with something other than this IC.

Note 3: Temperature detection is the setting value at B constant 3435 (10KC15-1608 made by Ishizuka Denshi).

Note 4: Use a capacitor with good temperature characteristics in the oscillator. Capacitor deviation will contribute to timer error.

Note 5: If the battery overdischarges, charge 1mA for 14 seconds, and if it does not switch to pre-charging during that interval, it means the IC has identified a battery abnormality.

OSC CR Setting Reference Materials

(1) OSCR CR-Oscillation Cycle T Examples

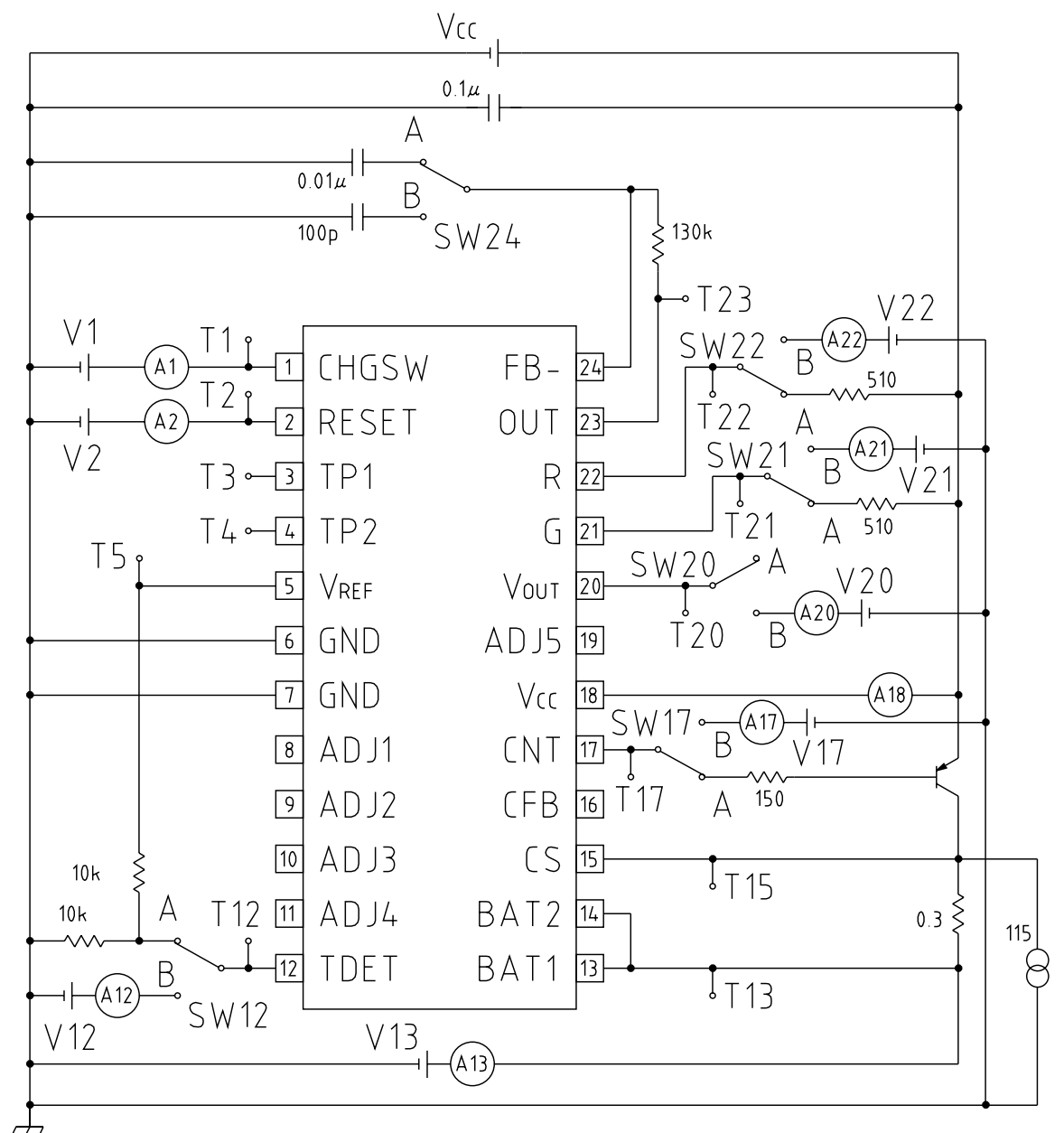
<div> R C </div>	75k	100k	120k	130k	150k	200k
0.0047 μ	0.47mS	0.63mS	0.75mS	0.82mS	0.94mS	1.26mS
0.0082 μ	0.83mS	1.10mS	1.32mS	1.43mS	1.65mS	2.20mS
0.01 μ	1.03mS	1.37mS	1.63mS	1.77mS	2.04mS	2.73mS
0.015 μ	1.48mS	1.98mS	2.38mS	2.58mS	2.97mS	3.95mS
0.022 μ	2.16mS	2.87mS	3.44mS	3.73mS	4.30mS	5.76mS

(2) Timer Times

Item	Calculation formula	Examples of calculation (for C = 0.01μ, R = 230k)
Pre-charge timer	$T \times 2^{19}$	15min. 28S
Full charge timer	$T \times 2^{23}$	4h7min.
1mA charge time	$T \times 2^{13}$	14.5S
Full charge detection delay time	$T \times 2^6$	0.11S
Overcurrent detection delay time	$T \times 2^8$	0.45S
Overvoltage detection delay time	$T \times 2^8$	0.45S
Re-charge detection delay time	$T \times 2^5$	56.6mS
LED R blinking cycle	$T \times 2^{10}$	1.8S

Note: T: OSC oscillation cycle

Measuring Circuit



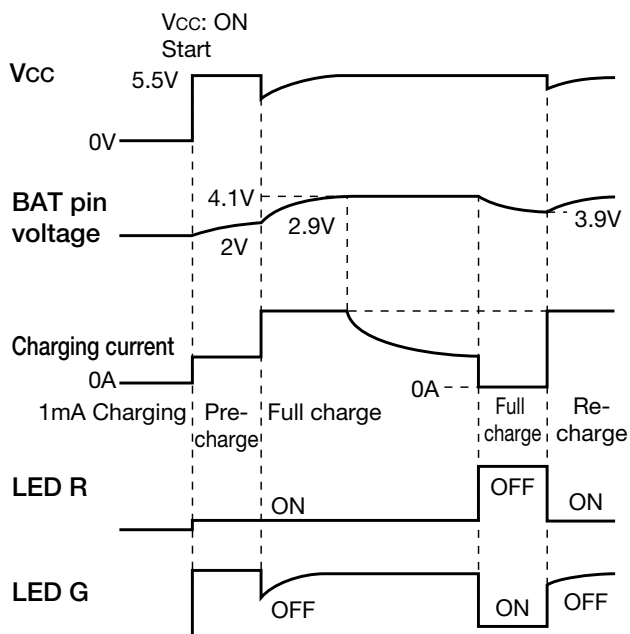
Measurement Procedures

(Except where noted otherwise, $T_a = 25^\circ\text{C}$, $V_{CC}=5\text{V}$, $V_1=V_2=0\text{V}$, $V_{13}=4.2\text{V}$, SW12, 17, 20, 22, 24:A, $I_{15}=0\text{mA}$ Timers are not in time up state.)

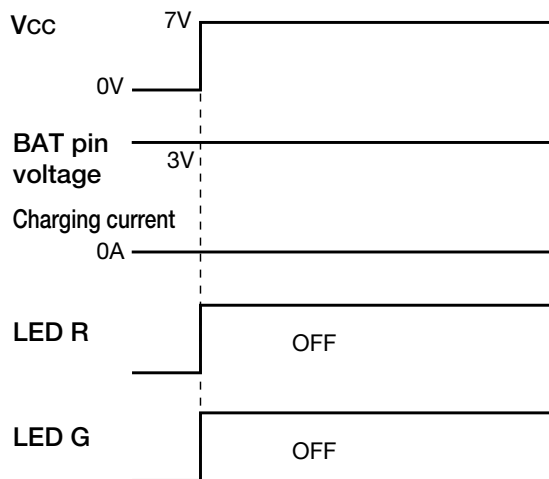
Item	Measurement Procedures
Consumption current	$V_1 = 1.2\text{V}$. Measure A18 current value I_{CC} .
Reference voltage	Measure T5 potential V_{REF} .
ADP detection voltage L	Gradually lower V_{CC} from 5V; V_{CC} - potential is V_{ADPL} when T20 potential drops below 0.5V.
ADP detection voltage L Hysteresis voltage width	Gradually lower V_{CC} - from 2V. V_{CC} - potential is V_{ADPL2} when T20 potential goes over $V_{CC} - 0.5\text{V}$. $V_{ADPLW} = V_{ADPL2} - V_{ADPL}$
ADP detection voltage H	Gradually increase V_{CC} from 5V. V_{CC} potential is V_{ADPH} when T20 potential drops below 0.5V.
ADP detection voltage H Hysteresis voltage width	Gradually lower V_{CC} from 7V. V_{CC} potential is V_{ADPH2} when T20 potential goes over $V_{CC} - 0.5\text{V}$. $V_{ADPHW} = V_{ADPH} - V_{ADPH2}$
Impedance for ADP Ldetection output	$V_{CC} = 7\text{V}$, SW20: B, $V_{20} = 0.5\text{V}$, impedance between T20-GND is Z_{ADPL} .
BAT pin leak current	$V_{CC} = 0\text{V}$, SW17: B, $V_{17} = 0\text{V}$. Measure A13 current value I_{BAT} .
BAT pin output voltage	Gradually lower V_{13} from 3.5V. T13 potential is V_{BAT} when T15 - T13 potential difference falls to less than 20mV.
CNT pin output voltage	$V_{13} = 3.5\text{V}$, SW17: B. Gradually raise V_{17} from 0V. T17 potential is V_{CNT} when A17 current value 20mA.
CHGSW pin input current	Measure A1 current value I_{SW} .
CHGSW pin input voltage H	$V_{13} = 3.5\text{V}$. Raise V_1 from 0V to 1.2V. CHGSW: ON when A13 is more than 500mA. CHGSW: OFF when A13 is less than 1mA. Measure V_{SW} .
CHGSW pin input voltage L	
RESET pin input current	Measure A2 current value I_{RE} .
RESET pin input voltage H	$V_{13} = 3.5\text{V}$. Raise V_2 from 0V to 1.2V. Charging control circuit: ON when A13 is more than 500mA. Charging control circuit: OFF when A13 is less than 1mA. Measure V_{RE} .
RESET pin input voltage L	
Current limit 1	$V_{13} = 3.5\text{V}$. T15-T13 potential difference is V_{L1} .
Current limit 2	$V_{13} = 3.5\text{V}$. T15-T13 potential difference is V_{L2} .
Full charge detection	SW24: B, $I_{15} = 100\text{mA}$. Gradually reduce I_{15} current value after reset. T15 - T13 potential difference is V_F when T21 potential goes under 0.5V.
Low voltage detection voltage	Gradually raise V_{13} from 0V. T13 potential is V_{LV} when A13 current value goes over 50mA.
Low voltage detection voltage Hysteresis voltage width	Gradually lower V_{13} from 2.5V. T13 potential is V_{LV2} when A13 current value goes over 10mA. $V_{LVW} = V_{LV} - V_{LV2}$
Pre-charge detection voltage	Gradually raise V_{13} from 2.5V. T13 potential is V_P when A13 current value goes over 500mA.
Pre-charge detection voltage Hysteresis voltage width	Gradually lower V_{13} from 3.5V. T13 potential is V_{P2} when A13 current value goes under 150mA. $V_{PW} = V_P - V_{P2}$
Re-charge detection voltage	Wait about 1S at $V_{13} = 4.2\text{V}$; in full charge detection state, gradually lower V_{13} potential to lower T21 potential to under 0.5V. T13 potential is V_R when T21 potential is more than $V_{CC} - 0.5\text{V}$.
Overvoltage detection voltage	Gradually raise V_{13} from 4V. T13 potential is V_{OV} when T22 potential starts to repeat HI/LOW.
Battery temperature detection voltage H	$V_{13} = 3.5\text{V}$, SW12: B. Gradually raise V_{12} from 0.6V. T12 potential is V_{TH} when A13 current value goes under 1mA.
Battery temperature detection voltage L1	$V_{13} = 3.5\text{V}$, SW12: B. Gradually raise V_{12} from 0V. T12 potential is V_{TL1} when A13 current value goes over 500mA.
Battery temperature detection voltage L2	$V_{13} = 3.5\text{V}$, SW12: B. Gradually raise V_{12} from 0.6V. T12 potential is V_{TL2} when A13 current value goes over 1mA.
TDET input bias current	SW12: B, $V_{12} = 0\text{V}$. Measure A12 current value I_T .
LED R pin output voltage	$V_{13} = 3.5\text{V}$, SW22: B. Gradually raise V_{22} from 0V. T22 potential is V_{LEDR} when A22 current value is 10mA.
LED G pin output voltage	Wait about 1S at $V_{13} = 4.2\text{V}$; in full charge detection state, make T21 potential 0.5V or less. Next at SW21: B, gradually raise V_{21} from 0V. T21 potential is V_{LEDG} when A21 current value is 10mA.

Timing Chart

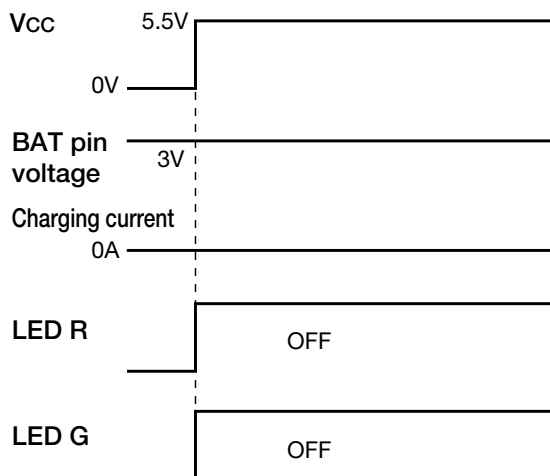
Charging performed normally



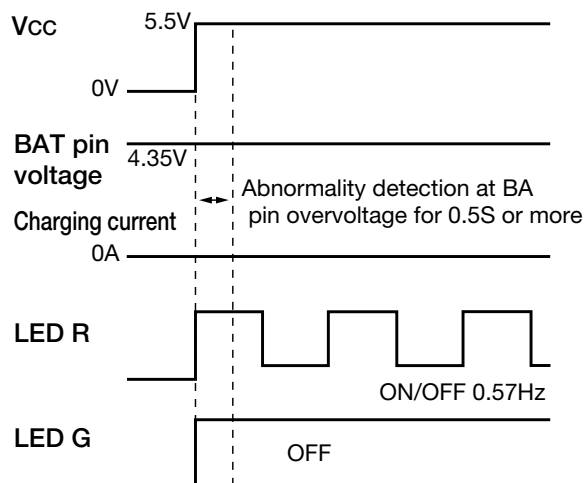
Adapter abnormality



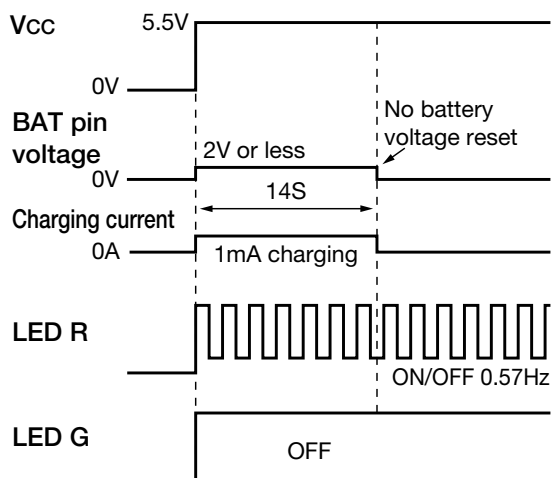
Power supply setting error (temperature detection pin open)



Battery overcharge



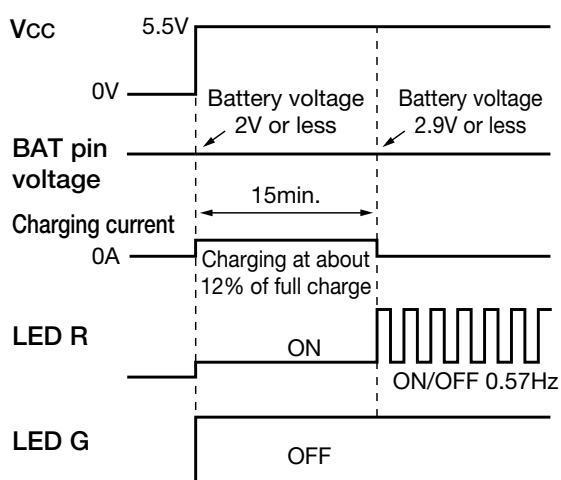
■ Battery overdischarge



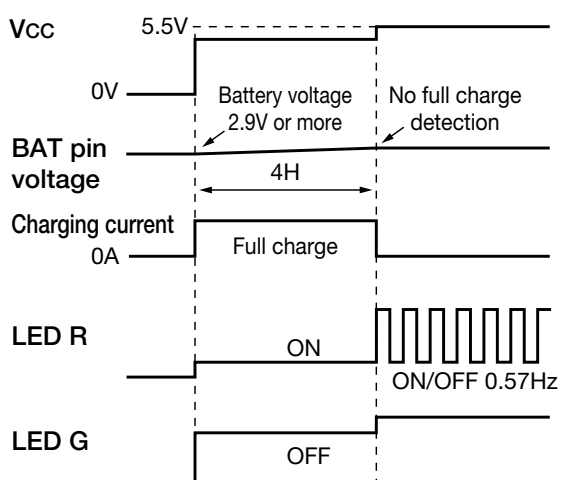
■ Overcurrent detection

Overcurrent detection does not function

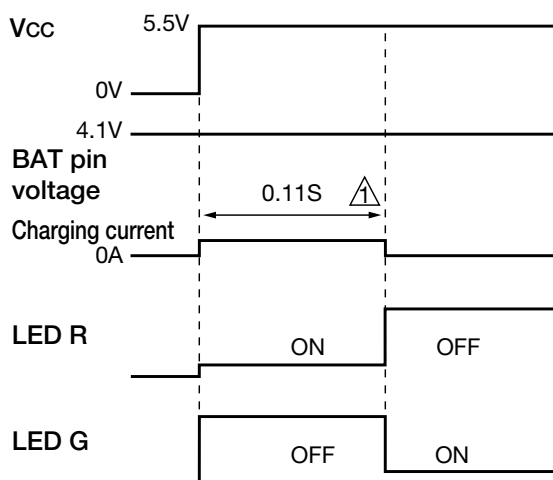
■ Pre-charge time up



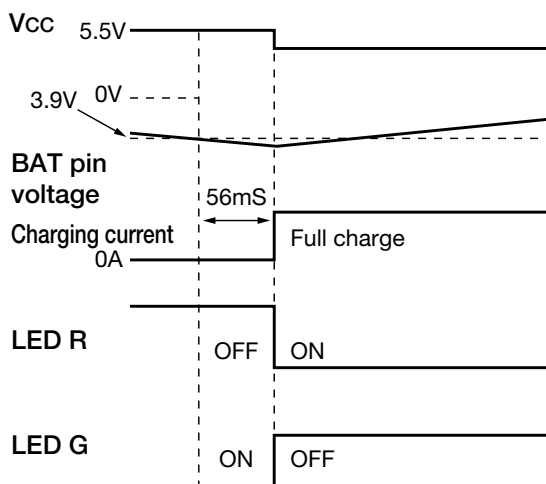
■ Full charge time up



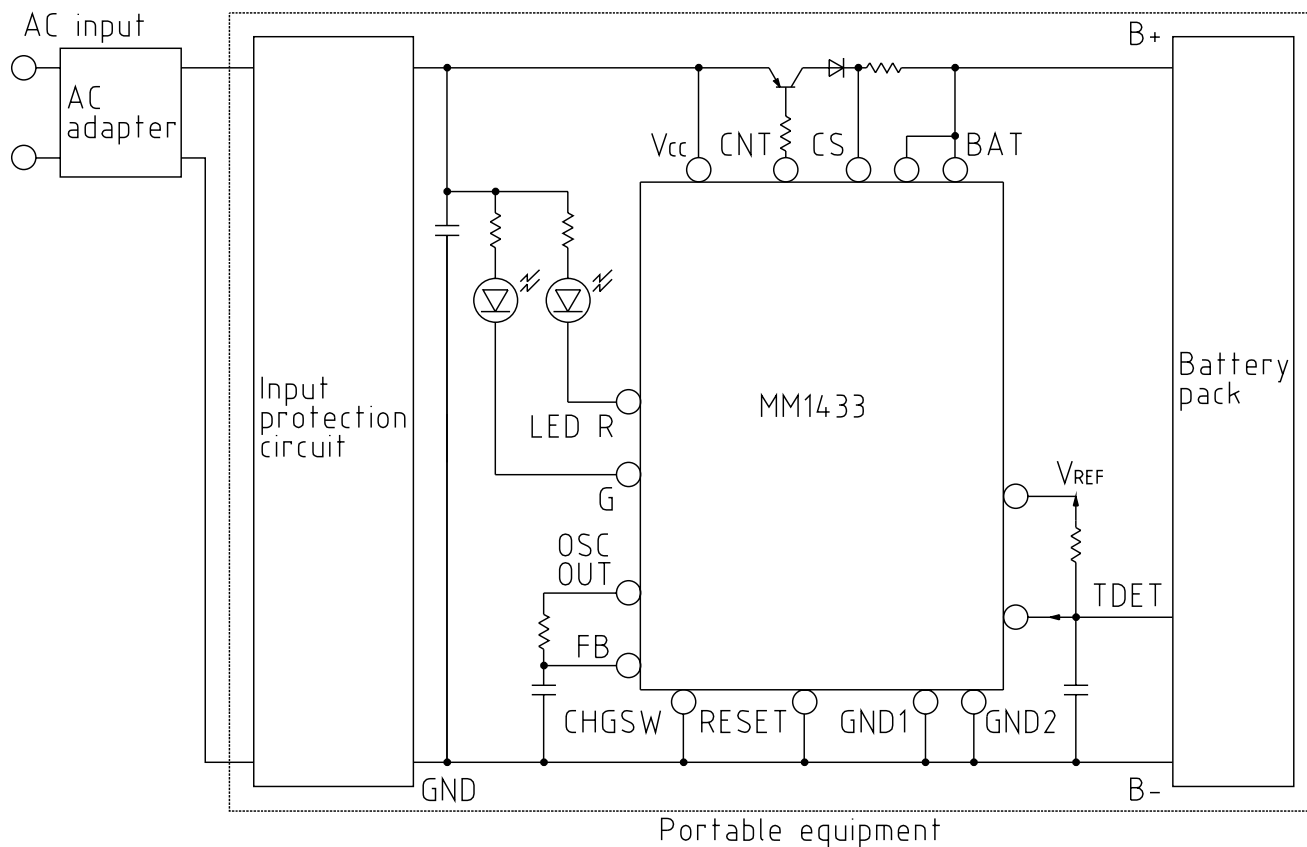
Battery full charge



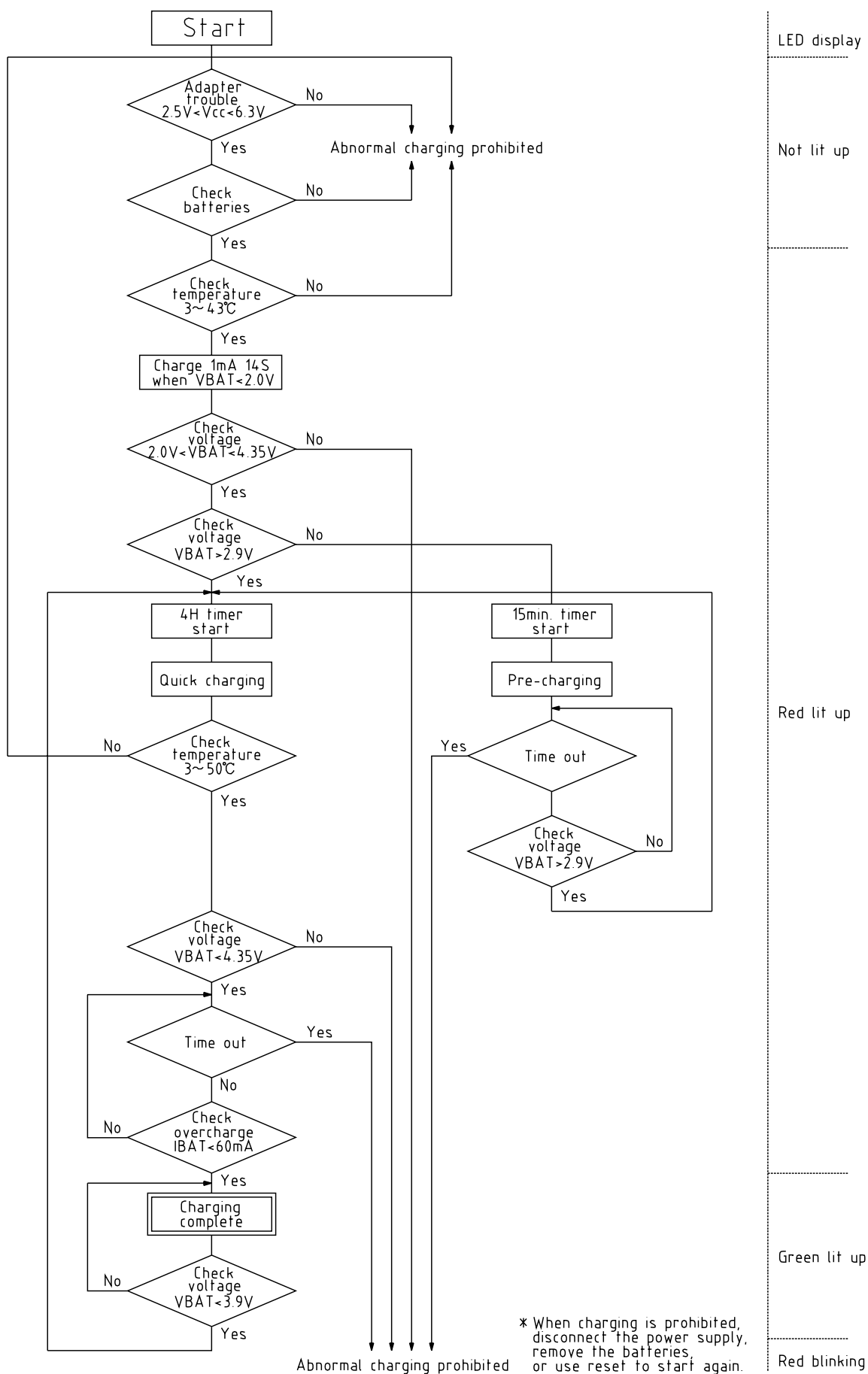
Re-charge detection



Application Circuit

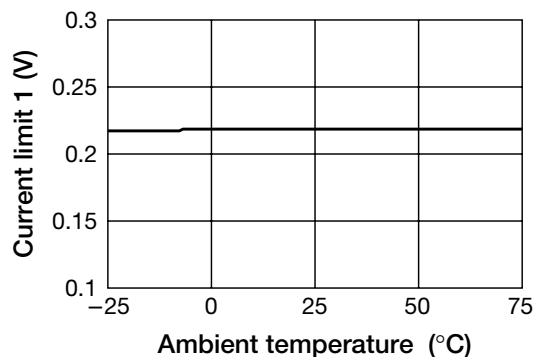


Flow Chart

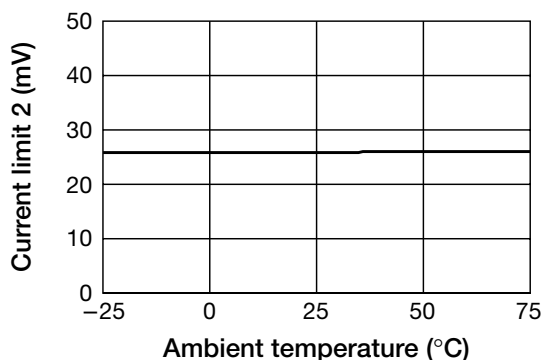


Characteristics

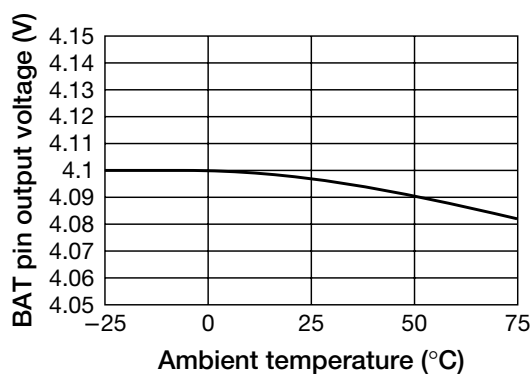
■ Current limit 1 – Temperature



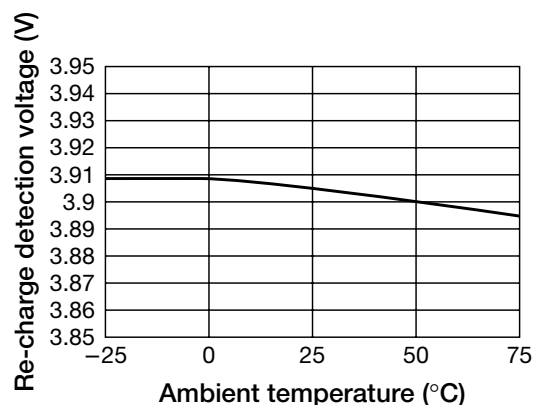
■ Current limit 2 – Temperature



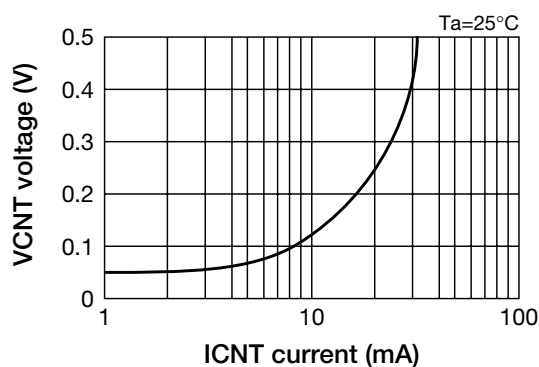
■ BAT pin output voltage – Temperature



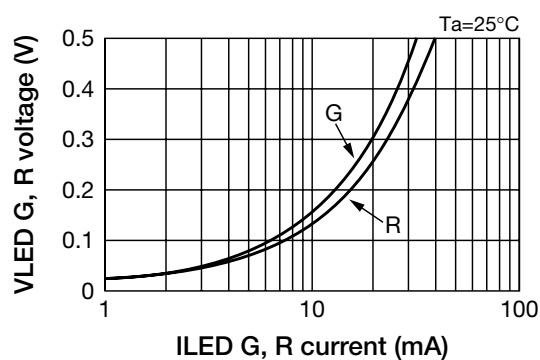
■ Re-charge detection voltage – Temperature



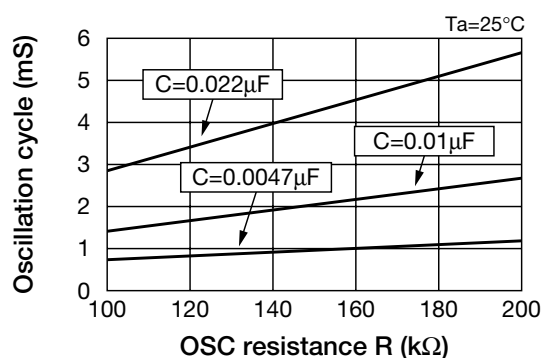
■ VCNT voltage – ICNT current



■ VLED G, R voltage - ILED G, R current



■ OSC oscillation cycle – CR



■ BAT pin reverse current – BAT pin voltage

