

Product Preview
Medium Power Surface Mount Products
**TMOS Dual N-Channel
Field Effect Transistors**



WaveFET™ devices are an advanced series of power MOSFETs which utilize Motorola's latest MOSFET technology process to achieve the lowest possible on-resistance per silicon area. They are capable of withstanding high energy in the avalanche and commutation modes and the drain-to-source diode has a very low reverse recovery time. WaveFET™ devices are designed for use in low voltage, high speed switching applications where power efficiency is important. Typical applications are dc-dc converters, and power management in portable and battery powered products such as computers, printers, cellular and cordless phones. They can also be used for low voltage motor controls in mass storage products such as disk drives and tape drives. The avalanche energy is specified to eliminate the guesswork in designs where inductive loads are switched and offer additional safety margin against unexpected voltage transients.

- Zener Protected Gates Provide Electrostatic Discharge Protection
- Designed to withstand 200 V Machine Model and 2000 V Human Body Model
- Low $R_{DS(on)}$ Provides Higher Efficiency and Extends Battery Life
- Logic Level Gate Drive — Can Be Driven by Logic ICs
- Miniature SO-8 Surface Mount Package — Saves Board Space
- Diode Is Characterized for Use In Bridge Circuits
- Diode Exhibits High Speed, With Soft Recovery
- $Idss$ Specified at Elevated Temperature
- Mounting Information for SO-8 Package Provided

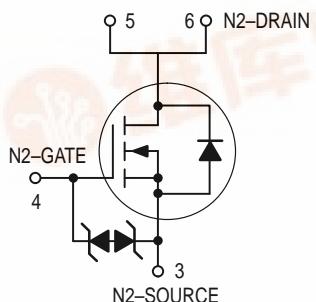
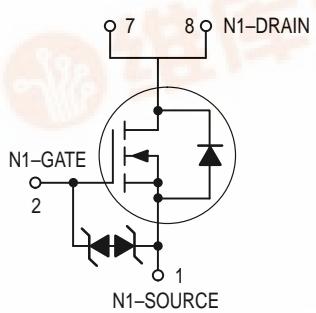
MMDF3200Z

Motorola Preferred Device

**DUAL TMOS
POWER MOSFET
11.5 AMPERES
20 VOLTS
 $R_{DS(on)} = 0.015$ OHM**



CASE 751-06, Style 11
SO-8



N1-Source	1	8	N1-Drain
N1-Gate	2	7	N1-Drain
N2-Source	3	6	N2-Drain
N2-Gate	4	5	N2-Drain

TOP VIEW

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Max	Unit
Drain-to-Source Voltage	V_{DSS}	20	V
Drain-to-Gate Voltage ($R_{GS} = 1.0 \text{ M}\Omega$)	V_{DGR}	20	V
Gate-to-Source Voltage — Continuous	V_{GS}	± 12	V
Operating and Storage Temperature Range	T_J, T_{Stg}	-55 to 150	$^\circ\text{C}$

DEVICE MARKING

ORDERING INFORMATION

Device	Reel Size	Tape Width	Quantity
D3200	13"	12 mm embossed tape	4000 units

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Preferred devices are Motorola recommended choices for future use and best overall value.

MMDF3200Z

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

When mounted on 1 inch square (25.40 mm square) FR-4 or G-10 board ($V_{GS} = 10 \text{ V}$ @ 10 Seconds)

Parameter	Symbol	Maximum	Unit
Drain Current — Continuous @ $T_A = 25^\circ\text{C}$	I_D	11.5	A
— Continuous @ $T_A = 70^\circ\text{C}$	I_D	9.2	A
— Pulsed Drain Current (1)	I_{DM}	57.5	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	2.0	Watts
Linear Derating Factor		16	$\text{mW}/^\circ\text{C}$
Thermal Resistance — Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Continuous Source Current (Diode Current)	I_S	TBD	A

When mounted on 1 inch square (25.40 mm square) FR-4 or G-10 board ($V_{GS} = 10 \text{ V}$ @ Steady State)

Parameter	Symbol	Maximum	Unit
Drain Current — Continuous @ $T_A = 25^\circ\text{C}$	I_D	8.0	A
— Continuous @ $T_A = 70^\circ\text{C}$	I_D	5.9	A
— Pulsed Drain Current (1)	I_{DM}	40	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	1.28	Watts
Linear Derating Factor		10.2	$\text{mW}/^\circ\text{C}$
Thermal Resistance — Junction to Ambient	$R_{\theta JA}$	98	$^\circ\text{C}/\text{W}$
Continuous Source Current (Diode Current)	I_S	TBD	A

When mounted on minimum FR-4 or G-10 board ($V_{GS} = 10 \text{ V}$ @ Steady State)

Parameter	Symbol	Maximum	Unit
Drain Current — Continuous @ $T_A = 25^\circ\text{C}$	I_D	7.1	A
— Continuous @ $T_A = 70^\circ\text{C}$	I_D	5.2	A
— Pulsed Drain Current (1)	I_{DM}	35.5	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	0.75	Watts
Linear Derating Factor		6.0	$\text{mW}/^\circ\text{C}$
Thermal Resistance — Junction to Ambient	$R_{\theta JA}$	166	$^\circ\text{C}/\text{W}$
Continuous Source Current (Diode Current)	I_S	TBD	A

(1) Repetitive rating; pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Drain-to-Source Breakdown Voltage ($V_{GS} = 0 \text{ Vdc}$, $I_D = 0.25 \text{ mA}$) Temperature Coefficient (Positive)	$V_{(BR)DSS}$	20 —	— TBD	— —	Vdc $\text{mV}/^\circ\text{C}$
Zero Gate Voltage Drain Current ($V_{DS} = 20 \text{ Vdc}$, $V_{GS} = 0 \text{ Vdc}$) ($V_{DS} = 20 \text{ Vdc}$, $V_{GS} = 0 \text{ Vdc}$, $T_J = 125^\circ\text{C}$)	I_{DSS}	— —	— —	1.0 10	μA
Gate-Body Leakage Current ($V_{GS} = \pm 12 \text{ Vdc}$, $V_{DS} = 0 \text{ Vdc}$)	I_{GSS}	—	TBD	1.0	μA

ON CHARACTERISTICS⁽¹⁾

Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 0.25 \text{ mA}$) Threshold Temperature Coefficient (Negative)	$V_{GS(\text{th})}$	0.5 —	0.8 TBD	1.2 —	Vdc $\text{mV}/^\circ\text{C}$
Static Drain-to-Source On-Resistance ($V_{GS} = 4.5 \text{ Vdc}$, $I_D = 11.5 \text{ Adc}$) ($V_{GS} = 2.5 \text{ Vdc}$, $I_D = 5.9 \text{ Adc}$)	$R_{DS(\text{on})}$	— —	TBD TBD	15 25	$\text{m}\Omega$
Forward Transconductance ($V_{DS} = 8.0 \text{ Vdc}$, $I_D = 3.0 \text{ Adc}$)	g_{FS}	5.0	TBD	—	Mhos

DYNAMIC CHARACTERISTICS

Input Capacitance	$(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C_{iss}	—	TBD	TBD	pF
Output Capacitance		C_{oss}	—	TBD	TBD	
Transfer Capacitance		C_{rss}	—	TBD	TBD	

SWITCHING CHARACTERISTICS⁽²⁾

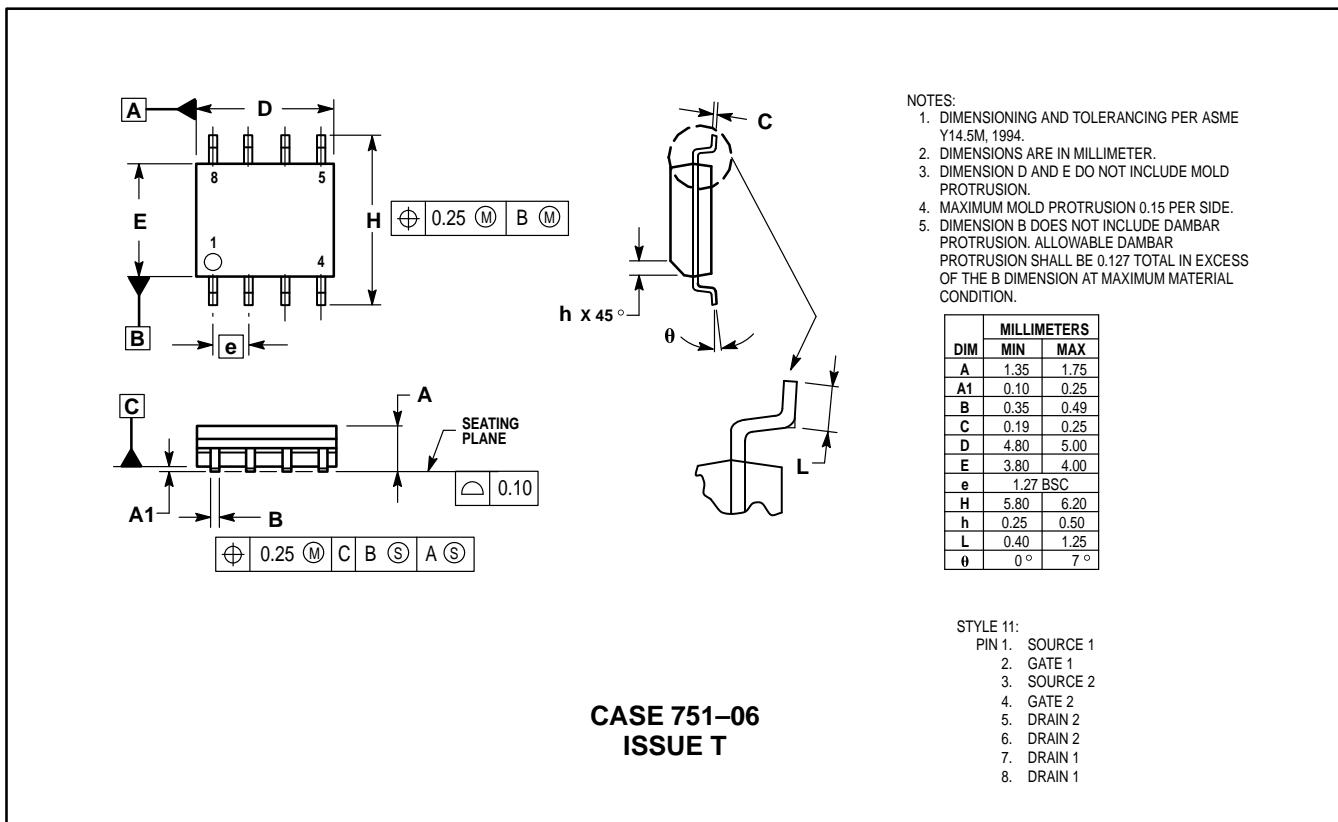
Turn-On Delay Time	$(V_{DD} = 16 \text{ Vdc}, I_D = 11.5 \text{ Adc}, V_{GS} = 4.5 \text{ Vdc}, R_G = 10 \Omega)$	$t_{d(\text{on})}$	—	TBD	TBD	ns
Rise Time		t_r	—	TBD	TBD	
Turn-Off Delay Time		$t_{d(\text{off})}$	—	TBD	TBD	
Fall Time		t_f	—	TBD	TBD	
Gate Charge See Figure 8	$(V_{DS} = 16 \text{ Vdc}, I_D = 11.5 \text{ Adc}, V_{GS} = 4.5 \text{ Vdc})$	Q_T	—	TBD	TBD	nC
		Q_1	—	TBD	—	
		Q_2	—	TBD	—	
		Q_3	—	TBD	—	

SOURCE-DRAIN DIODE CHARACTERISTICS

Forward On-Voltage	$(I_S = 11.5 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 11.5 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^\circ\text{C})$	V_{SD}	— —	TBD TBD	1.2 —	Vdc
Reverse Recovery Time	$(I_S = 11.5 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, dI_S/dt = 100 \text{ A}/\mu\text{s})$	t_{rr}	—	TBD	—	ns
		t_a	—	TBD	—	
		t_b	—	TBD	—	
		Q_{RR}	—	TBD	—	

(1) Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$.

(2) Switching characteristics are independent of operating junction temperature.

MMDF3200Z**PACKAGE DIMENSIONS**

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