

MMPQ6700
SURFACE MOUNT
COMPLEMENTARY
SILICON QUAD TRANSISTORS



CentralTM
Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR MMPQ6700, consisting of two complementary pairs of transistors, available in the SOIC-16 surface mount package, is designed for general purpose amplifier and switching applications.

MAXIMUM RATINGS (T_A=25°C)

	SYMBOL		UNITS
Collector-Base Voltage	V _{CB0}	40	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Continuous Collector Current	I _C	200	mA
Power Dissipation	P _D	1000	mW
Operating and Storage			
Junction Temperature	T _J , T _{stg}	-55 to +150	°C
Thermal Resistance (Total Package)	θ _{JA}	125	°C/W
Thermal Resistance (Each Transistor)	θ _{JA}	240	°C/W

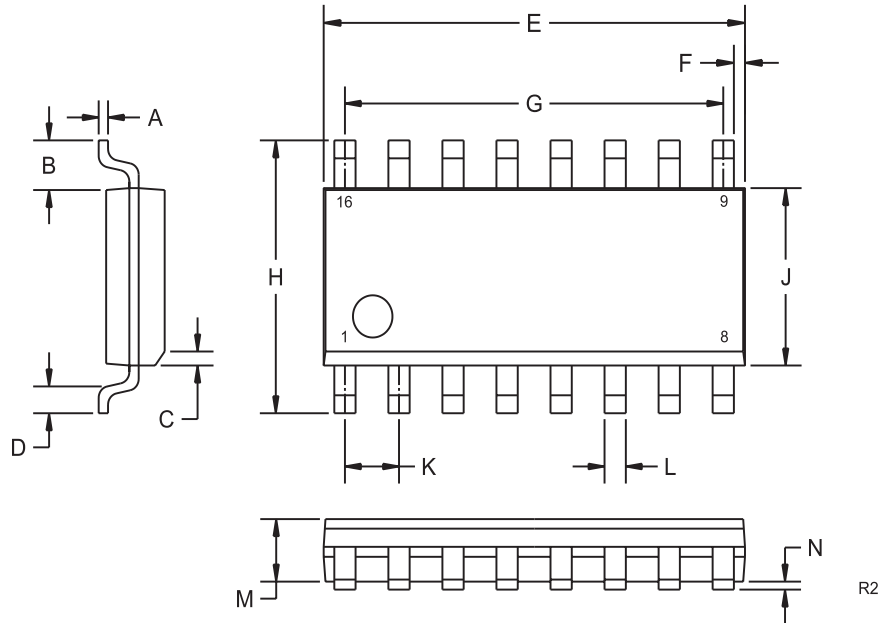
ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{CBO}	V _{CB} =30V			50	nA
I _{EBO}	V _{BE} =4.0V			50	nA
BV _{CEO}	I _C =10mA	40			V
BV _{CBO}	I _C =10μA	40			V
BV _{EBO}	I _E =10μA	5.0			V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA			0.25	V
V _{BE(SAT)}	I _C =10mA, I _B =1.0mA			0.90	V
h _{FE}	V _{CE} =1.0V, I _C =0.1mA	30			
h _{FE}	V _{CE} =1.0V, I _C =1.0mA	50			
h _{FE}	V _{CE} =1.0V, I _C =10mA	70			
f _T	V _{CE} =20V, I _C =10mA, f=100MHz	200			MHz
C _{ib}	V _{EB} =0.5V, f=1.0kHz			10	pF
C _{ob}	V _{CB} =5.0V, I _E =0, f=100kHz			4.5	pF

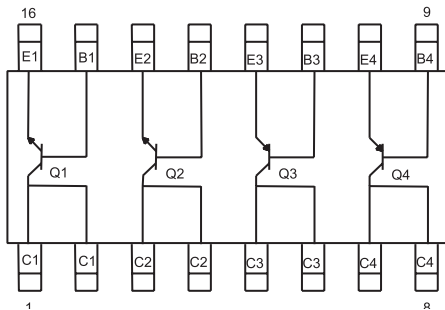
R0 (7-November 2001)

**SMD COMPLEMENTARY
SILICON QUAD
TRANSISTORS**

SOIC-16 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.007	0.010	0.19	0.25
B	0.041		1.04	
C	0.010	0.020	0.25	0.50
D	0.020	0.035	0.50	0.90
E	0.386	0.394	9.80	10.00
F	0.010		0.25	
G	0.350		8.89	
H	0.228	0.244	5.80	6.20
J	0.150	0.157	3.80	4.00
K	0.050		1.27	
L	0.0138	0.0201	0.35	0.51
M	0.0531	0.0689	1.35	1.75
N	0.0039	0.0098	0.10	0.25

SOIC-16 (REV:R2)