

MN3881S

PAL-Compatible CCD Video Signal Delay Element

Overview

The MN3881S is a CCD signal delay element for video signal processing applications.

It contains such components as a shift register clock driver, charge I/O blocks, 1/2nd frequency doubler, two switchable CCD analog shift registers, a clamp bias circuit, resampling output amplifiers, a mode selection circuit and booster circuits.

When the switch pin is grounded, the MN3881S samples the input using the supplied clock signal with a frequency 8.8672375 MHz of twice the PAL color signal subcarrier frequency, and after adding in the attached filter delay, produces independent delays of 1 H (the horizontal scan period for the PAL system) for the Y output and 2 H for the C output.

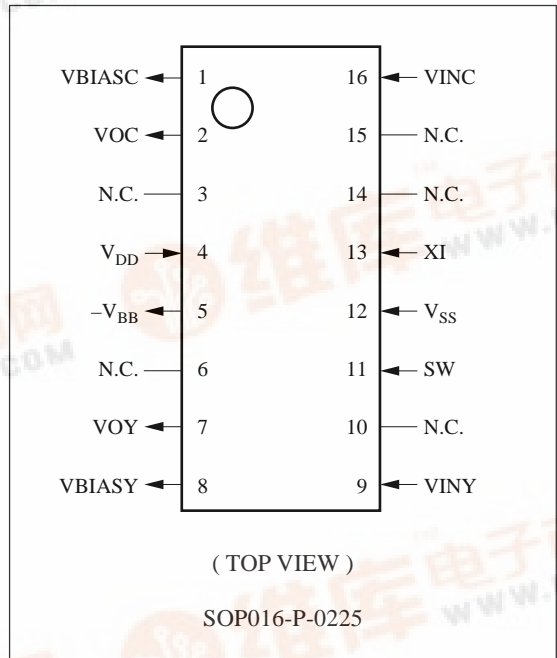
Features

- Single 4.9 V power supply
- Single chip combining luminance signal delay element and delay element for color signal converted to low frequency.

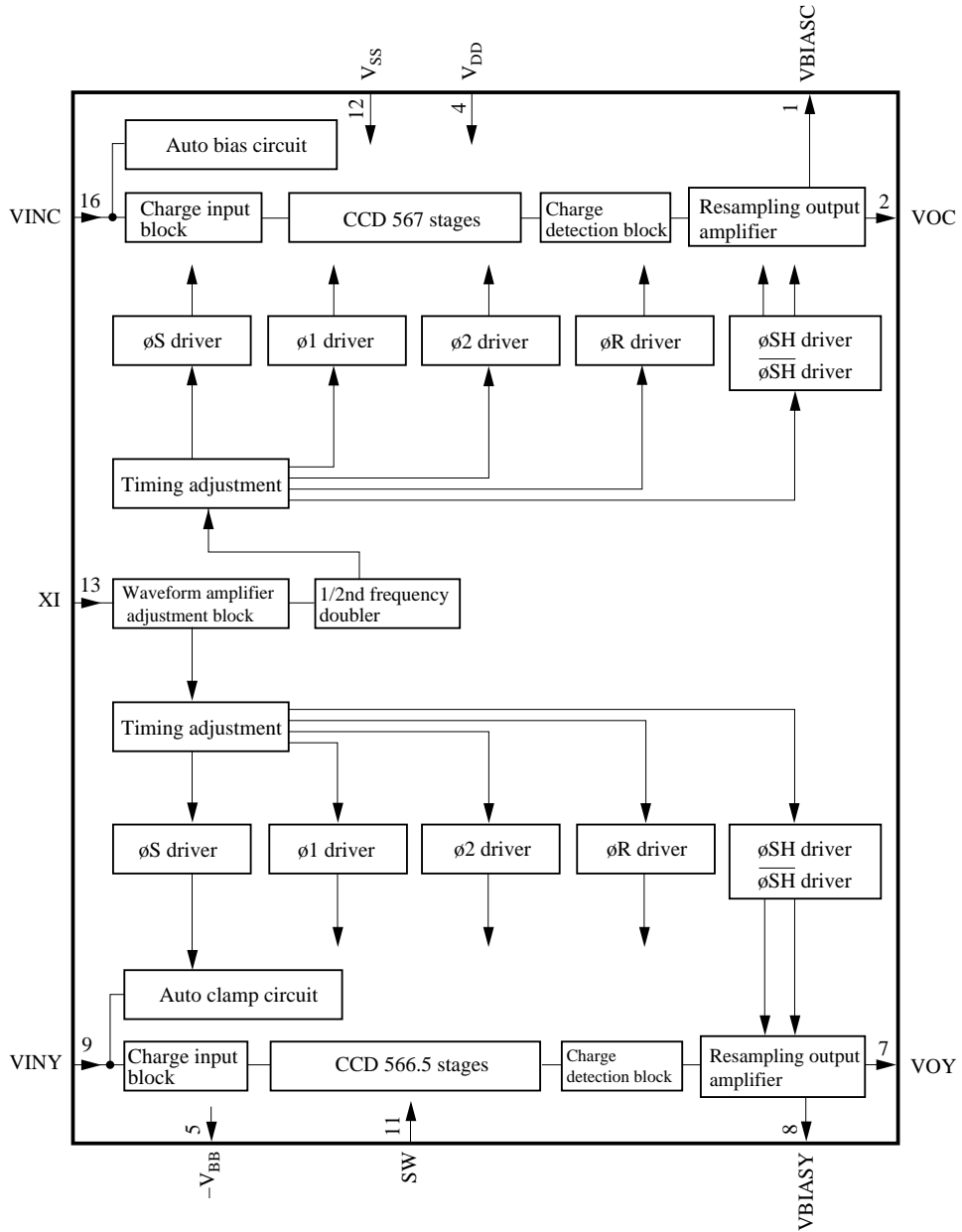
Applications

- VCRs

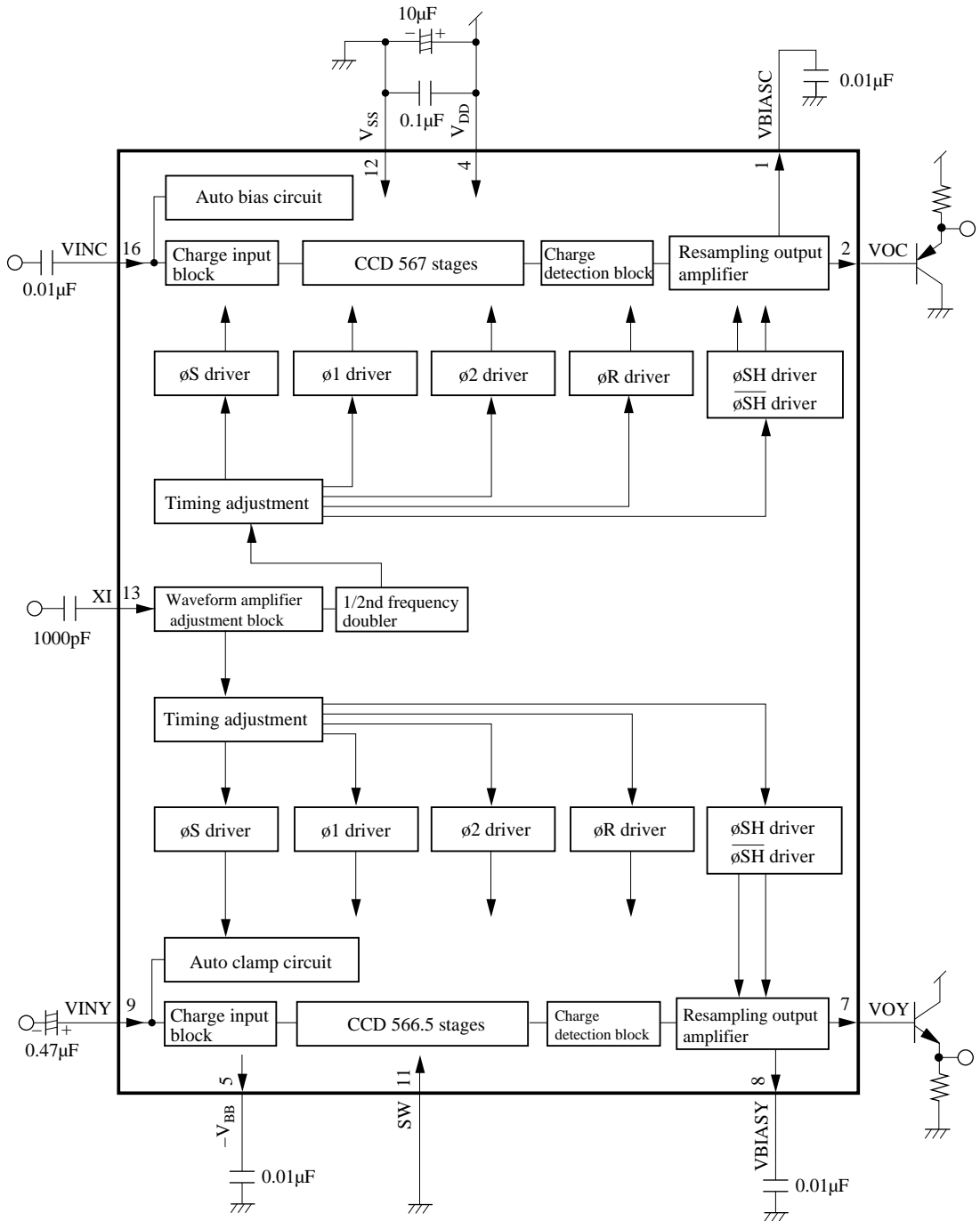
Pin Assignment



■ Block Diagram



■ Application Circuit Example



Note: If the capacitor attached to pin 5 has a polarity, attach the negative pole to pin 5.

■ Package Dimensions (Unit:mm)

SOP016-P-0225

