



**Discrete POWER & Signal
Technologies**

MPS6518



PNP General Purpose Amplifier

This device is designed for use as general purpose amplifiers and switches requiring collector currents to 100 mA. Sourced from Process 66. See 2N3906 for characteristics.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	40	V
V _{EBO}	Emitter-Base Voltage	4.0	V
I _C	Collector Current - Continuous	200	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		MPS6518	
P _D	Total Device Dissipation Derate above 25°C	625	mW
		5.0	mW/°C
R _{θJC}	Thermal Resistance, Junction to Case	83.3	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	200	°C/W

PNP General Purpose Amplifier

(continued)

MPS6518

Electrical Characteristics

TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
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OFF CHARACTERISTICS

$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage*	$I_C = 0.5 \text{ mA}, I_B = 0$	40		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10 \text{ } \mu\text{A}, I_C = 0$	4.0		V
I_{CBO}	Collector Cutoff Current	$V_{CB} = 30 \text{ V}, I_E = 0$ $V_{CB} = 30 \text{ V}, I_E = 0, T_A = 60^\circ\text{C}$	0.5 1.0		μA μA

ON CHARACTERISTICS*

h_{FE}	DC Current Gain	$V_{CE} = 10 \text{ V}, I_C = 2.0 \text{ mA}$ $V_{CE} = 10 \text{ V}, I_C = 100 \text{ mA}$	150 90	300	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		0.5	V

SMALL SIGNAL CHARACTERISTICS

C_{ob}	Output Capacitance	$V_{CB} = 10 \text{ V}, f = 100 \text{ kHz}$		4.0	pF
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* Pulse Test: Pulse Width $\leq 300 \text{ } \mu\text{s}$, Duty Cycle $\leq 2.0\%$