The RF Line NPN Silicon RF Power Transistor

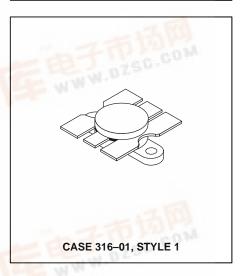
- ... designed primarily for wideband large-signal output and driver amplifier stages in 100 to 500 MHz frequency range.
- Specified 28 Volt, 400 MHz Characteristics —
 Output Power = 30 Watts
 Minimum Gain = 8.5 dB
 Efficiency = 54% (Min)
- Built–In Matching Network for Broadband Operation Using Internal Matching Techniques
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR
- Gold Metallization for High Reliability Applications

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	33	Vdc
Collector–Base Voltage	V _{СВО}	60	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous — Peak	lC	3.4 4.5	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	82 0.47	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

MRF325

30 W, 225 to 400 MHz CONTROLLED "Q" BROADBAND RF POWER TRANSISTOR NPN SILICON



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.13	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Symbol	Min	Тур	Max	Unit
V _(BR) CEO	33	-	布场	Vdc
V(BR)CES	60	E WWW	.075C-1	Vdc
V(BR)EBO	4.0	_	_	Vdc
V(BR)CBO	60	_	_	Vdc
ICBO	_	_	3.0	mAdc
	V(BR)CEO V(BR)CES V(BR)EBO V(BR)CBO	V(BR)CEO 33 V(BR)CES 60 V(BR)EBO 4.0 V(BR)CBO 60	V(BR)CEO 33 — V(BR)CES 60 — V(BR)EBO 4.0 — V(BR)CBO 60 —	V(BR)CEO 33 — — V(BR)CES 60 — — V(BR)EBO 4.0 — — V(BR)CBO 60 — —

18181						_
DC Current Gain	hFE	20	_	80	_	
$(I_C = 1.5 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc})$						

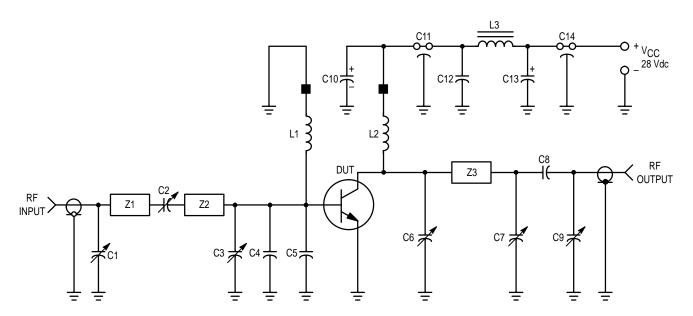
OTE: (continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.



ELECTRICAL CHARACTERISTICS — **continued** (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS	•			•	
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	30	40	pF
FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 30 W, f = 400 MHz)	G _{PE}	8.5	9.5	_	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{Out} = 30 W, f = 400 MHz)	η	50	60	_	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 30 W, f = 400 MHz, VSWR = 30:1 all angles)	Ψ	No Degradation in Output Power			



C1, C9 — 1.0-10 pF Johanson Capacitor (JMC 5201)

C2, C3, C6, C7 — 1.0-20 pF Johanson Capacitor (JMC 5501)

C4, C5 — 36 pF ATC 100-mil Chip Capacitor

C8 — 100 pF UNELCO

C10, C13 $\stackrel{\cdot}{-}$ 1.0 μF 50 V Tantalum C11, C14 $\stackrel{\cdot}{-}$ 680 pF Feedthru

 $C12 - 0.1 \, \mu F$ Erie Redcap

L1 — 8 Turns #26 AWG Enameled, 1/16" ID Closewound with Ferroxcube Bead (#56-590-65/4B) on Ground End L2 — 14 Turns, #22 AWG Enameled, Closewound on a 470 $\Omega,\,$ 2.0 Watt Resistor with Ferroxcube Bead (#56-590-65/4B) on Cold End of L2

L3 — Ferroxcube VK200–19/4B Ferrite Choke

Z1 — Microstrip 0.19" W x 0.88" L

Z2 — Microstrip 0.28" W x 1.0" L

Z3 — Microstrip 0.31" W x 1.25" L

Board — Glass Teflon ε_r = 2.56, t = 0.062"

Input/Output Connectors — Type N

DUT Socket Lead Frame Etched from 80-mil-Thick Copper

Figure 1. 400 MHz Test Circuit

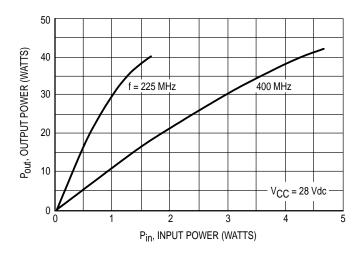


Figure 2. Output Power versus Input Power

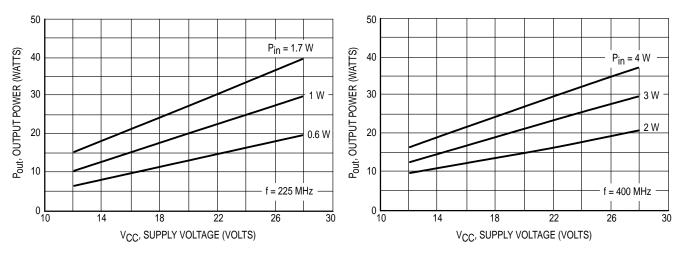
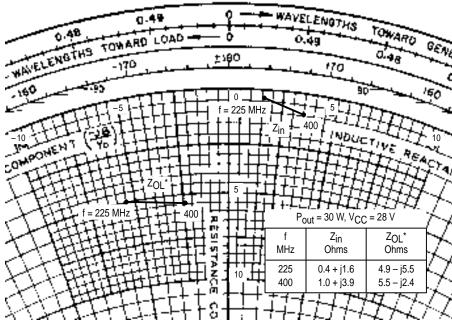


Figure 3. Output Power versus Supply Voltage

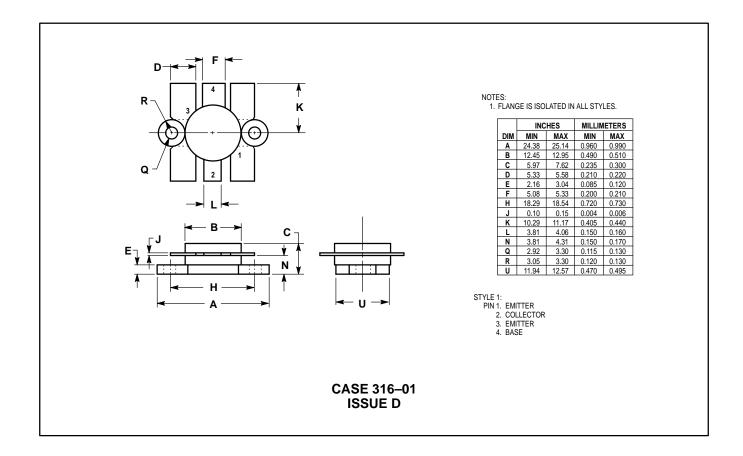
Figure 4. Output Power versus Supply Voltage



 Z_{OL}^* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 5. Series Equivalent Impedance

PACKAGE DIMENSIONS



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