



MOSL VITELIC INC.

Preliminary

MSU2031

October 1996

8 - Bit Microcontroller

Features

- General 8051 family compatible
- 64 K byte External Memory
- 8 bit bus I/O ports
- ROM less for U2031
- 128 byte RAM
- Two 16 bit Timers (Event Counters)
- 128 byte depth stack
- Full duplex serial channel
- Bit operation instructions
- Page free jumps
- 8 - bit Unsigned Division
- 8 - bit Unsigned Multiply
- BCD arithmetic
- Direct Addressing
- Indirect Addressing
- Nested Interrupt
- Two priority level interrupt
- A serial I/O port
- Working at 16/25/40 MHz Clock

Description

The MVI U2031 is an 8 - bit single chip microcontroller. It provides hardware features and powerful instruction set that are necessary to make it a versatile and cost effective controller for those applications need up to 64K byte external memory either for program or for data and mixed.

A serial input / output port is provided for I/O expansion, Inter - processor communications, full duplex UART.

Ordering Information

MSU2031ajks

a:process identifier. {C:=COMS}

jk:working clock in MHz. {16, 25, 40}

s:package type. {blank:=dice, P:=40-L 600 mil PDIP; j:=44-L PLCC}

See 14-page **PID 251***** 10/96** for details which belongs to MSU2051.

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