



SamHop Microelectronics Corp.

SDU/D30N03L

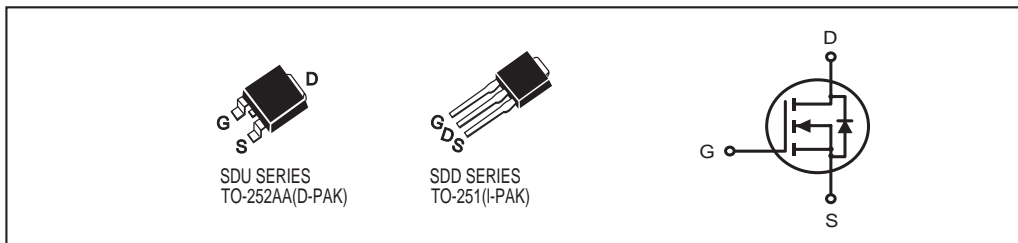
JULY, 2002

N-Channel Logic Level Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{DS(ON)} (mW) TYP
30V	30A	11.5 @ V _{GS} = 10V
		17 @ V _{GS} = 4.5V

FEATURES

- Super high dense cell design for low R_{DS(ON)}.
- Rugged and reliable.
- TO-252 and TO-251 Package.



ABSOLUTE MAXIMUM RATINGS (T_c=25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	V
Drain Current-Continuous @T _J =125°C -Pulsed ^a	I _D	30	A
	I _{DM}	90	A
Drain-Source Diode Forward Current	I _S	30	A
Maximum Power Dissipation @T _c =25°C Derate above 25°C	P _D	50	W
		0.3	W/°C
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 175	°C
THERMAL CHARACTERISTICS			
Thermal Resistance, Junction-to-Case	R _{θJC}	3	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	50	°C/W

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ELECTRICAL CHARACTERISTICS (T_c=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			10	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = +/-20V, V _{DS} = 0V			+/-100	nA
ON CHARACTERISTICS^a						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1	1.5	3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 15A		11.5	14	m ohm
		V _{GS} = 4.5V, I _D = 12A		17	21	m ohm
On-State Drain Current	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	40			A
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D = 20A	30			S
DYNAMIC CHARACTERISTICS^b						
Input Capacitance	C _{ISS}	V _{DD} = 15V, V _{GS} = 0V f = 1.0MHz		1200		pF
Output Capacitance	C _{OSS}			530		pF
Reverse Transfer Capacitance	C _{RSS}			150		pF
SWITCHING CHARACTERISTICS^b						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 15V, I _D = 1A, V _{GS} = 10V, R _{GEN} = 6 ohm		5		ns
Rise Time	t _r			65		ns
Turn-Off Delay Time	t _{D(OFF)}			67		ns
Fall time	t _f			90		ns
Total Gate Charge	Q _g	V _{DS} = 15V, I _D = 15A, V _{GS} = 10V		34.4		nC
Gate-Source Charge	Q _{gs}			5.1		nC
Gate-Drain Charge	Q _{gd}			7.7		nC

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ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS^a						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_s = 25\text{A}$			1.3	V

Notes

a. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

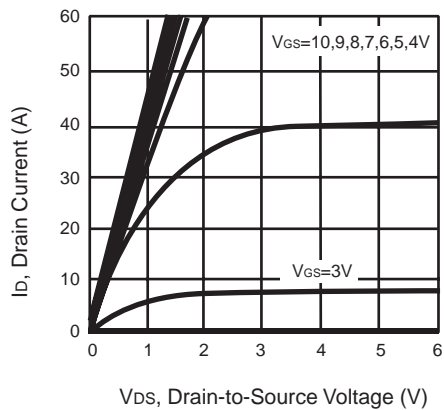


Figure 1. Output Characteristics

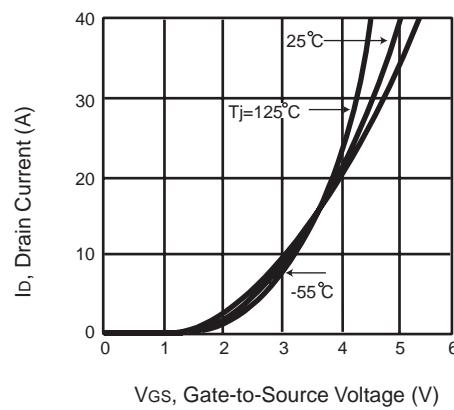


Figure 2. Transfer Characteristics

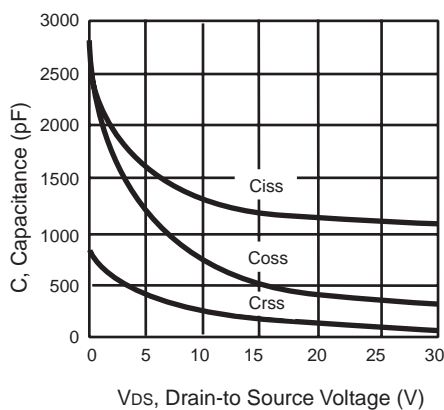


Figure 3. Capacitance

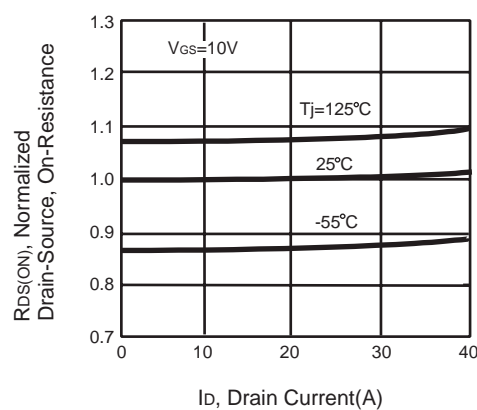


Figure 4. On-Resistance Variation with Drain Current and Temperature

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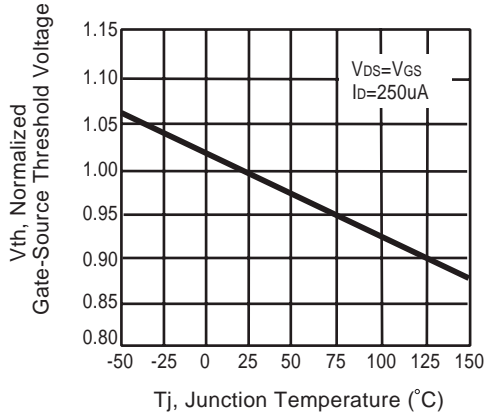


Figure 5. Gate Threshold Variation with Temperature

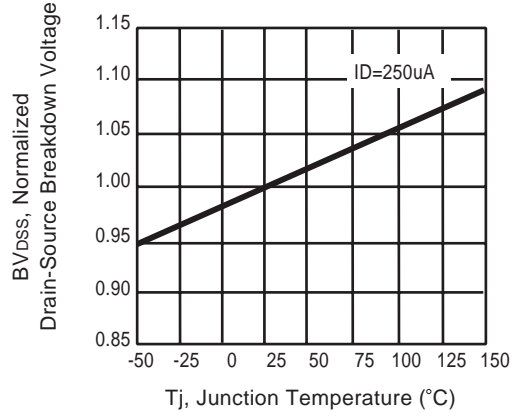


Figure 6. Breakdown Voltage Variation with Temperature

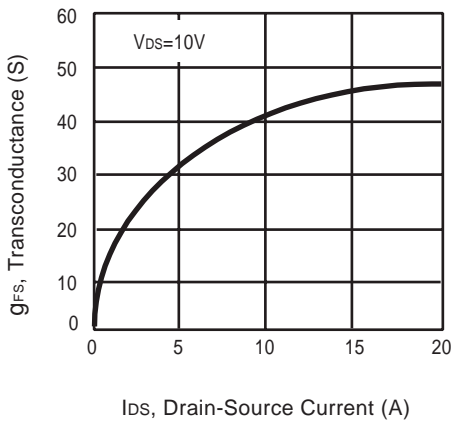


Figure 7. Transconductance Variation with Drain Current

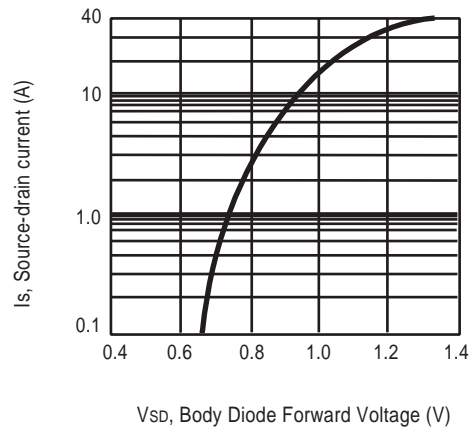


Figure 8. Body Diode Forward Voltage Variation with Source Current

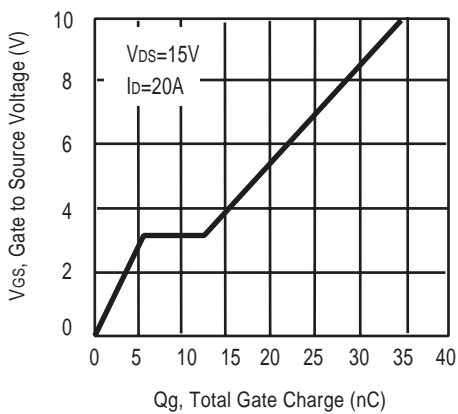


Figure 9. Gate Charge

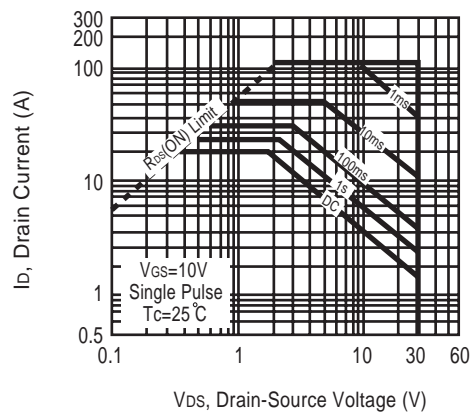


Figure 10. Maximum Safe Operating Area

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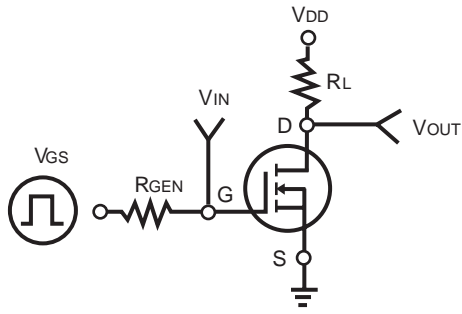


Figure 11. Switching Test Circuit

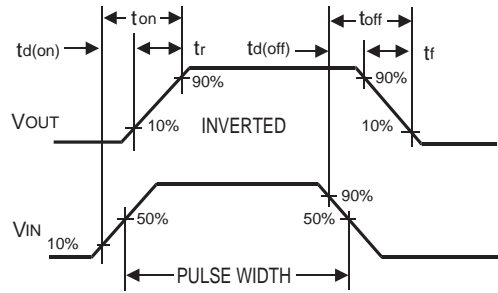


Figure 12. Switching Waveforms

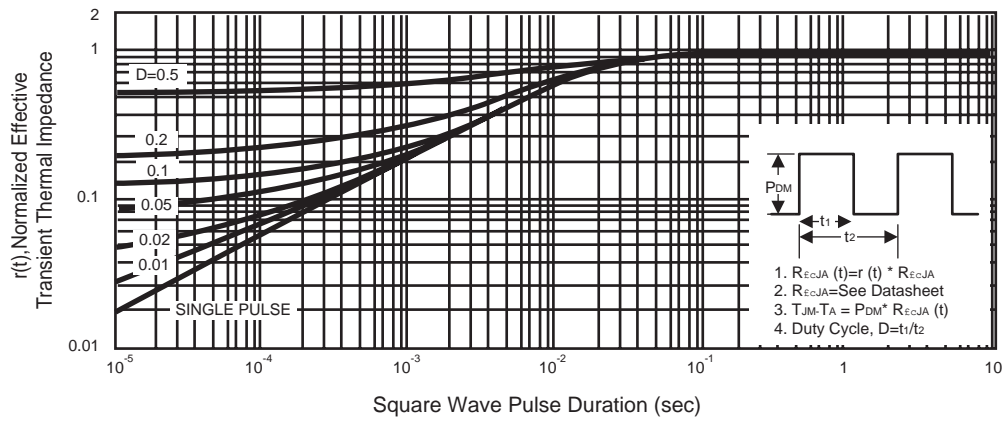


Figure 13. Normalized Thermal Transient Impedance Curve

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