

DIODE ARRAY PRODUCT SPECIFICATION

MONOLITHIC AIR ISOLATED DIODE ARRAY

FEATURES:

- HERMETIC CERAMIC PACKAGE
- $B_v > 60V$ at $10\mu A$
- $I_r < 100nA$ at $40V$
- $C < 8.0$ pF

Absolute Maximum Ratings:

Symbol	Parameter	Limit	Unit
VBR(R) *1 *2	Reverse Breakdown Voltage	60	Vdc
IO *1 *3	Continuous Forward Current	300	mAdc
IFSM *1	Peak Surge Current ($t_p = 1/120$ s)	500	mAdc
PT1 *4	Power Dissipation per Junction @ 25°C	400	mW
PT2 *4	Power Dissipation per Package @ 25°C	600	mW
Top	Operating Junction Temperature Range	-65 to +150	°C
Tstg	Storage Temperature Range	-65 to +200	°C

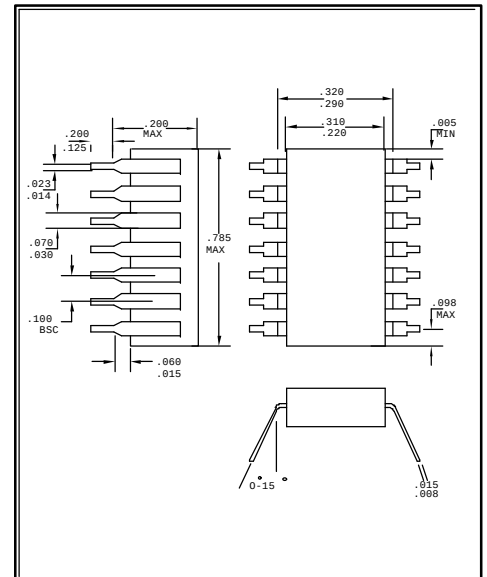
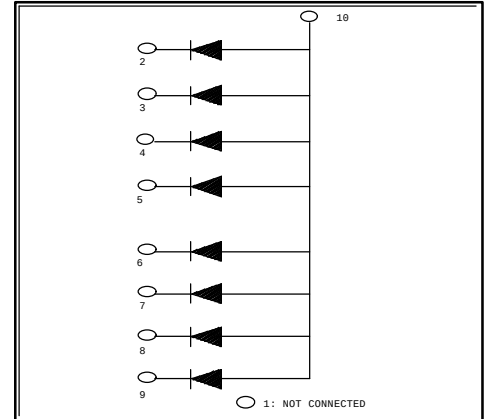
NOTE 1: Each Diode

NOTE 2: Pulsed: $PW = 100ms$ max.; duty cycle $\leq 20\%$

NOTE 3: Derate at $2.4mW/^\circ C$ above $+25^\circ C$

NOTE 4: Derate at $4.0mW/^\circ C$ above $+25^\circ C$

Electrical Characteristics (Per Diode) @ 25°C unless otherwise specified



PACKAGE OUTLINE

Symbol	Parameter	Conditions	Min	Max	Unit
Vf1	Forward Voltage	$I_f = 100mAdc$ *1		1	Vdc
Vf2	Forward Voltage	$I_f = 500mAdc$ *1		1.5	Vdc
IR1	Reverse Current	$V_R = 40$ Vdc		0.1	μAdc
Ct	Capacitance (pin to pin)	$V_R = 0$ Vdc ; $f = 1$ MHz		8.0	pF
tfr	Forward Recovery Time	$I_f = 500mAdc$		40	ns
trr	Reverse Recovery Time	$I_f = I_R = 200mAdc$, $i_{rr} = 20mAdc$, $R_L = 100$ ohms		20	ns

NOTE 1: Pulsed: $PW = 300\mu s \pm 50\mu s$, duty cycle $\leq 2\%$, $90\mu s$ after leading edge

Sertech reserves the right to make changes to any product design, specification or other information at any time without prior notice.

Copyright © Each Manufacturing Company.

All Datasheets cannot be modified without permission.

This datasheet has been download from :

www.AllDataSheet.com

100% Free DataSheet Search Site.

Free Download.

No Register.

Fast Search System.

www.AllDataSheet.com