# HIGH SPEED OPERATIONAL AMPLIFIER WITH SWITCH

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#### GENERAL DESCRIPTION

The NJM2121 is a dual operational amplifier of 2-INPUT and 1-OUTPUT with analog switch. The NJM2121 can be used as analog switch under the condition of Gy=0 dB, as Switch + Amp in order that each gain (A or B) can be adjusted independently. Each amplifier of the NJM2121 has the same electrical characteristics as the NJM4560. The NJM2121 is suit for Audio. Video, Electrical musical instrument...etc.

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7

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#### FEATURES

- Analog Switch Function
- Operating Voltage
- Slew Rate

JRC

- Wide Unity Gain Bandwidth .
- Package Outline .
- Bipolar Technology

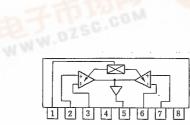
#### PIN CONFIGURATION

2

3

4

NJM21210 NJM2121M  $(\pm 3V \sim \pm 18V)$ (4V/ μs typ.) (14MHz typ.) DIP8, DMP8, SIP8



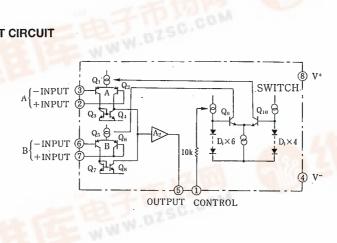
NJM2121L

PIN FUNCTION
1. SW. CONTROL
2. A +INPUT
3. A INPUT
4 V-

NJM2121L

- 5. OUTPUT
- 6. B -INPUT 7. +INPUT
- В 8.









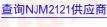
<u>捷多邦,专业PCB打样工厂,24小时加</u>1M2121 急出版











## ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V*/V-	±18(36)	v
Differential Input Voltage	Vid	±30	V
Input Voltage	Vic	±15 (note)	v
Output Current	Io	±50	mA
	Ро	(DIP8) 500	mW
Power Dissipation		(DMP8) 300	mW
		(SIP8) 800	mW
Operating Temperature Range	Topr	-20~+75	Ĉ
Storage Temperature Range	Tstg	-40~+125	C

### ELECTRICAL CHARACTERISTICS

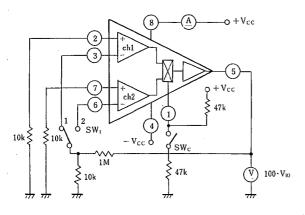
 $(V^{+}/V^{-}=\pm 15V, Ta=25^{\circ}C)$ 

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	Icc	Vin SW ON	-	2.3	6.0	mA
		SW OFF		2.1	6.0	mΑ
Input Offset Voltage	V <sub>IO</sub>	$R_s = 10k\Omega$	—.	0.8	6.0	mV
Input Bias Current	IB		-	0.2	1.0	μA
Lage Signal Voltage Gain	Av	$R_{L} = 2k\Omega$	-	110	-	dB
Maximum Output Voltage Swing	Vом	$R_L \ge 10k\Omega$	±12	±14		v
Total Harmonic Distortion	THD	f=1kHz, Vo=5Vrms, Gv=20dB		0.002	—	%
Supply Voltage Rejection Ratio	SVR			20	150	μV/\
Channel Separation	CS	ſ=1kHz	—	82	—	dB
Unity Gain Bandwidth	ն	Gv=0dB	-	14	-	MHz
Slew Rate	SR	$Gv=0dB, R_L=2k\Omega//100pF$	-	4		V/µs
Equivalent Input Noise Voltage	V <sub>NI</sub>	Rs=1kΩ, BW=10Hz~30kHz, Flat	-	2.0	-	μVrn

NJM2121

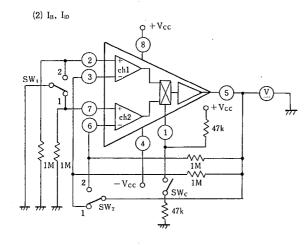
#### TEST CIRCUIT

(1)  $I_{CC}$ ,  $V_{10}$ , SVR



	SWc	SW1	Select ch
$I_{cc1}$ , $V_{101}$ , $SVR_1$	OFF	1	ch 1
$I_{CC2}$ , $V_{102}$ , $SVR_2$	ON	2	ch 2

Unit Resistance :  $\Omega$ Capacity : F



$I_{B}^{+} = V_{0}^{+}/11$	MΩ
$I_{B}^{-} = V_{0}^{-}/11$	MΩ
$I_{10} =  I_B^+ - I_B^+ $	в-

οVo

SW,

1

2

 $SW_2$ 

1

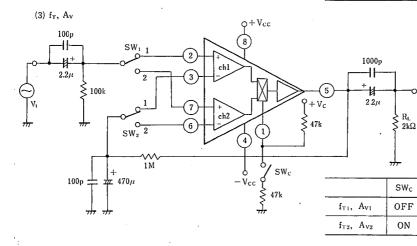
2

Select ch

ch 1

ch 2

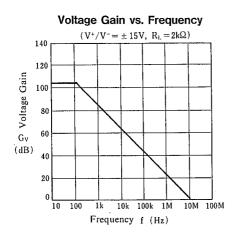
	SWc	SW1	SW2	Select ch
Voi	OFF	1	1	ch 1
Voi	OFF	2	2	ch l
V <sub>02</sub>	ON	2	2	ch 2
, V <sub>02</sub>	ON	1	1	ch 2



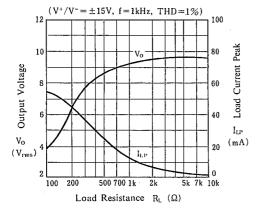
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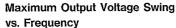
# NJM2121

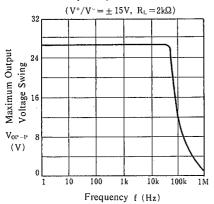
#### **TYPICAL CHARACTERISTICS**

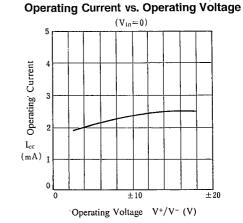


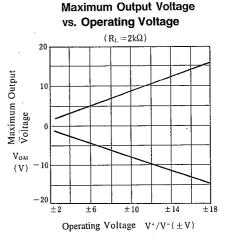
Output Voltage, Load Current Peak vs. Load Resistance



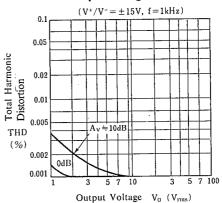






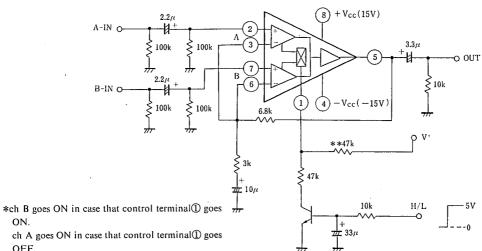


Total Harmonic Distortion vs.Output Voltage



#### APPLICATION CIRCUIT

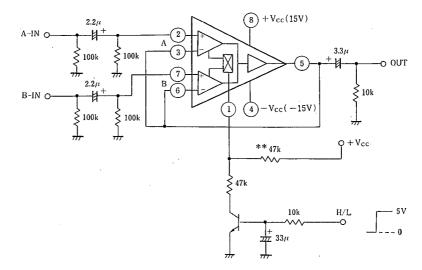
(1) Gv=10dB FLAT Amp+ Analog Switch Circuit



ON.

OFF.

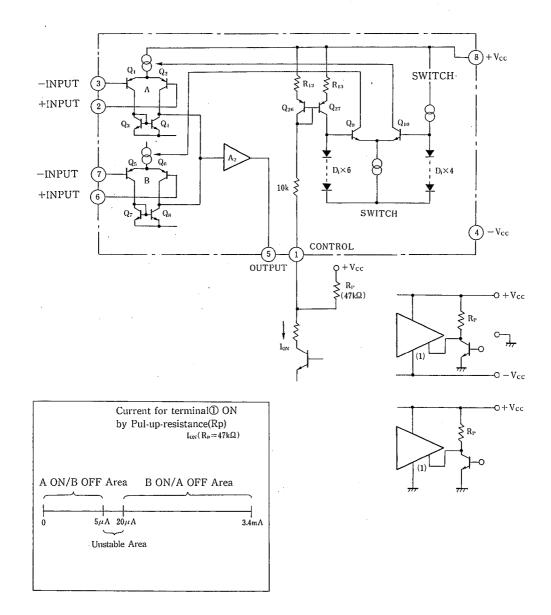
+(2) Analog Switch Circuit (Gv=0dB Voltage Follower Amp)



\*Resistance(\*\*) is Pull-up resistance for pervent from switching terminal going ON by reakage of external circuit(TR...etc).

# NJM2121

#### SWITCHING MECHANISM



Switching Mechanism of NJM2121 is as follows.

Switch signal is communicated in case that  $V_F$  of Q26 goes ON on current mirror which is composed with Q26 and Q27. Q10 goes ON by 4 diodes of Q10 in case that terminal<sup>①</sup> goes OFF and Amp(ch A) goes active. Q9 goes ON by 6 diodes of Q9 in case that terminal<sup>①</sup> goes ON and Amp(ch B) goes active. So, NJM2121 have merit that drive system is controlled freely. Because drive system is not related to supply voltage system(Single supply type/Two supply type) in order that switch change by current ON/OFF.

But, this switch goes On by very little current because of signal communicate system which depend on ON of  $V_F$ . So, please use NJM2121 under the condition of lowerring sensitivity for current ON/OFF by external Pull-up-resistance(Rp)

MEMO

[CAUTION] The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.

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