



NJW1137



PRELIMINARY

AUDIO PROCESSOR with SOUND ENHANCEMENT

■ GENERAL DESCRIPTION

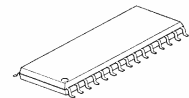
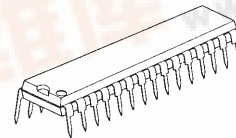
The **NJW1137** is an audio processor with sound enhancement (BBE). It includes all of functions processing audio signal for TV, such as tone control, balance, volume, mute, NJRC original surround, and AGC functions.

The NJRC original surround system reproduces natural surround sound and clear vocal orientation.

The sound enhancement regenerates high definitive and nearly real sound.

All of internal status and variables are controlled by I²C BUS interface.

■ PACKAGE OUTLINE



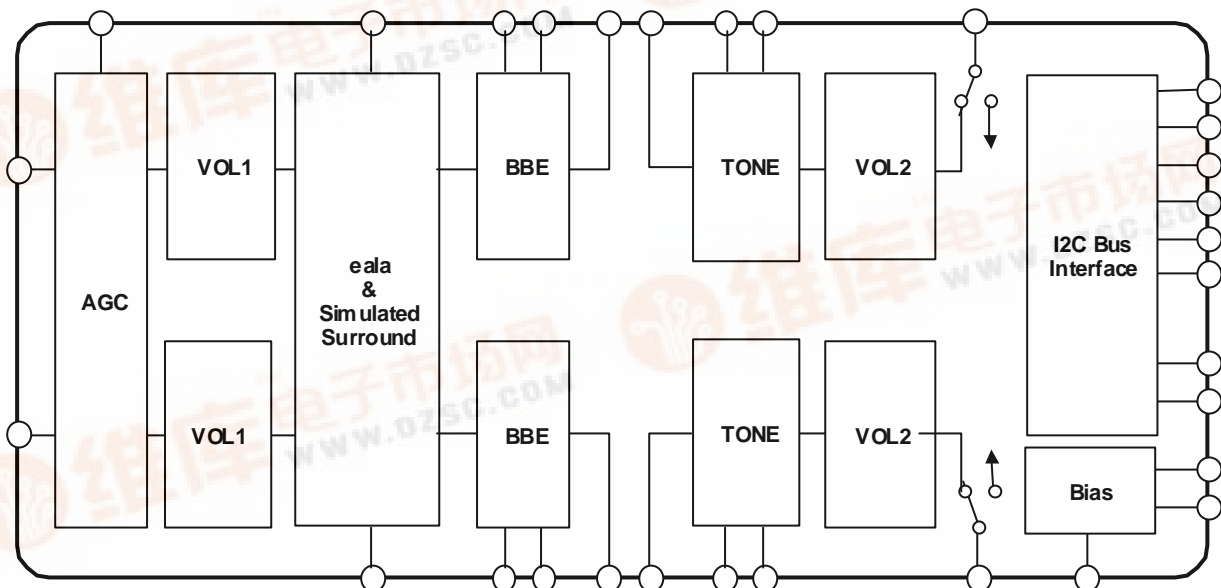
NJW1137L

NJW1137M

■ FEATURES

- Operating Voltage 8 to 13V
- I²C BUS Interface
- BBE Sound Enhancement (Low Boost and High Boost: 15dB max.)
- AGC Circuit (It reduces volume difference among input sources.)
- eala(NJRC Original Surround)
- Simulated Surround
- Bi-CMOS Technology
- Package Outline SDIP30, SDMP30

■ BLOCK DIAGRAM



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■ PIN FUNCTION

1	INa	INb	30
2	BBE1a	BBE1b	29
3	BBE2a	BBE2b	28
4	BBEOUTa	BBEOUTb	27
5	TONE INa	TONE INb	26
6	TONE-Ha	TONE-Hb	25
7	TONE-La	TONE-Lb	24
8	OUTa	OUTb	23
9	AGC	SR-FIL	22
10	SS-FIL	VREF	21
11	CVB	CTH	20
12	CVA	CTL	19
13	SDA	AUX0	18
14	SCL	AUX1	17
15	GND	Vcc	16

No.	Symbol	Function	No	Symbol	Function
1	INa	Ach Input	16	Vcc	Power Supply Pin
2	BBE1a	Ach BBE Filter1 (Process)	17	AUX1	Auxiliary Output1
3	BBE2a	Ach BBE Filter2 (Lo Contour)	18	AUX0	Auxiliary Output0
4	BBE OUTa	Ach Output for the Other Accessories	19	CTL	DAC Output for Tone Low Frequency
5	TONE INa	Ach Input From the Other Accessories	20	CTH	DAC Output for Tone High Frequency
6	TONE-Ha	Ach Treble Filter	21	VREF	Reference Voltage
7	TONE-La	Ach Bass Filter	22	SR-FIL	NJRC Original Surround Filter
8	OUTa	Ach Output	23	OUTb	Bch Output
9	AGC	AGC Filter	24	TONE-Lb	Bch Bass Filter
10	SS-FIL	Simulated Stereo Filter	25	TONE-Hb	Bch Treble Filter
11	CVB	DAC Output for Bch Volume & Balance	26	TONE INb	Bch Input from the Other Accessories
12	CVA	DAC Output for Ach Volume & Balance	27	BBE OUTb	Bch Output for the Other Accessories
13	SDA	SDA Data Input (I ² C BUS)	28	BBE2b	Bch BBE Filter2 (Process)
14	SCL	SCL Data Input (I ² C BUS)	29	BBE1b	Bch BBE Filter1 (Lo Contour)
15	GND	GND	30	INb	Bch Input

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V ⁺	14	V
Power Dissipation	P _D	700	mW
Operating Temperature Range	T _{opr}	-20 to +75	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

■ ELECTRICAL CHARACTERISTICS

(Ta=25°C, V₊=9V, R_g=600Ω, R_L=47kΩ, V_{in}=100mVrms/1kHz unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V ⁺		8.0	9.0	13.0	V
Supply Current	I _{CC}	No Signal	-	13	25	mA
Reference Voltage	V _{REF}	No Signal	4.0	4.5	5.0	V
Maximum Input Voltage	V _{IM}	VOL=-20dB, THD=1%	2.8	3.0	-	Vrms
Maximum Output Voltage	V _{OM}	OUTPUT VOL=0dB, THD=1%	-	2.5	-	Vrms
Channel Balance	G _{CB}	VOL=0dB	-1.5	0.0	1.5	dB
Balance Boost A	BA _{BST}	CHS="0", BAL="11111"	-2.0	0.0	2.0	dB
Balance Cut A	BA _{CUT}	CHS="1", BAL="11111" V _{in} = 1Vrms	-	-	-70	dB
Balance Boost B	BB _{BST}	CHS="1", BAL="11111"	-2.0	0.0	2.0	dB
Balance Cut B	BB _{CUT}	CHS="0", BAL="11111" V _{in} = 1Vrms	-	-	-70	dB
Total Harmonic Distortion	THD	V _o =0.5Vrms BW=400Hz to 30kHz	-	-	0.5	%
Maximum Gain	G _{VMAX}	VOL= 0dB	-2.0	0.0	2.0	dB
Minimum Gain	G _{VMIN}	VOL= MUTE	-	-	-70	dB
Channel Separation	CS	V _{in} = 2Vrms	-	-	-70	dB
Output Noise 1	V _{NO1}	VOL = 0dB BW=400Hz to 30kHz	-	-90 (31.6)	-85 (56.2)	dBV (μVrms)
Output Noise 2	V _{NO2}	VOL = MUTE BW = 400Hz to 30kHz	-	-106 (5.0)	-96 (15.8)	dBV (μVrms)
AUX Output Voltage	V _{AUX}	Logic Output : High	4.5	-	5.0	V
		Logic Output : Low	0	-	0.3	

BW : Band Width

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◆ TONE CONTROL

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
High Frequency Boost	HF _{BST}	BCT="1", TREB="1111", f=10kHz	12.5	15.0	17.5	dB
High Frequency Flat	HF _{FLT}	TRBE="0000", f=10kHz	-2.0	0.0	2.0	dB
High Frequency Cut	HF _{CUT}	BCT="0", TRBE="1111", f=10kHz	-17.5	-15.0	-12.5	dB
Low Frequency Boost	LF _{BST}	BCB="1", BASS="1111", f=100Hz	12.5	15.0	17.5	dB
Low Frequency Flat	LF _{FLT}	BASS="0000", f=100Hz	-2.0	0.0	2.0	dB
Low Frequency Cut	LF _{CUT}	BCB="0", BASS="1111", f=100Hz	-17.5	-15.0	-12.5	dB

◆ SUB-TONE CONTROL

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
High Frequency Boost	SHF _{BST}	BCST="1", SUB-TREB="11", f=10kHz	2.0	3.0	4.0	dB
High Frequency Cut	SHF _{CUT}	BCST="0", SUB-TREB="11", f=10kHz	-4.0	-3.0	-2.0	dB
Low Frequency Boost	SLF _{BST}	BCSB="1", SUB-BASS="11", f=100Hz	2.0	3.0	4.0	dB
Low Frequency Cut	SLF _{CUT}	BCSB="0", SUB-BASS="11", f=100Hz	-4.0	-3.0	-2.0	dB

◆ AGC CONTROL(AGC-ON)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
AGC Boost	AGC _{BST}	Vin=50mVrms, f=1kHz	1.5	3.5	5.5	dB
AGC Flat1	AGC _{FLT1}	Vin=150mVrms, f=1kHz	-2.5	0.0	2.5	dB
AGC Flat2	AGC _{FLT2}	Vin=300mVrms, f=1kHz	-2.5	0.0	2.5	dB
AGC Flat3	AGC _{FLT3}	Vin=400mVrms, f=1kHz	-2.5	0.0	2.5	dB
AGC Flat4	AGC _{FLT4}	Vin=540mVrms, f=1kHz	-2.5	0.0	2.5	dB
AGC Cut	AGC _{CUT}	Vin=2Vrms, f=1kHz	-14	-10	-6.0	dB

◆ BBE(BBE-ON)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
BBE Low Frequency Boost Range	BBE _{LOW}	BBE-Low="1111", f = 50Hz	-	15.0	-	dB
BBE High Frequency Boost Range	BBE _{HIGH}	BBE-High="1111", f = 10KHz	-	15.0	-	dB

◆ **SURROUND (SURROUND-ON)**

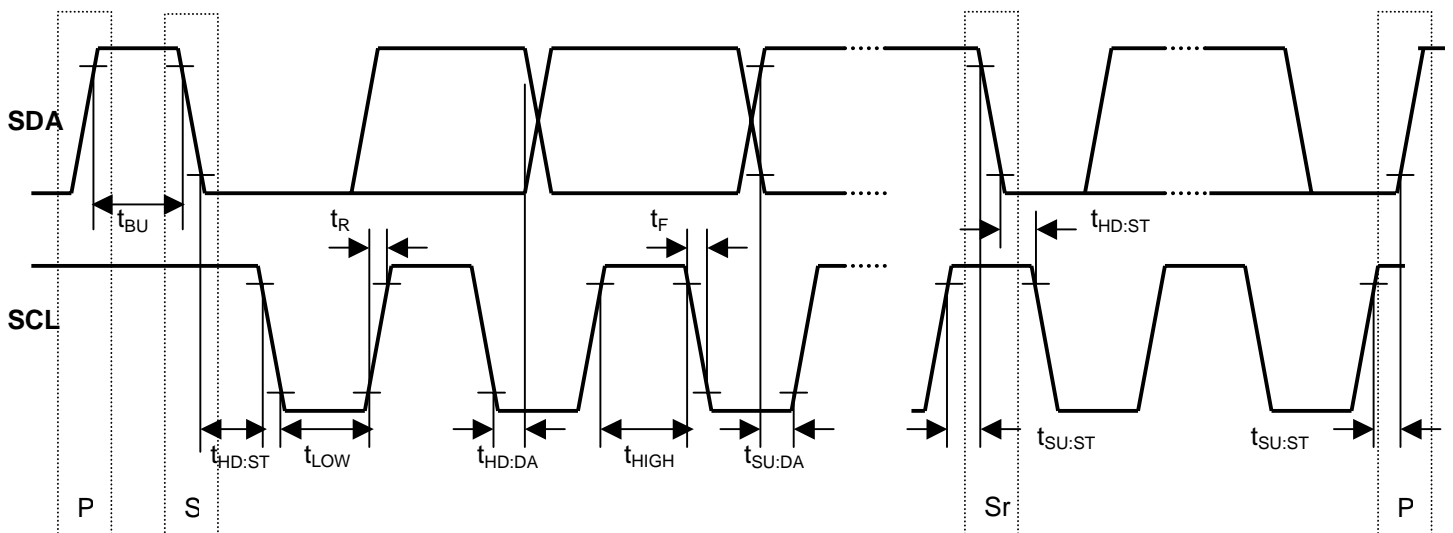
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Surround Gain1	SR _{GAIN1}	Ain→Aout, f=100Hz SUR0="0", SUR="1"	6.0	8.0	10.0	dB
Surround Gain2	SR _{GAIN2}	Ain→Bout, f=100Hz SUR0="0", SUR="1"	1.6	3.6	5.6	dB
Surround Gain3	SR _{GAIN3}	Ain→Aout, f=100Hz SUR0="1", SUR="1"	10.7	12.7	14.7	dB
Surround Gain4	SR _{GAIN 4}	Ain→Bout, f=100Hz SUR0="1", SUR="1"	8.4	10.4	12.4	dB
Simulated Surround Gain1	SR _{SIM1}	Ain+Bin→Aout, f=1kHz SUR0="1", SUR="0"	1.0	3.0	5.0	dB
Simulated Surround Gain2	SR _{SIM2}	Ain+Bin→Bout, f=1kHz SUR0="1", SUR="0"	1.0	3.0	5.0	dB

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■ I²C BUS BLOCK CHARACTERISTICS (SDA,SCL)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
High Level Input Voltage	V _{IH}	3.0	-	5.0	V
Low Level Input Voltage	V _{IL}	0	-	1.5	V
High Level Input Current	I _{IH}	-	-	10	μA
Low Level Input Current	I _{IL}	-	-	10	μA
Low Level Output Voltage (3mA at SDA pin)	V _{OL}	0	-	0.4	V
Maximum Output Current	I _{OL}	-3.0	-	-	mA
Maximum Clock Frequency	f _{SCL}	0	-	100	kHz
Data Change Minimum Waiting Time	t _{BUF}	4.7	-	-	μs
Data Transfer Start Minimum Waiting Time	t _{HD:STA}	4.0	-	-	μs
Low Level Clock Pulse Width	t _{LOW}	4.7	-	-	μs
High Level Clock Pulse Width	T _{HIGH}	4.0	-	-	μs
Minimum Start Preparation Waiting Time	t _{SU:STA}	4.7	-	-	μs
Minimum Data Hold Time	t _{HD:DAT}	5.0	-	-	μs
Minimum Data Preparation Time	t _{SU:DAT}	250	-	-	ns
Rise Time	t _R	-	-	1.0	μs
Fall Time	t _F	-	-	300	ns
Minimum Stop Preparation Waiting Time	t _{SU:STO}	4.7	-	-	μs

I²C BUS Load Condition: Pull up resistance 4kΩ (Connected to +5V)
Load capacitance 200pF (Connected to GND)



■TERMINAL DESCRIPTION

No.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
1 30	INa INb	Ach Input terminal Bch Input terminal		V+/2
4 8 23 27	BBE OUTa OUTa OUTb BBE OUTb	Ach Output for the Other Accessories Ach Output Bch Output Bch Output for the Other Accessories		V+/2
10	SS-FIL	Simulated Stereo Filter terminal		V+/2
2 3 28 29	BBE1a BBE2a BBE2b BBE1b	Ach BBE Filter1 (Process) Ach BBE Filter2 (Lo Contour) Bch BBE Filter2 (Lo Contour) Bch BBE Filter1 (Process)		V+/2
6 25	TONE-Ha TONE-Hb	Treble(tone control) filter terminal		V+/2

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■TERMINAL DESCRIPTION

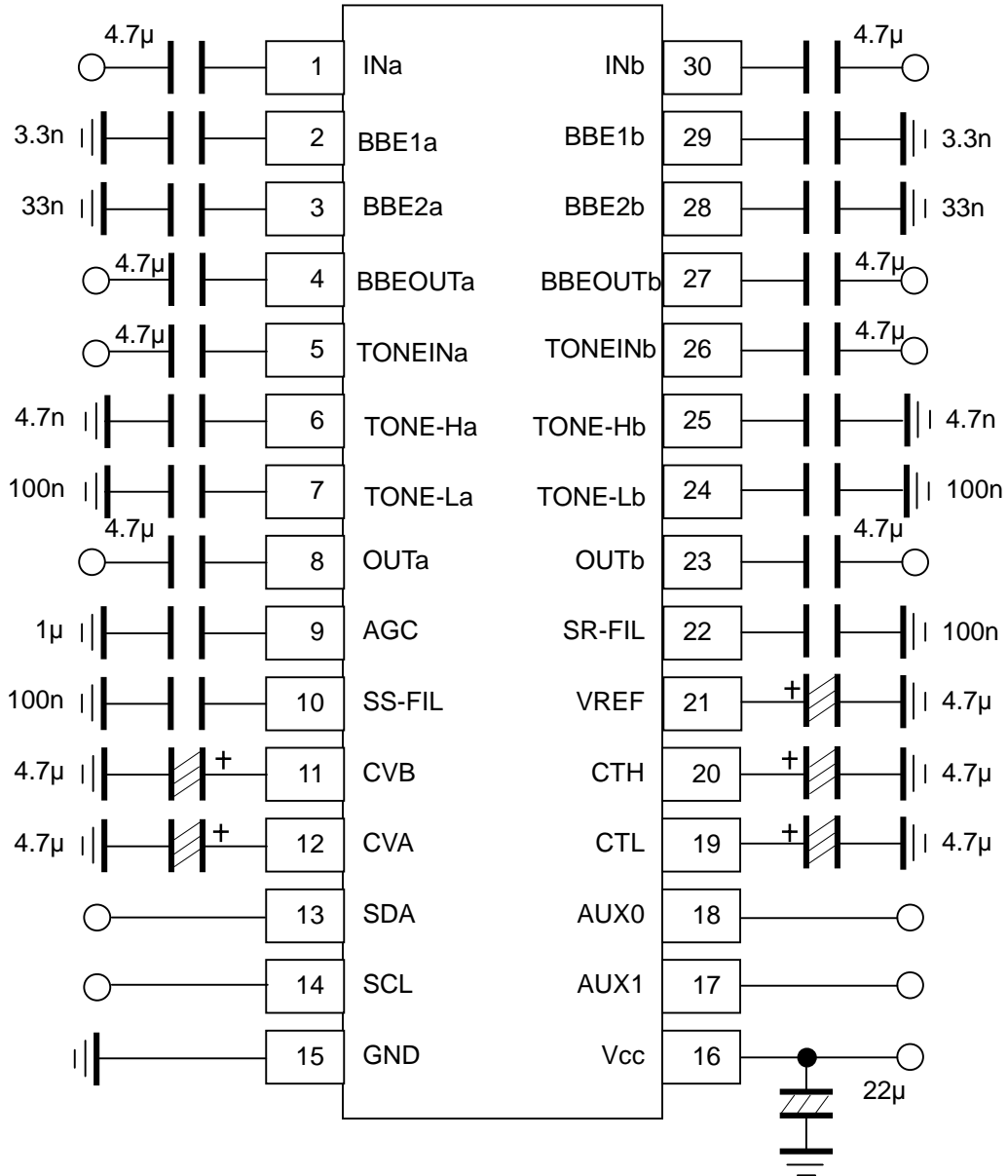
No.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
7 24	TONE-La TONE-Lb	Bass(tone control) filter terminal		V+/2
9	AGC	Capacitor connection terminal for AGC attack and recovery time setting		-
11 12	CVB CVA	DAC output terminal for Bch volume control DAC output terminal for Ach volume control		-
13	SDA	I ² C data terminal		-
14	SCL	I ² C clock terminal		-

■ TERMINAL DESCRIPTION

No.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
5 26	TONE INa TONE INb	Ach Input from the Other Accessories Bch Input from the Other Accessories		V+/2
21	Vref	Reference voltage terminal		V+/2
17 18	AUX0 AUX1	Auxiliary 2 values voltage output terminal		0/5V
20 19	CTH CTL	DAC output for tone control terminal		-
22	SR-FIL	eala filter terminal		V+/2
15 16	V+ GND	Ground terminal Supply voltage terminal	-	-

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APPLICATION CIRCUIT



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■INSTRUCTION CODE

a) MASTER VOLUME SETTING

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
00H	VOL							

The volume control for both Ach and Bch(0.33dB/step).

The volume is consisted of volume1 and volume2 and the level is divided into half to each volume1 and volume2.

b) BALANCE, AGC AND SURROUND SETTING

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
01H	CHS	BAL					AGC	SUR

- CHS : Channel select for balance control

“0” : Ach “Bch is attenuated”

“1” : Bch “Ach is attenuated”

- BAL : Balance control for both Ach and Bch(1dB/Step)

The balance is consisted of volume1 and volume2 and the level is divided into half to each volume1 and volume2.

- AGC : AGC switch

“0” : AGC OFF

“1” : AGC ON (Default Setting : 150mVrms)

- SUR : Surround mode switch

“0” : Surround OFF

“1” : Surround ON(Default Setting : Surround Effect1)

c)TONE CONTROL BASS SETTING

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
02H	BCB	BASS				BCSB	SUB-BASS	

- BCB : Boost cut select for Bass control

“0” : Cut

“1” : Boost

- BASS: BASS control

Cut Level : -15dB to 0dB(1dB/Step)

Boost Level : 0dB to +15dB(1dB/Step)

- BCSB : Boost cut select for SUB-BASS control

“0” : Cut

“1” : Boost

- SUB-BASS: SUB- BASS control(1dB/Step)

Sub-Cut Level : -3dB to 0dB(1dB/Step)

Sub-Boost Level : 0dB to +3dB(1dB/Step)

d) TONE CONTROL TREBLE SETTING

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
03H	BCT	TREB				BCST	SUB-TREB	

- BCT : Boost cut select for Treble control
 "0" : Cut
 "1" : Boost
- TREB: Treble control(1dB/step)
 Cut Level : -15dB to 0dB(1dB/Step)
 Boost Level : 0dB to +15dB(1dB/Step)
- BCST : Boost cut select for Sub-Treble control
 "0" : Cut
 "1" : Boost
- SUB-TREB: Sub-Treble control(1dB/step)
 Sub-Cut Level : -3dB to 0dB(1dB/Step)
 Sub-Boost Level : 0dB to +3dB(1dB/Step)

e) BBE Boost Level Setting

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
04H	BBE(Lo Contour)				BBE(Process)			

- BBE Lo Contour : 0dB to 15dB(1dB/step)
- BBE Process : 0dB to 15dB (1dB/step)

When all bits are "0"(=00H), BBE becomes off

f) OUTPUT AND AUXILIARY SETTING

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
05H	OUT	Don't Care	Don't Care	SUR0	AGC1	AGC0	AUX1	AUX0

- OUT: ON/OFF Switch for OUTPUT
 "0" : OFF
 "1" : ON

•Surround Setting

Surround Function	SUR0(05H:D4)	SUR(01H:D0),	Remarks
Surround OFF	0	0	Surround OFF
Surround Effect1	0	1	Surround Effect Small (8.0dB typ.)
Surround Effect2	1	1	Surround Effect Large (12.7dB typ.)
Simulated Stereo	1	0	Simulated Stereo

•AGC Level Setting

AGC Level	AGC1(D3)	AGC0(D2)
150mVrms	0	0
300mVrms	0	1
400mVrms	1	0
540mVrms	1	1

•AUX1/AUX0: Auxiliary port High/Low

- "0" : Logic output "Low"
- "1" : Logic output "High"

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■MASTER VOLUME (Select Address : 00H)

		VOL							
Gain(dB)	HEX	D7	D6	D5	D4	D3	D2	D1	D0
0	FF	1	1	1	1	1	1	1	1
-1	FC	1	1	1	1	1	1	0	0
-2	F9	1	1	1	1	1	0	0	1
-3	F6	1	1	1	1	0	1	1	0
-4	F3	1	1	1	1	0	0	1	1
-5	F0	1	1	1	1	0	0	0	0
-6	ED	1	1	1	0	1	1	0	1
-7	EA	1	1	1	0	1	0	1	0
-8	E7	1	1	1	0	0	1	1	1
-9	E4	1	1	1	0	0	1	0	0
-10	E1	1	1	1	0	0	0	0	1
-11	DE	1	1	0	1	1	1	1	0
-12	DB	1	1	0	1	1	0	1	1
-13	D8	1	1	0	1	1	0	0	0
-14	D5	1	1	0	1	0	1	0	1
-15	D2	1	1	0	1	0	0	1	0
-16	CF	1	1	0	0	1	1	1	1
-17	CC	1	1	0	0	1	1	0	0
-18	C9	1	1	0	0	1	0	0	1
-19	C6	1	1	0	0	0	1	1	0
-20	C3	1	1	0	0	0	0	1	1
-21	C0	1	1	0	0	0	0	0	0
-22	BD	1	0	1	1	1	1	0	1
-23	BA	1	0	1	1	1	0	1	0
-24	B7	1	0	1	1	0	1	1	1
-25	B4	1	0	1	1	0	1	0	0
-26	B1	1	0	1	1	0	0	0	1
-27	AE	1	0	1	0	1	1	1	0
-28	AB	1	0	1	0	1	0	1	1
-29	A8	1	0	1	0	1	0	0	0
-30	A5	1	0	1	0	0	1	0	1
-31	A2	1	0	1	0	0	0	1	0
-32	9F	1	0	0	1	1	1	1	1
-33	9C	1	0	0	1	1	1	0	0
-34	99	1	0	0	1	1	0	0	1
-35	96	1	0	0	1	0	1	1	0
-36	93	1	0	0	1	0	0	1	1
-37	90	1	0	0	1	0	0	0	0
-38	8D	1	0	0	0	1	1	0	1
-39	8A	1	0	0	0	1	0	1	0
-40	87	1	0	0	0	0	1	1	1
-41	84	1	0	0	0	0	1	0	0
-42	81	1	0	0	0	0	0	0	1

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■BALANCE(Select Address : 01H)

Channel Select (CHS)	D7
Ach(Bch is attenuated)	0
Bch(Ach is attenuated)	1

Gain(dB)	BAL				
	D6	D5	D4	D3	D2
0	0	0	0	0	0
-1	0	0	0	0	1
-2	0	0	0	1	0
-3	0	0	0	1	1
-4	0	0	1	0	0
-5	0	0	1	0	1
-6	0	0	1	1	0
-7	0	0	1	1	1
-8	0	1	0	0	0
-9	0	1	0	0	1
-10	0	1	0	1	0
-11	0	1	0	1	1
-12	0	1	1	0	0
-13	0	1	1	0	1
-14	0	1	1	1	0
-15	0	1	1	1	1
-16	1	0	0	0	0
-17	1	0	0	0	1
-18	1	0	0	1	0
-19	1	0	0	1	1
-20	1	0	1	0	0
-21	1	0	1	0	1
-22	1	0	1	1	0
-23	1	0	1	1	1
-24	1	1	0	0	0
-25	1	1	0	0	1
-26	1	1	0	1	0
-27	1	1	0	1	1
-28	1	1	1	0	0
-29	1	1	1	0	1
-30	1	1	1	1	0
Mute	1	1	1	1	1

■TONE CONTROL BASS (Select Address : 02H)

Bass Cut or Boost	BCB
	D7
Cut	0
Boost	1

		BASS			
		D6	D5	D4	D3
Cut Gain(dB)	Boost Gain(dB)				
-15	15	1	1	1	1
-14	14	1	1	1	0
-13	13	1	1	0	1
-12	12	1	1	0	0
-11	11	1	0	1	1
-10	10	1	0	1	0
-9	9	1	0	0	1
-8	8	1	0	0	0
-7	7	0	1	1	1
-6	6	0	1	1	0
-5	5	0	1	0	1
-4	4	0	1	0	0
-3	3	0	0	1	1
-2	2	0	0	1	0
-1	1	0	0	0	1
0	0	0	0	0	0

■TONE CONTROL SUB-BASS (Select Address : 02H)

Sub-Bass Cut or Boost	BCSB
	D2
Cut	0
Boost	1

		SUB-BASS	
		D1	D0
Cut Gain(dB)	Boost Gain(dB)		
-3	3	1	1
-2	2	1	0
-1	1	0	1
0	0	0	0

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■TONE CONTROL TREBLE (Select Address : 03H)

Treble Cut or Boost	BCT
	D7
Cut	0
Boost	1

		TREB			
Cut Gain(dB)	Boost Gain(dB)	D6	D5	D4	D3
-15	15	1	1	1	1
-14	14	1	1	1	0
-13	13	1	1	0	1
-12	12	1	1	0	0
-11	11	1	0	1	1
-10	10	1	0	1	0
-9	9	1	0	0	1
-8	8	1	0	0	0
-7	7	0	1	1	1
-6	6	0	1	1	0
-5	5	0	1	0	1
-4	4	0	1	0	0
-3	3	0	0	1	1
-2	2	0	0	1	0
-1	1	0	0	0	1
0	0	0	0	0	0

■TONE CONTROL SUB-TREBLE (Select Address : 03H)

Sub-Treble Cut or Boost	BCST
	D2
Cut	0
Boost	1

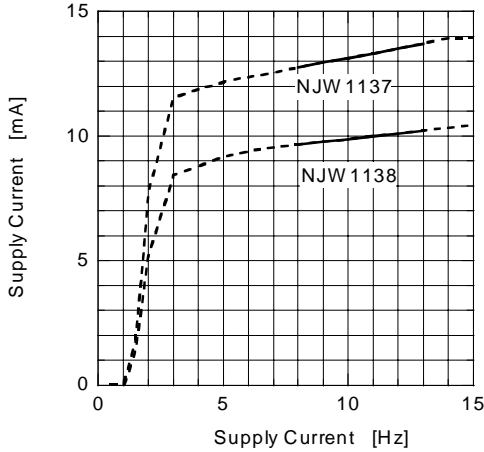
		SUB-TREB	
Cut Gain(dB)	Boost Gain(dB)	D1	D1
-3	3	1	1
-2	2	1	0
-1	1	0	1
0	0	0	0

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■TYPICAL CHARACTERISTICS

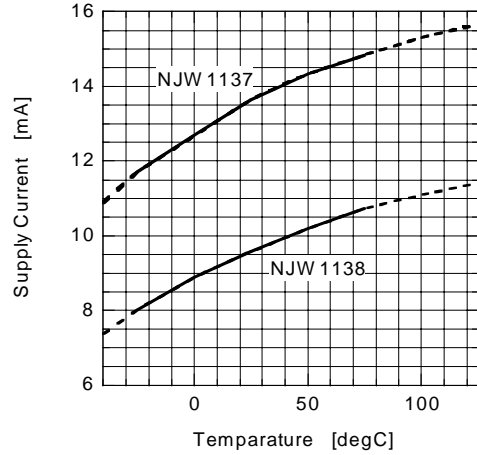
Supply Current vs Supply Voltage

Ta=25degC



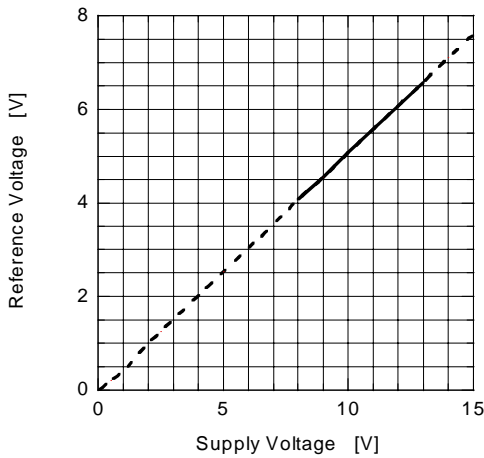
Supply Current vs Temperature

V+=9V



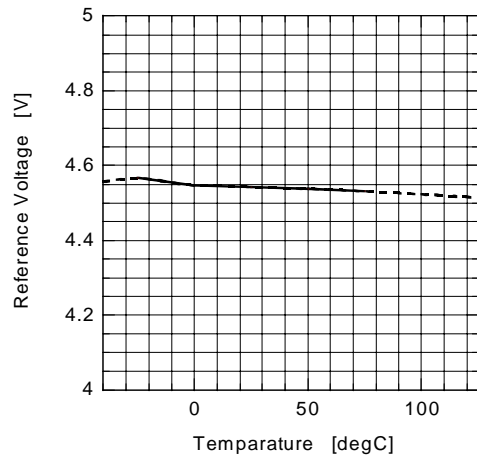
Reference Voltage vs Supply Voltage

Ta=25degC



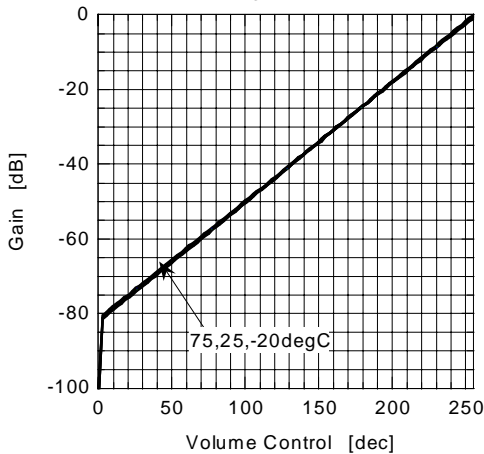
Reference Voltage vs Temperature

V+=9V



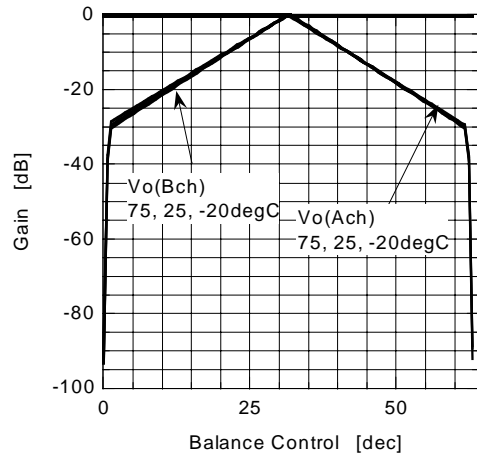
Gain vs Volume Control

V+=9V, Vin(Ach)=1Vrms, f=1kHz,
OUTPUT, Rg=600ohm



Gain vs Balance Control

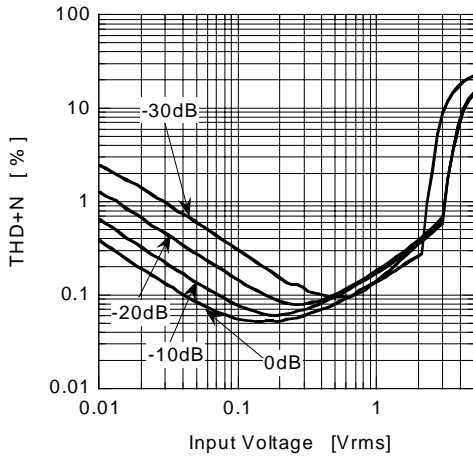
V+=9V, Vin=1Vrms, f=1kHz, Rg=600ohm



■ TYPICAL CHARACTERISTICS

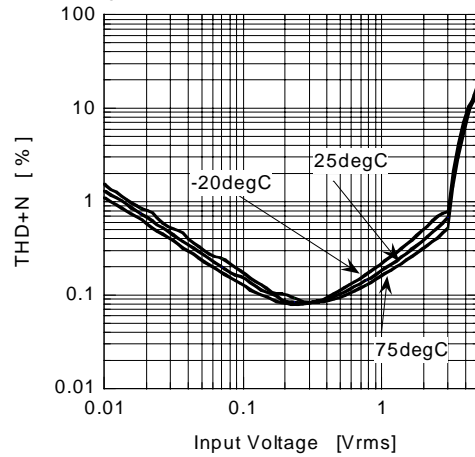
T.H.D.+N vs Input Voltage

V+=9V, Vin(Ach), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, BW=400Hz-30kHz, Ta=25degC



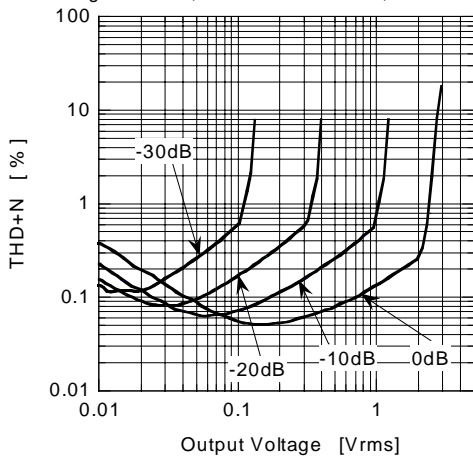
T.H.D.+N vs Input Voltage

V+=9V, Vin(Ach), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, BW=400Hz-30kHz, Vol=-20dB



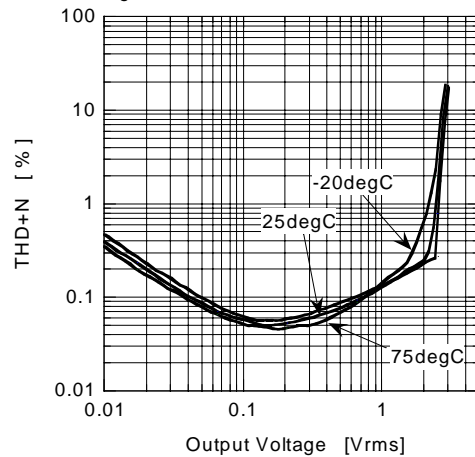
T.H.D.+N vs Output Voltage

V+=9V, Vin(Ach), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, BW=400Hz-30kHz, Ta=25degC



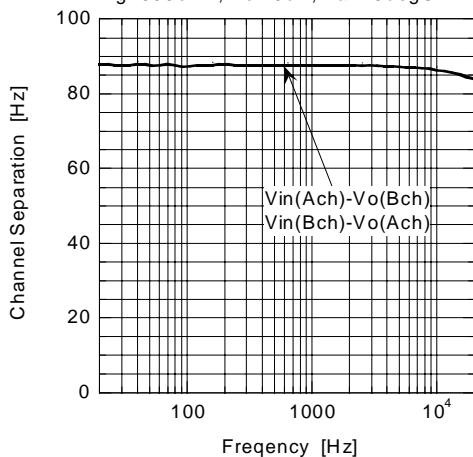
T.H.D.+N vs Output Voltage

V+=9V, Vin(Ach), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, BW=400Hz-30kHz, Vol=0dB



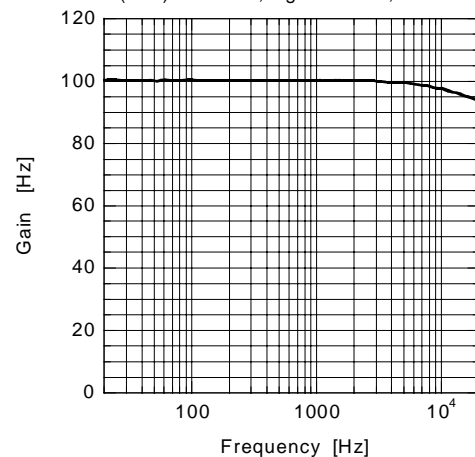
Channel Separation vs Frequency

V+=9V, Vin=2Vrms, f=1kHz, Vo=OUTPUT
Rg=600ohm, Vol=0dB, Ta=25degC



Gain vs Frequency (MUTE)

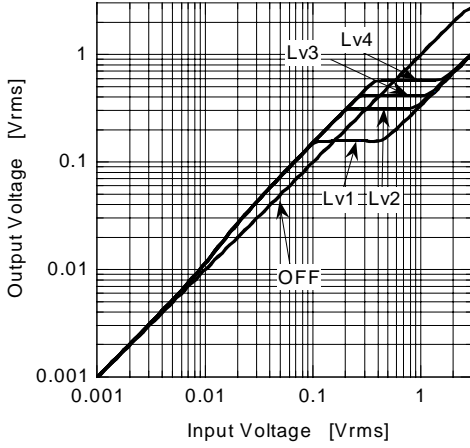
V+=9V, Vin(Ach)=1Vrms, Vol=MUTE
Vo(Ach)OUTPUT, Rg=600ohm, Ta=25degC



■TYPICAL CHARACTERISTICS

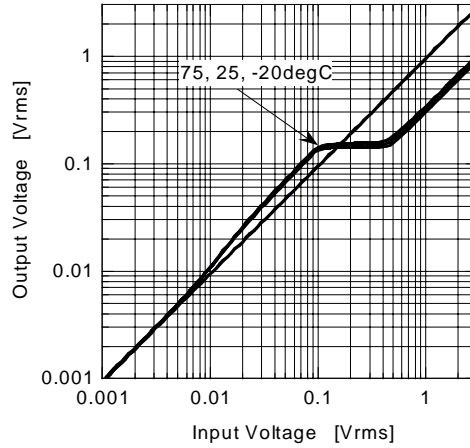
AGC Control

V+=9V, Vin(Ach+Bch), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, Ta=25degC



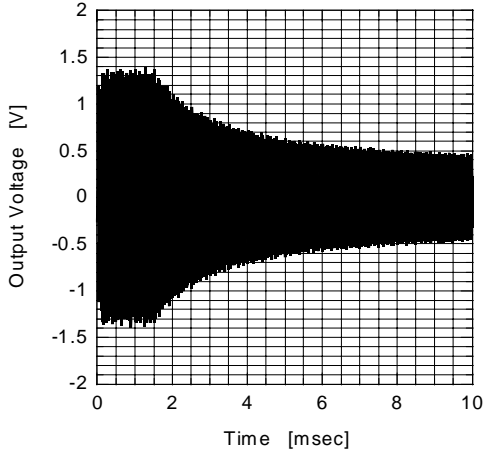
AGC Control

V+=9V, Vin(Ach+Bch), f=1kHz, Vo(Ach)OUTPUT
Rg=600ohm, Flat Level:0.15Vrms



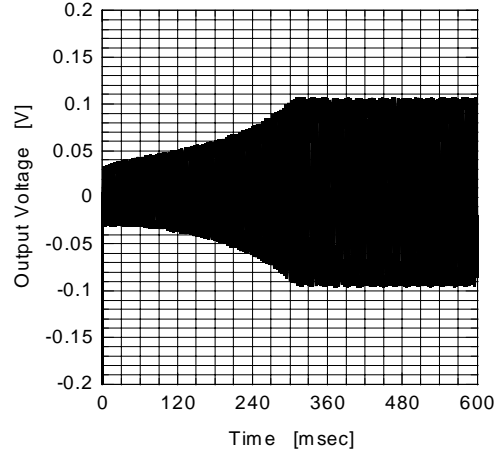
AGC Attack Time

V+=9V, Vin(Ach+Bch)=1Vrms, f=20kHz, Vo(Ach)OUTPUT
AGC level=0.15V, Rg=600ohm, Ta=25degC



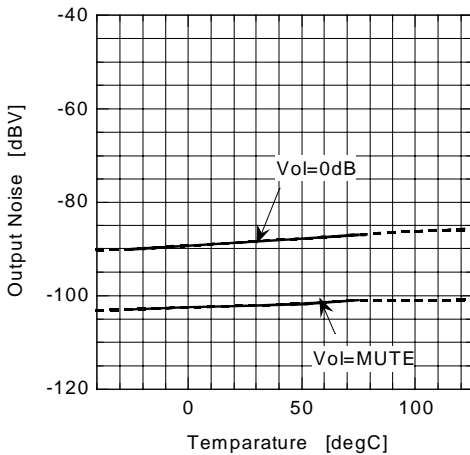
AGC Recovery Time

V+=9V, Vin(Ach+Bch)=1Vrms, f=10kHz, Vo(Ach)OUTPUT
AGC level=0.15V, Rg=600ohm, Ta=25degC



Output Noise vs Temperature

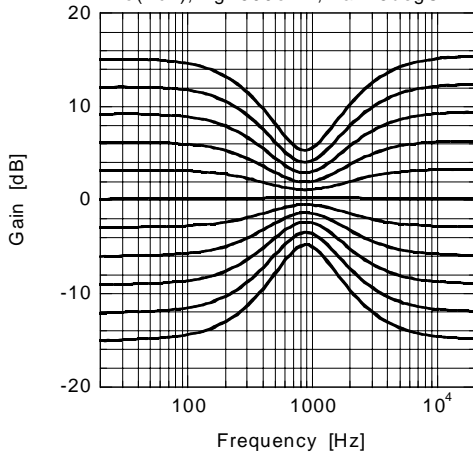
V+=9V, Rg=600ohm, BW =400Hz-30kHz



■ TYPICAL CHARACTERISTICS

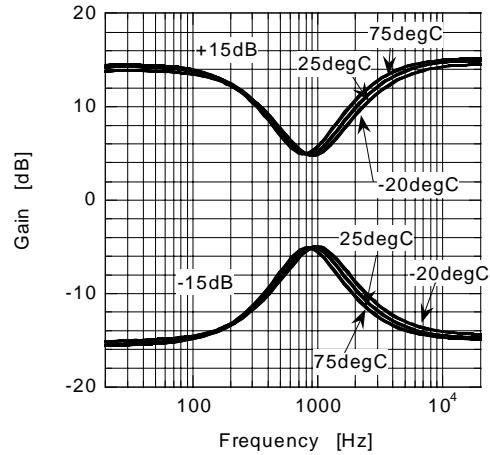
Gain vs Frequency (TONE)

V+=9V, Vin(Ach)=0.1Vrms, Gv:3dB steps
Vo(Ach), Rg=600ohm, Ta=25degC



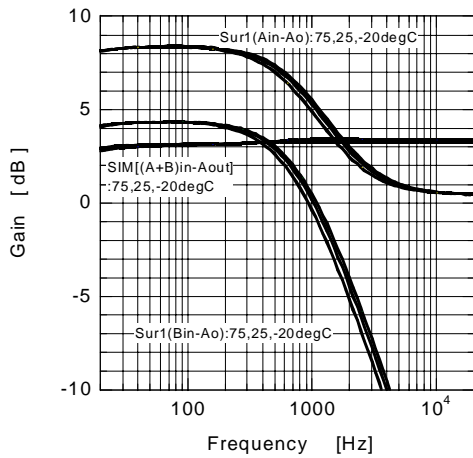
Gain vs Frequency (TONE)

V+=9V, Vin(Ach)=0.1Vrms, Gv=+15,0,-15dB
Vo(Ach), Rg=600ohm



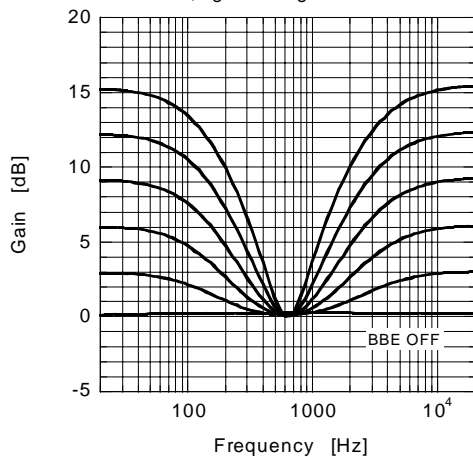
Gain vs Frequency (Eala & Simulated)

V+=9V, Vin(Ach)=0.1Vrms, Vo(Ach), Rg=600ohm



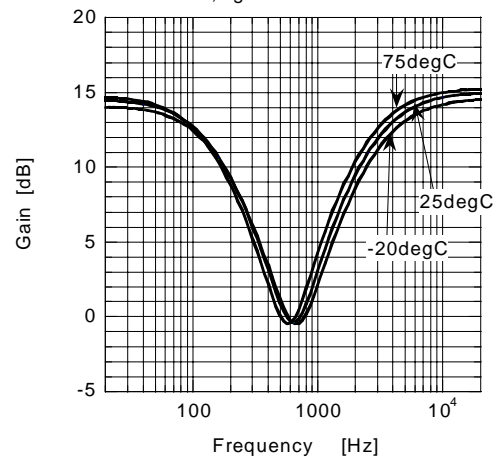
Gain vs Frequency (BBE)

V+=9V, Vin(Ach)=0.1Vrms, Gv=3dB steps
OUTPUT, Rg=600ohm



Gain vs Frequency (BBE)

V+=9V, Vin(Ach)=0.1Vrms, Gv=+15dB
OUTPUT, Rg=600ohm



NJW1137

■NOTE

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