## 查询PT78NP103H供应商 PT78NR100 Series

1 Amp Plus to Minus Voltage Integrated Switching Regulator



SLTS058B

(Revised 8/31/2000)

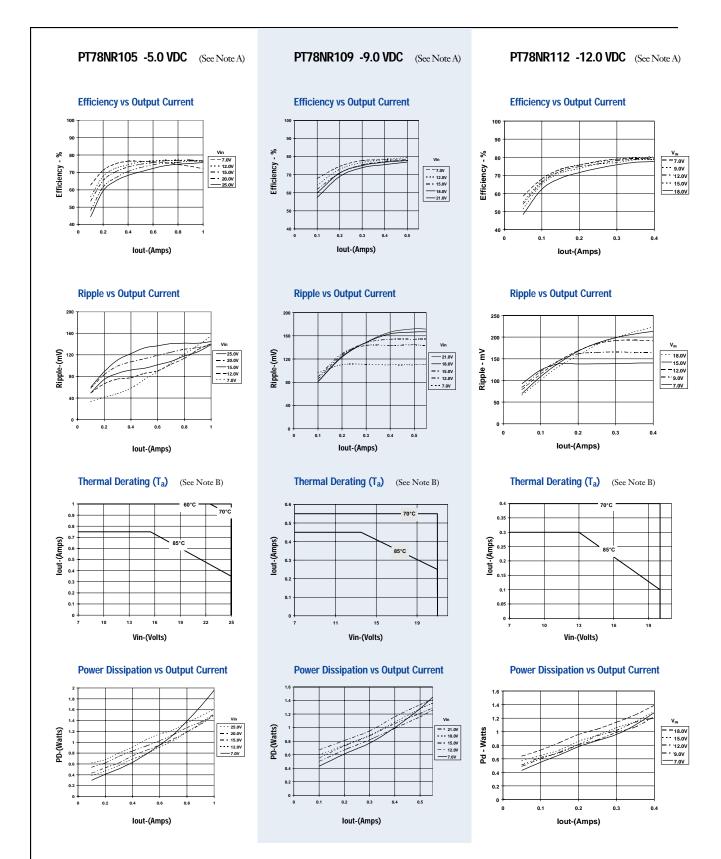
	ļ	<ul> <li>Negative output from positive input</li> <li>Wide Input Range</li> <li>Self-Contained Inductor</li> <li>Short Circuit Protection</li> <li>Over-Temperature Protection</li> <li>Fast Transient Response</li> </ul>	negative tive inp These e grated S have ma watts an that is l	The PT78NR100 Series creates a negative output voltage from a posi- tive input voltage greater than 7V. These easy-to-use, 3-terminal, Inte- grated Switching Regulators (ISRs) have maximum output power of 5 watts and a negative output voltage that is laser trimmed. They also have excellent line and load regulation.			
		Pin-Out Information Pin Function		g Inform 78NR1			
Standard Application		$\frac{1}{2} + V_{in}$		<b>X7 1</b>		age Suffix	
Vin 1 C1 PT78NR100 2 C1 C2 C1 Required 100µF electron C2 Required 100µF electron <b>Specifications</b>	+ COM	3 GND HORIZONTAL MOUNT SURFACE MOUNT 3 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	<b>12</b> = -12 <b>14</b> = -13	0 Volts 0 Volts 2 Volts 0 Volts 0 Volts 0 Volts 0 Volts	V = Ve S = Su H = H	ertical Mount urface Mount Iorizontal Aount	
<u>.</u>			P	T78NR100 S	ERIES		
Characteristics							
(T <sub>a</sub> = 25°C unless noted)	Symbols	Conditions	Min	Тур	Мах	Units	
	Symbols I <sub>o</sub>	$\begin{tabular}{ c c c c c } \hline Conditions & & & & & & & & & & & & & & & & & & &$		Тур — — — — —	Max           1.00           0.8           0.55           0.5           0.40           0.30	Units A	
(T <sub>a</sub> = 25°C unless noted)	-	$\begin{array}{ccc} \text{Over } V_{\text{in}} \text{ range } & V_{\text{o}}\text{=-}5V \\ & V_{\text{o}}\text{=-}-6V \\ & V_{\text{o}}\text{=-}7, -8, -9V \\ & V_{\text{o}}\text{=-}10V \\ & V_{\text{o}}\text{=-}12V \end{array}$	Min 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2)		1.00 0.8 0.55 0.5 0.40		
(T <sub>a</sub> = 25°C unless noted) Output Current	Io	$\begin{array}{ccc} \text{Over } V_{\text{in}} \text{ range } & V_{\text{o}}\text{=-}5V \\ & V_{\text{o}}\text{=-}-6V \\ & V_{\text{o}}\text{=-}, -8, -9V \\ & V_{\text{o}}\text{=-}10V \\ & V_{\text{o}}\text{=-}12V \\ & V_{\text{o}}\text{=-}12V \\ & V_{\text{o}}\text{=-}13.9, -15V \end{array}$	Min 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2)		1.00 0.8 0.55 0.5 0.40	A	
(T <sub>a</sub> = 25°C unless noted) Output Current Short Circuit Current	I <sub>o</sub> I <sub>sc</sub> I <sub>ir</sub>	$\begin{array}{c c} Over \ V_{in} \ range & V_{o}=-5V \\ V_{o}=-6V \\ V_{o}=-7, -8, -9V \\ V_{o}=-10V \\ V_{o}=-12V \\ V_{o}=-13.9, -15V \\ \hline \\ V_{in}=10V \\ \hline \end{array}$	Min 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2)	   4	1.00 0.8 0.55 0.5 0.40	A Apk A	
(T <sub>a</sub> = 25°C unless noted) Output Current Short Circuit Current Inrush Current	I <sub>o</sub> I <sub>ir</sub> I <sub>ir</sub>	$\label{eq:Vin} \begin{array}{ c c c } \hline Over \ V_{in} \ range & V_o{=}{-}5V \\ V_o{=}{-}7, {-}8, {-}9V \\ V_o{=}{-}10V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}13.9, {-}15V \\ \hline \end{array} \\ \hline \hline \\ \hline V_{in}{=}10V \\ On \ start{-}up \\ \hline \\ 0.1 \leq I_o \leq I_{max} & V_o{=}{-}5V \\ V_o{=}{-}6, {-}7, {-}8, {-}9V \\ V_o{=}{-}10, {-}12V \\ \hline \end{array}$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)		1.00 0.8 0.55 0.5 0.40 0.30   25 21 18	A Apk A mSec V V V V V V	
(T <sub>a</sub> = 25°C unless noted) Output Current Short Circuit Current Inrush Current Input Voltage Range	I <sub>o</sub> I <sub>ir</sub> I <sub>ir</sub> t <sub>ir</sub> V <sub>in</sub>	$\begin{tabular}{ c c c c c c } \hline Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -7, -8, -9V \\ V_o = -10V \\ V_o = -12V \\ V_o = -12V \\ V_o = -12V \\ V_o = -12V \\ V_o = -10V \\ \hline \hline \\ \hline $	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)	4 0.5	1.00 0.8 0.55 0.40 0.30 — — 25 21 18 15	A Apk A mSec V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$	$\begin{tabular}{ c c c c c c c } \hline Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -7, -8, -9V \\ V_o = -10V \\ V_o = -12V \\ V_o = -10, -15V \\ \hline Over \ V_{in} \ range \\ T_a = -20^\circ C \ to + 70^\circ C \\ \hline \end{tabular}$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)              7           7           7           7           7		$ \begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \end{array} $	A Apk A mSec V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation	$I_{o}$ $I_{sc}$ $I_{ir}$ $t_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$	$\begin{tabular}{ c c c c c c c } \hline Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -7, -8, -9V \\ V_o = -10V \\ V_o = -12V \\ V_o = -10, -15V \\ \hline Over \ V_{in} \ range \\ \hline \end{array}$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)              7           7           7           7           7		$ \begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \end{array} $	A Apk A mSec V V V V V V V V V V V V V V V V O O	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$	$\begin{tabular}{ c c c c c c c } \hline Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -10V \\ V_o = -12V \\ V_o = -10, -10, -12V \\ V_o = -10, -10V \\$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)              7           7           7           7           7		$ \begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \end{array} $	A Apk A mSec V V V V V V V V V V V V V V V S Vo %Vo	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         Vo Ripple/Noise         Transient Response	I <sub>o</sub> I <sub>sc</sub> I <sub>ir</sub> t <sub>ir</sub> V <sub>in</sub> ΔV <sub>o</sub> Reg <sub>line</sub> Reg <sub>load</sub> V <sub>n</sub>	$\begin{array}{c c} Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -7, -8, -9V \\ V_o = -12V \\ V_o = -6, -7, -8, -9V \\ V_o = -6, -7, -8, -9V \\ V_o = -10V \\ On \ start-up \\ \hline 0.1 \leq I_o \leq I_{max} \\ V_o = -6, -7, -8, -9V \\ V_o = -10, -12V \\ V_o = -10, -10V \\ V_o = -10, -10V$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)		$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \hline \\\\\\\\\\\\\\\\\\$	A Apk A mSec V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         VoRipple/Noise         Transient Response (with 100µF output cap)	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$	$\begin{array}{c c} Over \ V_{in} \ range & V_o{=}{-}5V \\ V_o{=}{-}6V \\ V_o{=}{-}7, {-}8, {-}9V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}10V \\ On \ start{-}up \\ \hline 0.1 \leq I_o \leq I_{max} & V_o{=}{-}5V \\ V_o{=}{-}0, {-}12V \\ V_o{=}{-}10, {-}12V \\ V_o{=}{-}10, {-}12V \\ V_o{=}{-}10, {-}12V \\ V_o{=}{-}10, {-}15V \\ \hline 0ver \ V_{in} \ range \\ T_a{=}{-}20^{\circ}C \ to {+}70^{\circ}C \\ \hline 0ver \ V_{in} \ range \\ 0.1 \leq I_o \leq I_{max} \\ V_{in}{=}10V, \ I_o{=}I_{max} \\ \hline V_{in}{=}10V, \ I_o{=}I_{max} \\ \hline 50\% \ load \ change \\ V_o \ over/undershoot \\ \hline \end{array}$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)		$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	A Apk A mSec V V V V V V V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         Vo Ripple/Noise         Transient Response (with 100µF output cap)         Efficiency	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$ $\eta$	$\label{eq:Vin} \begin{array}{ c c c c c } \hline Over \ V_{in} \ range & V_o{=}{-}5V \\ V_o{=}{-}7, {-}8, {-}9V \\ V_o{=}{-}10V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}12V \\ V_o{=}{-}10, {-}12V \\ V_o{=}10, {-}10, {-}12V \\ V_o{=}{-}10, {-}12V \\ V_o{=}$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)                 7           7           7           7		$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	A Apk A mSec V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         Vo Ripple/Noise         Transient Response (with 100µF output cap)         Efficiency         Switching Frequency         Absolute Maximum	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$ $\eta$ $f_{o}$	$\begin{tabular}{ c c c c c } \hline Over \ V_{in} \ range & V_o = -5V \\ V_o = -6V \\ V_o = -7, -8, -9V \\ V_o = -10V \\ V_o = -12V \\ V_o = -12V \\ V_o = -12V \\ V_o = -12V \\ V_o = -6, -7, -8, -9V \\ V_o = -6, -7, -8, -9V \\ V_o = -10, -12V \\ V_o = -10, -10V \\ V_o = -10, -10$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)                 7           7           7           7  <	$\begin{array}{c}$	$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\\\\\\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\\\\\\\\\\\\\ 700\\ \end{array}$	A Apk A mSec V V V V V V V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         Vo.Ripple/Noise         Transient Response (with 100µF output cap)         Efficiency         Switching Frequency         Absolute Maximum         Operating Temperaturte Range	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$ $\eta$ $f_{o}$ $T_{a}$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)                 7           7           7           7 <td>+1.0 +0.5 +1.0 ±0.5 ±0.5 ±0.5 ±2 100 5.0 75 650 </td> <td><math display="block">\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\ -\\ -\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \hline \\ \pm 8\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 3.0\\ \pm 1.0\\ \pm 3.0\\ \pm </math></td> <td>A Apk A mSec V V V V V V V V V V V V V</td>	+1.0 +0.5 +1.0 ±0.5 ±0.5 ±0.5 ±2 100 5.0 75 650 	$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\ -\\ -\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \hline \\ \pm 8\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 3.0\\ \pm 1.0\\ \pm 3.0\\ \pm $	A Apk A mSec V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         Vo.Ripple/Noise         Transient Response (with 100µF output cap)         Efficiency         Switching Frequency         Absolute Maximum         Operating Temperaturte Range         Thermal Resistance	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$ $\eta$ $f_{o}$ $T_{a}$ $\theta_{ja}$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)		$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm$	A Apk A mSec V V V V V V V V V V V V V	
(Ta = 25°C unless noted)         Output Current         Short Circuit Current         Inrush Current         Input Voltage Range         Output Voltage Tolerance         Line Regulation         Load Regulation         VoRipple/Noise         Transient Response         (with 100µF output cap)         Efficiency         Switching Frequency         Absolute Maximum         Operating Temperaturte Range         Thermal Resistance         Storage Temperature	$I_{o}$ $I_{sc}$ $I_{ir}$ $I_{ir}$ $V_{in}$ $\Delta V_{o}$ $Reg_{line}$ $Reg_{load}$ $V_{n}$ $t_{tr}$ $\eta$ $f_{o}$ $T_{a}$ $\theta_{ja}$ $T_{s}$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Min           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)           0.05 (2)		$\begin{array}{c} 1.00\\ 0.8\\ 0.55\\ 0.5\\ 0.5\\ 0.40\\ 0.30\\ \hline \\\\\\\\ 25\\ 21\\ 18\\ 15\\ \pm 3.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \pm 1.0\\ \hline \\ \pm 1.0\\ \pm 1.0\\ \hline \\ \pm 1.0\\ \hline \\ \pm 1.0\\ \pm 1.0\\ \hline \\ \\\\ \hline \\ \\\\ \hline \\ \\\\ \hline \\ \\\\ \hline \\ \\ +125\\ \hline \end{array}$	A Apk A mSec V V V V V V V V V V V V V	

**Notes:** (1) The PT78NR100 Series requires a 100µF electrolytic or tantalum capacitor at both the input and output for proper operation in all applications. The input capacitor,  $C_1$  must have a ripple current rating  $\geq 600$  mArms, and an ESR  $\leq 0.2\Omega$ . (2) The ISR will operate down to no load with reduced specifications.
 (3) See Thermal Derating chart.



## **Typical Characteristics**

1 Amp Plus to Minus Voltage Integrated Switching Regulator



Note A: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note B: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)

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O	rderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
	PT78NR103H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR103S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR103ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR103V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR105ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR106ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR107ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR108ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR109ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR110H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM

## PACKAGE OPTION ADDENDUM

13-May-2005

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
PT78NR110S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR110ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR110V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame



retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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Mailing Address:

Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

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