



ELECTRONICS, INC.
 44 FARRAND STREET
 BLOOMFIELD, NJ 07003
 (973) 748-5089

NTE1345 Integrated Circuit Module, Hybrid, Dual Audio Power Amp, 30W/Ch, 2 Power Supplies Required

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Supply Voltage, V_{CCmax}	$\pm 41\text{V}$
Substrate Temperature, T_C	$+105^\circ\text{C}$
Storage Temperature Range, T_{stg}	-30° to $+105^\circ\text{C}$
Turn-On Time, t_s ($V_{CC} = \pm 28\text{V}$, $P_O = 30\text{W}$, $R_L = 8\Omega$, $f = 50\text{Hz}$)	2sec

Recommended Operating Conditions: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Supply Voltage, V_{CC}	$\pm 28\text{V}$
Load Resistance, R_L	8Ω

Electrical Characteristics: ($T_A = 25^\circ\text{C}$, $V_{CC} = \pm 28\text{V}$, $R_L = 8\Omega$, $R_g = 600\Omega$, $V_G = 40\text{dB}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Idle Current	I_{CCO}	$V_{CC} = \pm 34\text{V}$	20	40	120	mA
Power Out	P_O	THD = 0.1%, $f = 20\text{Hz}$ to 20kHz	30	-	-	W
		$V_{CC} = \pm 25\text{V}$, THD = 0.2%, $R_L = 4\Omega$	40	-	-	W
Total Harmonic Distortion	THD	$P_O = 1.0\text{W}$	-	-	0.1	%
Breakpoints	f_L, f_H	$P_O = 1.0\text{W}$	10 to 100k			Hz
Source Impedance	r_i	$P_O = 1.0\text{W}$	-	32	-	k Ω
Input Noise Voltage	V_{NO}	$V_{CC} = \pm 34\text{V}$	-	-	1.2	mV _{rms}
Transient Noise Voltage	V_N	$V_{CC} = \pm 34\text{V}$	-70	0	70	mV



Pin Connection Diagram
(Front View)

16	Rt Ch Input
15	Rt Ch Feedback
14	GND
13	Rt Ch Bias
12	(-) V _{CC} 2
11	Rt Ch Feedback
10	Rt Ch Output
9	(+) V _{CC} 2
8	(+) V _{CC} 1
7	Lt Ch Output
6	Lt Ch Feedback
5	(-) V _{CC} 1
4	Lt Ch Bias
3	GND
2	Lt Ch Feedback
1	Lt Ch Input

