# Switching (-30V, -5.0A) SP8J1

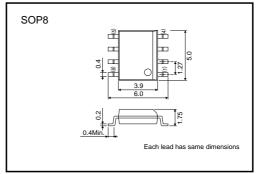
## Features

- 1) Low On-resistance. ( $40m\Omega$  at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive. (4.5V)

## Applications

Power switching, DC-DC converter

## •External dimensions (Unit : mm)



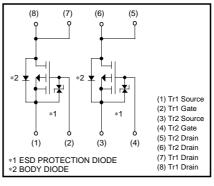
## Structure

Silicon P-channel MOS FET

## Packaging specifications

Package	Taping					
Code	TB					
Basic ordering unit (pieces)	2500					
	0					
	Code					

## •Equivalent circuit





## Transistors

## •Absolute maximum ratings (Ta=25°C)

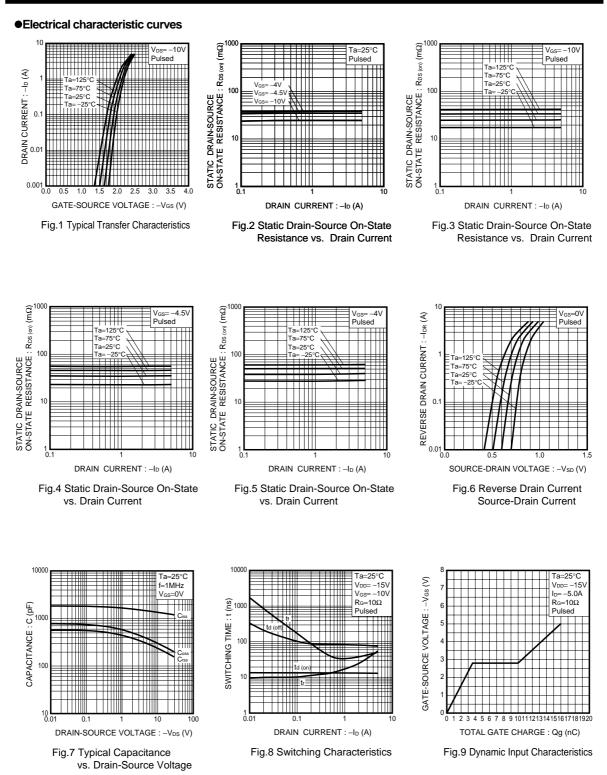
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Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	lo	±5.0	А	
	Pulsed	DP	±20	A *1	
Source current (Body diode)	Continuous	ls	-1.6	А	
	Pulsed	Isp	-20	A *1	
Total power dissipation		PD	2.0	W *2	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

\*1 Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board

## •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	-	±10	μΑ	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	
Drain-source breakdown voltage	V(BR) DSS	-30	-	_	V	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	Vds=-30V, Vgs=0V	
Gate threshold voltage	VGS (th)	-1.0	-	-2.5	V	$V_{DS} = -10V$ , $I_{D} = -1mA$	
Static drain-source on-state resistance	R <sub>DS (on)</sub>	-	30	42	mΩ	$I_D = -5.0A$ , $V_{GS} = -10V$	*
		-	40	56	mΩ	I <sub>D</sub> = -2.5A, V <sub>GS</sub> = -4.5V	*
		-	45	63	mΩ	$I_D = -2.5A$ , $V_{GS} = -4.0V$	*
Forward transfer admittance	Y <sub>fs</sub>	4.5	-	_	S	$V_{DS} = -10V, I_D = -2.5A$	*
Input capacitance	Ciss	-	1400	_	pF	V <sub>DS</sub> =-10V	
Output capacitance	Coss	-	300	_	pF	V <sub>GS</sub> =0V	
Reverse transfer capacitance	Crss	-	230	-	pF	f=1MHz	
Turn-on delay time	td (on)	-	15	-	ns	ID=-2.5A	*
Rise time	tr	-	30	-	ns	VDD≒-15V	*
Turn-off delay time	t <sub>d (off)</sub>	-	80	-	ns	Vgs= –10V Rι=6Ω	*
Fall time	tf	-	40	-	ns	$R_{GS}=10\Omega$	*
Total gate charge	Qg	-	16	-	nC	V <sub>DD</sub> ≒−15V	
Gate-source charge	Q <sub>gs</sub>	-	3.5	-	nC	V <sub>GS</sub> =-5V	
Gate-drain charge	Q <sub>gd</sub>	_	6.5	-	nC	I <sub>D</sub> =-5.0A	
*Pulsed							
Body diode characteristics (so	urce-drair	h charao	cteristic	s)			
Forward voltage	VSD	-	-	-1.2	V	Is=-1.6A, V <sub>GS</sub> =0V	

## Transistors



## Transistors

## Measurement circuits

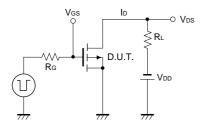


Fig.10 Switching Time Test Circuit

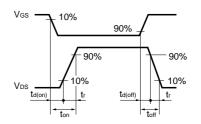


Fig.11 Switching Time Waveforms

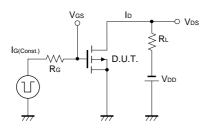


Fig.12 Gate Charge Test Circuit

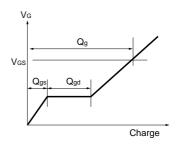


Fig.13 Gate Charge Waveform

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