Philips Semiconductors

Product specification

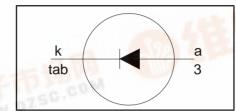
Rectifier diodes Schottky barrier

PBYR1045D series

FEATURES

- Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- Low thermal resistance

SYMBOL



QUICK REFERENCE DATA

$$V_R = 40 \text{ V} / 45 \text{ V}$$
 $I_{F(AV)} = 10 \text{ A}$
 $V_F \le 0.57 \text{ V}$

GENERAL DESCRIPTION

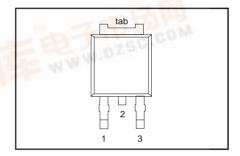
Schottky rectifier diodes in a plastic envelope. Intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYR1045D series is supplied in the surface mounting SOT428 package.

PINNING

PIN	DESCRIPTION	
1	no connection	
2	cathode ¹	
3	anode	
tab	cathode	

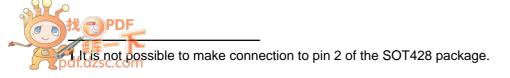
SOT428



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	UNIT	MAX.		MIN.	CONDITIONS	PARAMETER	SYMBOL
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	٧				PBYR10		V_{RRM}
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	V	45	40	-	THE COLUMN	Working peak reverse	V_{RWM}
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	V	45	40	-	T _{mb} ≤ 113 °C		V_R
$ \begin{array}{c} \text{current} \\ \text{Non-repetitive peak forward} \\ \text{current} \end{array} \begin{array}{c} \text{t} = 10 \text{ ms} \\ \text{t} = 8.3 \text{ ms} \\ \text{sinusoidal; T}_{\text{j}} = 125 \text{ °C prior to} \\ \text{surge; with reapplied V}_{\text{RRM(max)}} \\ \text{pulse width and repetition rate} \end{array} \begin{array}{c} \text{-} \\ \text{100} \\ \text{-} \\ \text{110} \\ \text{-} \\ \text{-} \\ \text{1} \end{array} $	Α	0	1	-	square wave; $\delta = 0.5$; $T_{mb} \le 134$ °C		$\mathbf{I}_{F(AV)}$
current $t = 8.3 \text{ ms}$ $\sin \cos i dal; T_j = 125 ^{\circ}\text{C prior to}$ $\sin i dal; T_j = 125 ^{\circ}\text{C prior to}$ $\sin i dal; T_j = 125 ^{$	Α	0	2	-	square wave; $\delta = 0.5$; $T_{mb} \le 134$ °C		I _{FRM}
I _{RRM} Peak repetitive reverse pulse width and repetition rate - 1	A A			E	t = 8.3 ms		FSM
ISUIGE CUITEDL HIMILEG DV 1: may	Α	1	M.M.M.	150	pulse width and repetition rate limited by T _{i max}	Peak repetitive reverse surge current	I _{RRM}
T _j Operating junction temperature - 150	°C	50	15	-		Operating junction	T_j
T _{stg} Storage temperature - 65 175	°C	75	17	- 65	Trans		T _{stq}



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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb}	Thermal resistance junction			-	2	K/W
R _{th j-a}	to mounting base Thermal resistance junction to ambient	pcb mounted, minimum footprint, FR4 board	ı	50	-	K/W

ELECTRICAL CHARACTERISTICS

T_i = 25 °C unless otherwise specified

]						
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{F}	Forward voltage	$I_{\rm F} = 10 \text{ A}; T_{\rm i} = 125 ^{\circ}\text{C}$	-	0.5	0.57	٧
1		$I_{\rm F} = 20 \text{ A}; T_{\rm i} = 125 ^{\circ}\text{C}$	-	0.74	0.8	V
		$I_{\rm F} = 20 {\rm A}^{\prime}$	-	0.65	0.84	V
I _R	Reverse current	$\dot{V}_{R} = V_{RWM}$	-	0.2	1.3	mΑ
1		$V_R = V_{RWM}$; $T_i = 100$ °C	-	22	35	mΑ
C _d	Junction capacitance	$V_R = 5 \text{ W}; \text{ f} = 1 \text{ MHz}, T_j = 25 \text{ °C to } 125 \text{ °C}$	-	350	-	pF

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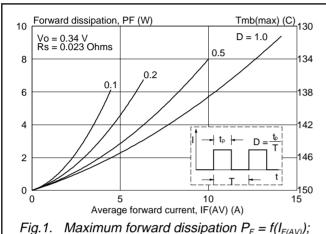


Fig.1. Maximum forward dissipation $P_F = f(I_{F(AV)});$ square current waveform where $I_{F(AV)} = I_{F(RMS)} \times \sqrt{D}.$

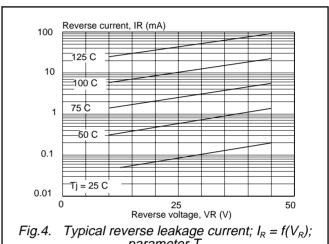


Fig.4. Typical reverse leakage current; $I_R = f(V_R)$; parameter T_i

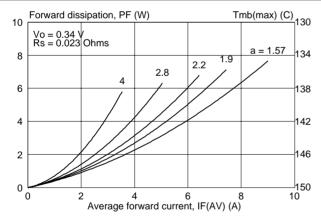
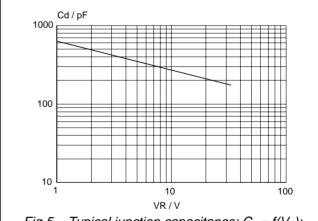


Fig.2. Maximum forward dissipation $P_F = f(I_{F(AV)})$; sinusoidal current waveform where $a = f(I_{F(AV)})$; $factor = I_{F(RMS)} / I_{F(AV)}$.



Typical junction capacitance; $C_d = f(V_R)$; f = 1 MHz; $T_j = 25 ^{\circ}\text{C}$ to $125 ^{\circ}\text{C}$. Fig.5.

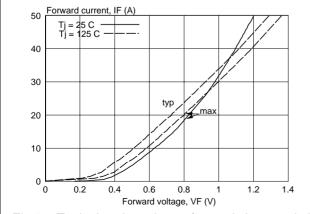


Fig.3. Typical and maximum forward characteristic $I_F = f(V_F)$; parameter T_i

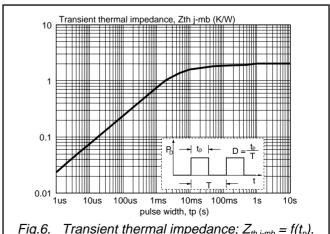
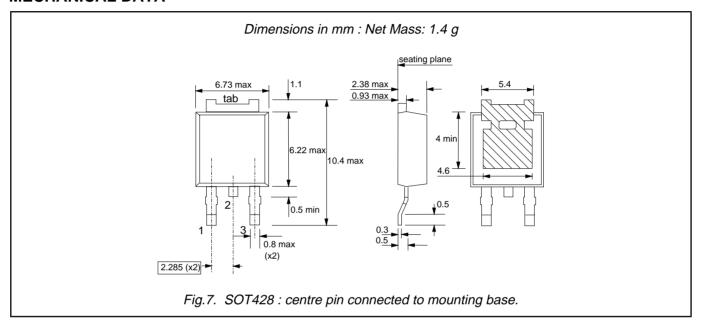


Fig.6. Transient thermal impedance; $Z_{th j-mb} = f(t_p)$.

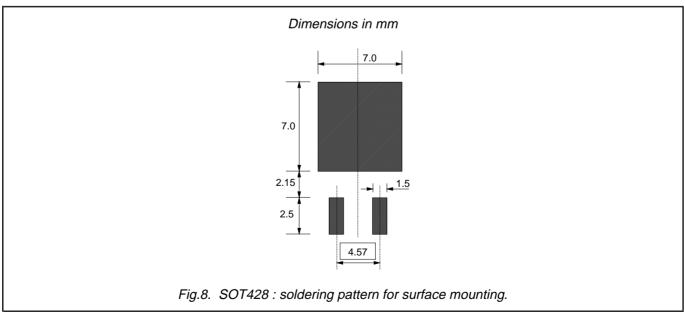
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MECHANICAL DATA



MOUNTING INSTRUCTIONS



Notes

- Observe the general handling precautions for electrostatic-discharge sensitive devices (ESDs) to prevent damage to MOS gate oxide.
 Epoxy meets UL94 V0 at 1/8".

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Schottky	barrier

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DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification This data sheet contains final product specifications.				
Limiting values				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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