**Philips Semiconductors** 

**Product specification** 

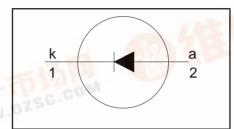
## Rectifier diodes Schottky barrier

# PBYR1645F, PBYR1645X

## **FEATURES**

- Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- · Isolated mounting tab

## **SYMBOL**



## QUICK REFERENCE DATA

$$V_R = 40 \text{ V/ } 45 \text{ V}$$

$$I_{F(AV)} = 16 \text{ A}$$

$$V_F \le 0.6 \text{ V}$$

## **GENERAL DESCRIPTION**

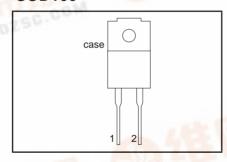
Schottky rectifier diodes in a plastic envelope with electrically isolated mounting tab. Intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYR1645F is supplied in the SOD100 package. The PBYR1645X is supplied in the SOD113 package.

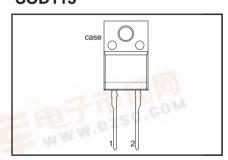
## **PINNING**

PIN	DESCRIPTION	
1	cathode	
2	anode	
tab	isolated	

## **SOD100**



## **SOD113**



## **LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	M	AX.	UNIT
V <sub>RRM</sub>	Peak repetitive reverse	PBYR16 PBYR16	-	<b>40F</b> <b>40X</b> 40	<b>45F</b> <b>45X</b> 45	V
$V_{RWM}$	voltage Working peak reverse voltage		="	40	45	V
$V_R$	Continuous reverse voltage	T <sub>hs</sub> ≤ 97 °C	12.	40	45	V
I <sub>F(AV)</sub>	Average rectified forward current	square wave; δ = 0.5; T <sub>hs</sub> ≤ 95 °C	-	16		A
I <sub>FRM</sub>	Repetitive peak forward current	square wave; $\delta = 0.5$ ; $T_{hs} \le 95$ °C	-	3	32	А
I <sub>FSM</sub>	Non-repetitive peak forward current	t = 10  ms t = 8.3  ms sinusoidal; $T_j = 125 ^{\circ}\text{C}$ prior to surge; with reapplied $V_{\text{RRM(max)}}$	-		20 32	A A
I <sub>RRM</sub>	Peak repetitive reverse surge current	pulse width and repetition rate limited by T <sub>i max</sub>	-	,	1	Α
T <sub>j</sub>	Operating junction temperature	Jillax	-	1	50	°C
T <sub>stg</sub>	Storage temperature		- 65	1	75	°C

PBYR1645F, PBYR1645X

# **ISOLATION LIMITING VALUE & CHARACTERISTIC**

T<sub>hs</sub> = 25 °C unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	Peak isolation voltage from both terminals to external heatsink	SOD100 package; R.H. ≤ 65%; clean and dustfree	-	-	1500	V
V <sub>isol</sub>	R.M.S. isolation voltage from both terminals to external heatsink	SOD113 package; f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65%; clean and dustfree	-	-	2500	V
C <sub>isol</sub>	Capacitance from pin 1 to external heatsink	f = 1 MHz	-	10	-	pF

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Thermal resistance junction to heatsink	with heatsink compound	-	-	4.2	K/W
R <sub>th i-a</sub>	100 110 0110 1111	in free air	-	55	-	K/W

## **ELECTRICAL CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{F}$	Forward voltage	$I_F = 16 \text{ A}; T_i = 125^{\circ}\text{C}$	-	0.53	0.6	V
		$I_{\rm F} = 16  {\rm A}$	-	0.55	0.68	V
$I_R$	Reverse current	$V_R = V_{RWM}$	-	0.2	1.7	mΑ
		$V_R = V_{RWM}^{N}$ ; $T_j = 100^{\circ}C$	-	27	40	mΑ
C <sub>d</sub>	Junction capacitance	$V_R = 5 \hat{V}$ ; $\hat{f} = 1 \text{ MHz}$ , $T_j = 25 \hat{C}$ to $125 \hat{C}$	-	470	-	pF

# PBYR1645F, PBYR1645X

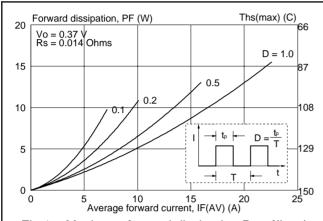


Fig.1. Maximum forward dissipation  $P_F = f(I_{F(AV)})$ ; square current waveform where  $I_{F(AV)} = I_{F(RMS)} \times \sqrt{D}$ .

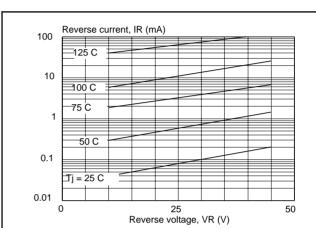


Fig.4. Typical reverse leakage current;  $I_R = f(V_R)$ ; parameter  $T_j$ 

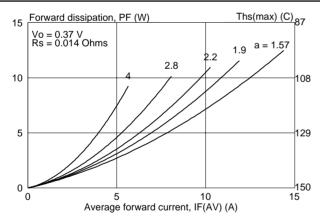


Fig.2. Maximum forward dissipation  $P_F = f(I_{F(AV)})$ ; sinusoidal current waveform where a = form factor  $= I_{F(RMS)} / I_{F(AV)}$ .

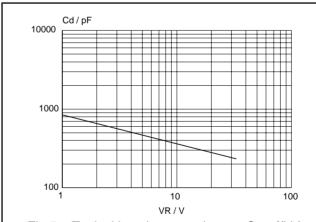


Fig.5. Typical junction capacitance;  $C_d = f(V_R)$ ; f = 1 MHz;  $T_j = 25^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ .

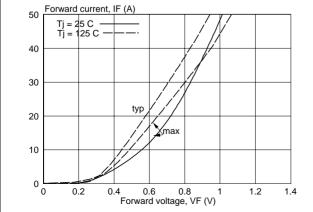


Fig.3. Typical and maximum forward characteristic  $I_F = f(V_F)$ ; parameter  $T_i$ 

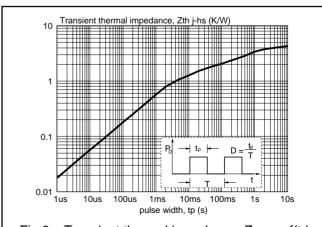
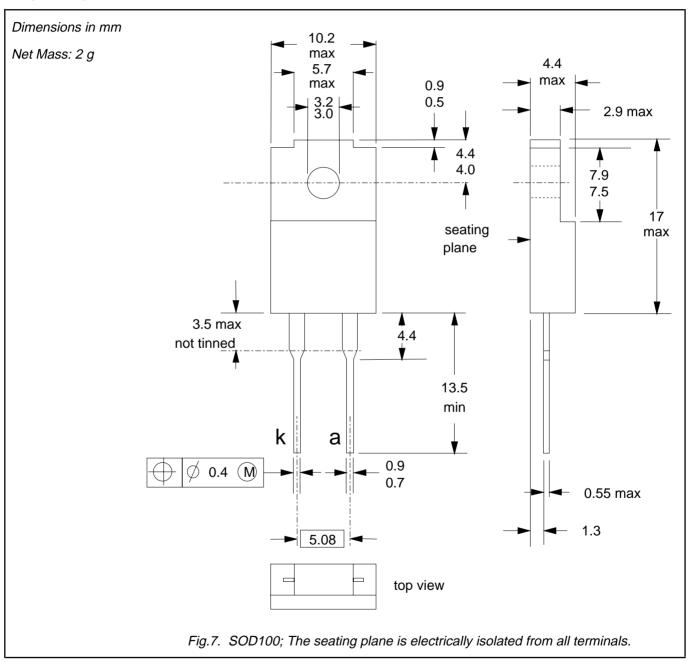


Fig.6. Transient thermal impedance;  $Z_{th j-hs} = f(t_p)$ .

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## **MECHANICAL DATA**

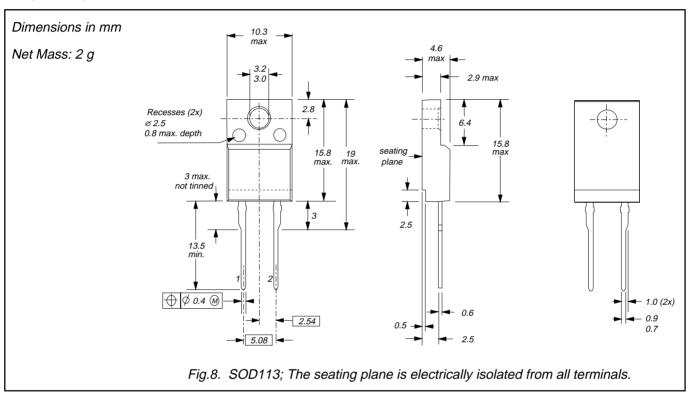


## **Notes**

- Refer to mounting instructions for F-pack envelopes.
   Epoxy meets UL94 V0 at 1/8".

# PBYR1645F, PBYR1645X

## **MECHANICAL DATA**



- Refer to mounting instructions for F-pack envelopes.
   Epoxy meets UL94 V0 at 1/8".

Rectifier	diodes
Schottky	barrier

PBYR1645F, PBYR1645X

## **DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

#### Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

## **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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