# FRD MODULE 50A/200V/trr:80nsec

## PC50F2

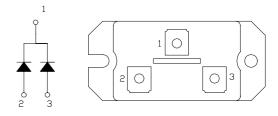
#### **OUTLINE DRAWING**

#### **FEATURES**

- \* Isolated Base
- \* Dual Diode Cathode Common
- \* Ultra Fast Recovery
- \* High Surge Capability
- \* UL Recognized, File No. E187184

#### TYPICAL APPLICATIONS

\* High Frequency Rectification





### Maximum Ratings

Approx Net Weight:105g

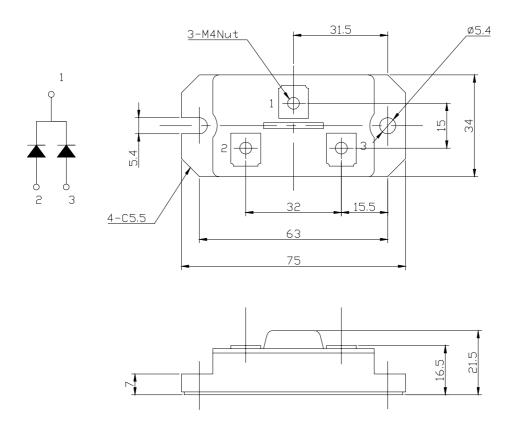
Voltage Rating	Symbol	PC50F2		Unit
Repetitive Peak Reverse Voltage per Arm	Vrrm	200		V
Non-Repetitive Peak Reverse Voltage	V <sub>RSM</sub>	220		V
Electrical Rating		Condition	Rating	
Average Rectified Output Current	Io	50Hz Half Sine Wave condition per Arm Tc=111°C	50	Α
RMS Forward Current	I <sub>F(RMS)</sub>	per Arm	78	Α
Surge Forward Current	ECM	50 Hz Half Sine Wave,1cycle Non-repetitive per Arm	800	A
I Squared t	I2t	2 msec to 10 msec per Arm	3200	$A^2s$
Operating JunctionTemperature Range	Tjw		-40 to +150	°C
Storage Temperature Range	Tstg		-40 to +125	°C
Isoration Voltage	Viso	Base Plate to Terminal, AC1min	2000	V
Mounting torque	Ftor	Case mounting(recommended)	2.6	N•m
		Terminal Screw(recommended)	1.4	

#### **Electrical** • Thermal Characteristics

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Characteristics	Symbol	Test Conditions	Max.	Unit
Peak Forward Voltage	V <sub>FM</sub>	I <sub>FM</sub> = 50A, Tj=25°C, per Arm	1.0	V
Peak Reverse Current	$I_{RM}$	V <sub>RM</sub> = V <sub>RRM</sub> , Tj= 150°C, per Arm	10	mA
Reverse Recovery Time	trr	$I_{FM}$ = 10A, -di/dt= 50 A/ $\mu$ s, Ta= 25°C Per Arm	80	ns
Thermal Resistance	Rth(j-c)	Junction to Case per Arm	0.8	
	Rth(c-f)	Base Plate to Heat Sink with Thermal Compound	0.1	°C/W
Internal Lead Inductance	Ls	Anode Terminal to Cathode Terminal Per Element	30	nН

# Nihon Inter Electronics Corporation

## PC50F2 OUTLINE DRAWING (Dimensions in mm)



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