

FRD MODULE 50A/500V/trr:90nsec

PC50F5

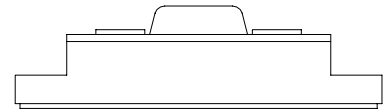
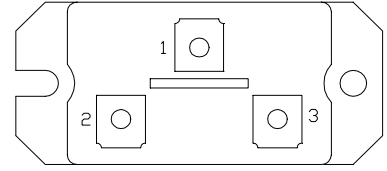
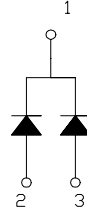
OUTLINE DRAWING

FEATURES

- * Isolated Base
- * Dual Diode Cathode Common
- * Ultra Fast Recovery
- * High Surge Capability
- * UL Recognized, File No. E187184

TYPICAL APPLICATIONS

- * High Frequency Rectification



Maximum Ratings

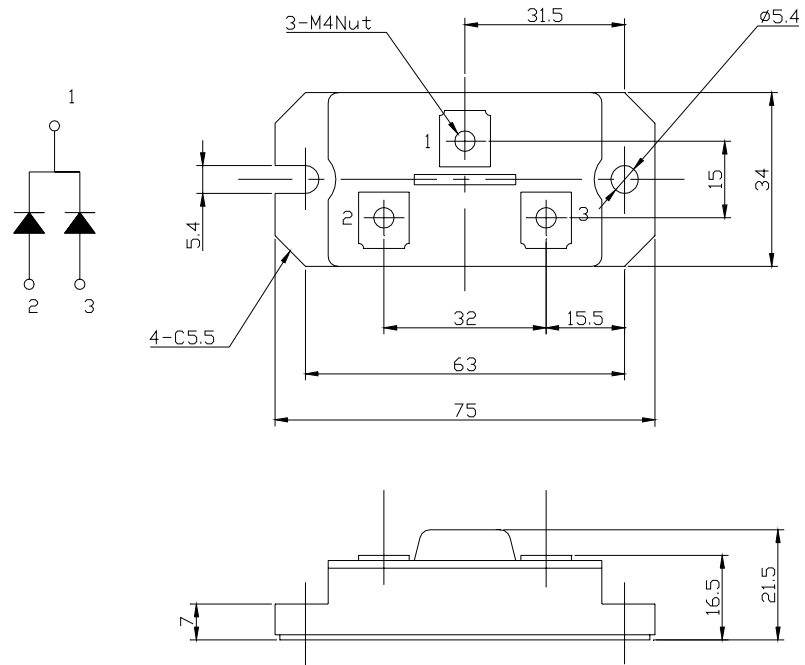
Approx Net Weight:105g

Voltage Rating	Symbol	PC50F5		Unit
Repetitive Peak Reverse Voltage per Arm	V_{RRM}	500		V
Electrical Rating		Condition	Rating	
Average Rectified Output Current	I_o	50Hz Half Sine Wave condition per Arm $T_c=90^\circ C$	50	A
RMS Forward Current	$I_{F(RMS)}$	per Arm	78	A
Surge Forward Current	I_{FSM}	50 Hz Half Sine Wave, 1 cycle Non-repetitive per Arm	800	A
I Squared t	I^2t	2 msec to 10 msec per Arm	3200	A^2s
Operating Junction Temperature Range	T_{jw}		-40 to +150	$^\circ C$
Storage Temperature Range	T_{stg}		-40 to +125	$^\circ C$
Isolation Voltage	Viso	Base Plate to Terminal, AC1min	2000	V
Mounting torque	Ftor	Case mounting(recommended)	2.6	N.m
		Terminal Screw(recommended)	1.4	

Electrical • Thermal Characteristics

Characteristics	Symbol	Test Conditions	Max.	Unit
Peak Forward Voltage	V_{FM}	$I_{FM}= 50A, T_j=25^\circ C, \text{ per Arm}$	1.35	V
Peak Reverse Current	I_{RM}	$V_{RM}= V_{RRM}, T_j= 150^\circ C, \text{ per Arm}$	10	mA
Reverse Recovery Time	trr	$I_{FM}= 10A, -di/dt= 50 A/\mu s, T_a= 25^\circ C$ Per Arm	90	ns
Thermal Resistance	Rth(j-c)	Junction to Case per Arm	0.8	$^\circ C/W$
	Rth(c-f)	Base Plate to Heat Sink with Thermal Compound	0.1	
Internal Lead Inductance	Ls	Anode Terminal to Cathode Terminal Per Element	30	nH

PC50F5 OUTLINE DRAWING (Dimensions in mm)



Copyright © Each Manufacturing Company.

All Datasheets cannot be modified without permission.

This datasheet has been download from :

www.AllDataSheet.com

100% Free DataSheet Search Site.

Free Download.

No Register.

Fast Search System.

www.AllDataSheet.com