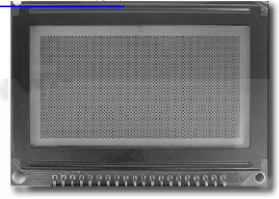
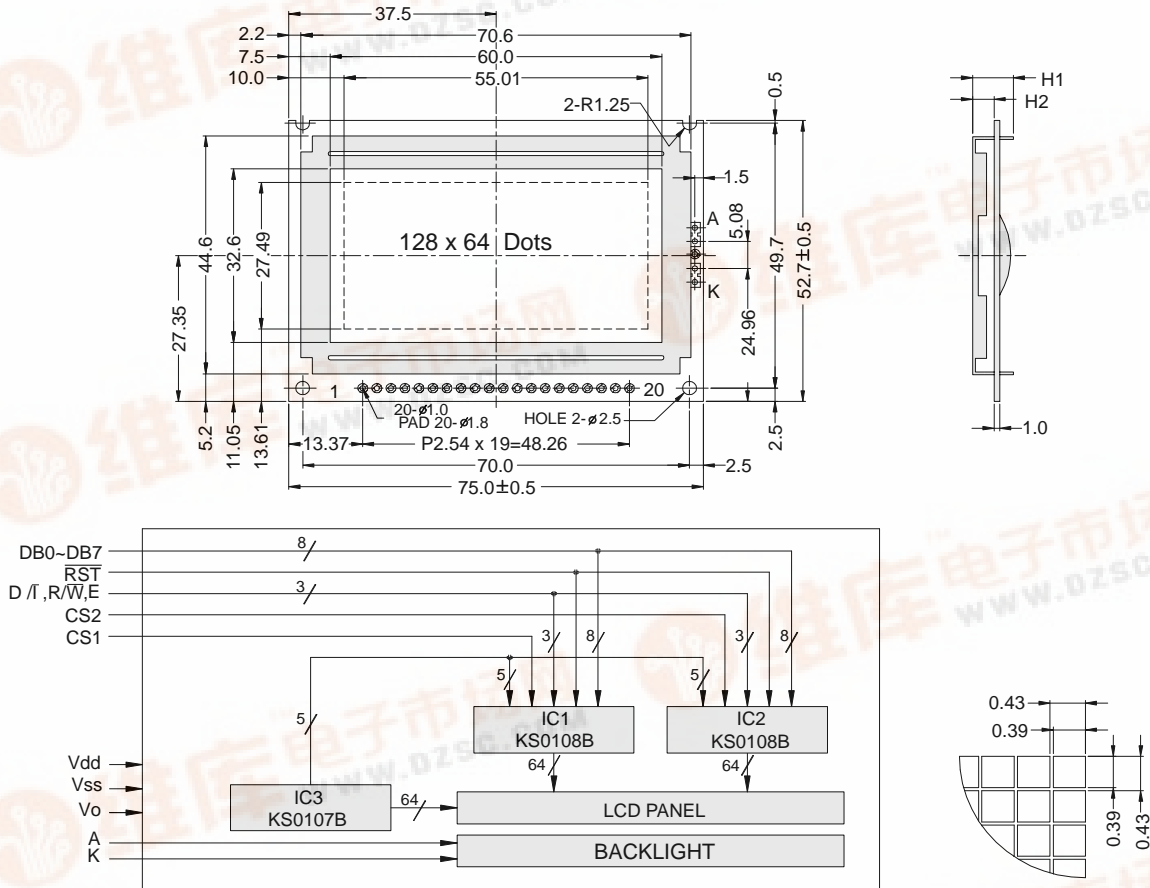


PG 12864-J



OUTLINE DIMENSION & BLOCK DIAGRAM



The tolerance unless classified $\pm 0.3\text{mm}$

MECHANICAL SPECIFICATION					
Overall Size	75.0 x 52.7	Module	H2 / H1		
View Area	60.0 x 32.6	W/O B/L	3.9 / 7.4		
Dot Size	0.39 x 0.39	EL B/L	3.9 / 7.4		
Dot Pitch	0.43 x 0.43	LED B/L	5.4 / 8.4		

PIN ASSIGNMENT		
Pin no.	Symbol	Function
1	Vdd	Power supply(+)
2	Vss	Power supply(GND)
3	Vo	Contrast Adjust
4-11	DB0-DB7	Data bus line
12	CS1	Chip select driver 1
13	CS2	Chip select driver 2
14	RST	Reset
15	R/W	Data read / write
16	D/I	Command / data select
17	E	Chip enable signal
18	Vss	Power supply (GND)
19	A	Power supply for LED B/L(+)
20	K	Power supply for LED B/L(-)

ABSOLUTE MAXIMUM RATING									
Item	Symbol	Condition	Min.	Max.	Units				
Supply for logic voltage	Vdd-Vss	25°C	-0.3	7.0	V				
LCD driving supply voltage	Vdd-Vee	25°C	-0.3	19.0	V				
Input voltage	Vin	25°C	-0.3	Vdd-0.3	V				
ELECTRICAL CHARACTERISTICS									
Item	Symbol	Condition	Min.	Typical	Max.	Units			
Power supply voltage	Vdd-Vss	25°C	4.5	-	5.5	V			
		Top	N	W	N	W	N	W	V
LCD operation voltage	Vop	-20°C	-	8.3	-	9	-	9.7	V
		0°C	8.2	-	8.9	-	9.6	-	V
		25°C	7.7	8.3	8.4	9	9.1	9.7	V
		50°C	7.3	-	8.0	-	8.7	-	V
		70°C	-	7.7	-	8.4	-	9.1	V
LCM current consumption (No B/L)	Idd	Vdd=5V	-	2.5	5	mA			
Backlight current consumption	LED/edge	VB/L=4.2V	-	100	-	mA			
	LED/array	VB/L=4.2V	-	-	-	mA			

