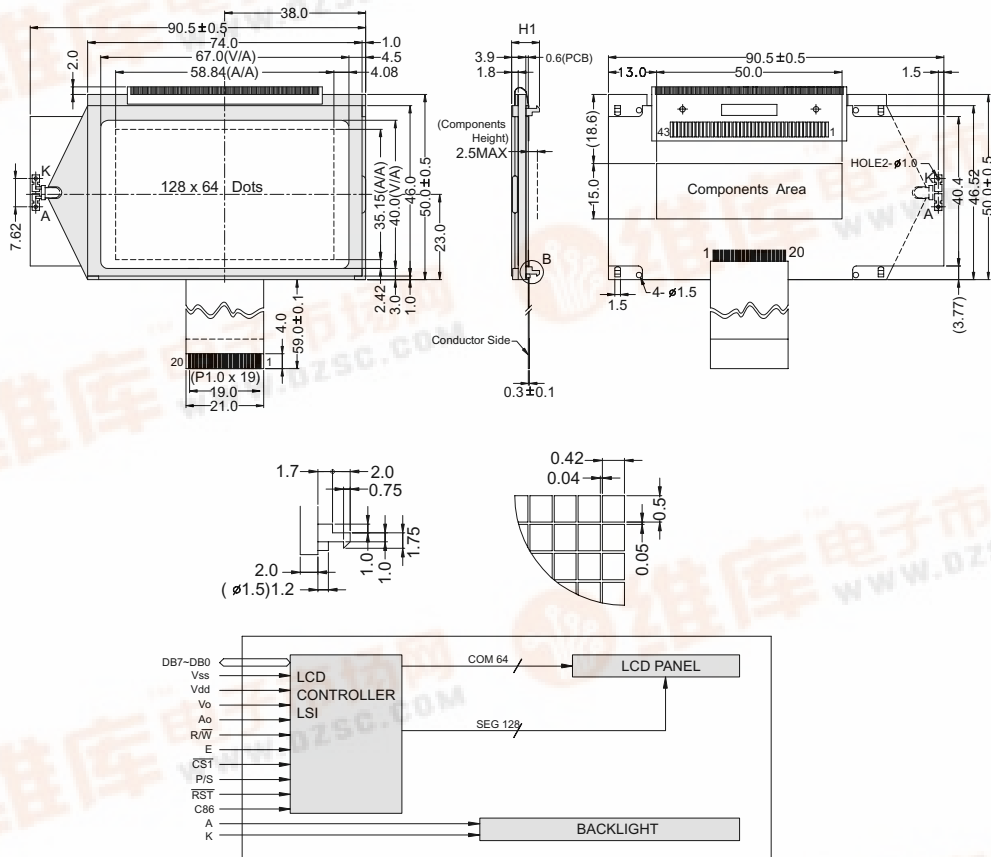


PG 12864-Q



OUTLINE DIMENSION & BLOCK DIAGRAM

The tolerance unless classified $\pm 0.3\text{mm}$

MECHANICAL SPECIFICATION

Overall Size	90.5 x 52.0	Module	H2 / H1
View Area	67.0 x 40.0	W / O B/L	- / 7.5
Dot Size	0.42 x 0.5	EL B/L	- / -
Dot Pitch	0.46 x 0.55	LED B/L	- / 7.5

PIN ASSIGNMENT

Pin no.	Symbol	Function
1	Vss	Power supply(GND)
2	Vdd	Power supply(+)
3	Vo	Contrast Adjust
4	A0	Command / data input
5	R/W	Data read / write signal(/RW)
6	E	Enable signal
7-14	DB0-DB7	Data bus line
15	CS1	Chip select
16	P/S	Parallel/serial interface select
17	RST	Reset
18	C86	8080 / 6800 select
19	A	Power supply for LED B/L (+)
20	K	Power supply for LED B/L (-)

ABSOLUTE MAXIMUM RATING

Item	Symbol	Condition	Min.	Max.	Units
Supply for logic voltage	Vdd-Vss	25°C	-0.3	7.0	V
LCD driving supply voltage	Vdd-Vee	25°C	-0.3	18.0	V
Input voltage	Vin	25°C	-0.3	Vdd+0.3	V

ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typical	Max.	Units
Power supply voltage	Vdd-Vss	25°C	1.8	-	5.5	V
LCD operation voltage	Vop	Top	N	W	N	W
		-20°C	-	9.9	-	10.4
		0°C	-	-	-	-
		25°C	-	8.4	-	9.4
		50°C	-	-	-	-
		70°C	-	7.8	-	8.3
LCM current consumption (No B/L)	Idd	Vdd=5V	-	1.0	2.5	mA
Backlight current consumption	LED/edge	VB/L=3.5V	-	20	-	mA
	LED/array	VB/L=4.2V	-	-	-	mA