



PI3B16210 PI3B162210 (25Ω)

3.3V, Hot Insertion 20-Bit, 2-Port BusSwitch

Product Features

- Near-zero propagation delay
- 5Ω or 25Ω switches connect inputs to outputs
- Fair Switching Speed - 4.5ns max.
- 32X384 function with flow through pinout make board layout easier
- Permits Hot Insertion.
- Vcc Operating Range: 3.0V to 3.6V
- Industrial operating temperature: -40°C to +85°C
- Packages available:
 - 48-pin 150-mil wide plastic QSOP (B)
 - 48-pin 240-mil wide plastic TSSOP (A)
 - 48-pin 300-mil wide plastic SSOP (V)

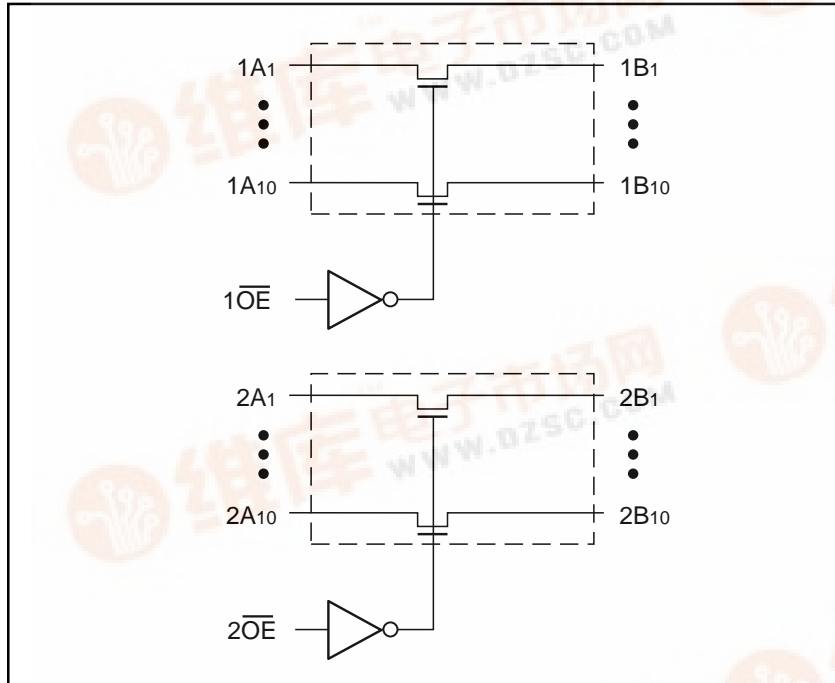
Product Description

Pericom Semiconductor's PI3B series of BusSwitch circuits are produced in the Company's advanced 0.35 micron CMOS technology, achieving industry leading speed.

The PI3B16210 is configured as 3.3 volt 20-bit, 2-port bus switches designed with a low ON resistance (5Ω) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable (\overline{xOE}) input signal.

The PI3B162210 device has a built-in 25-ohm series resistor to reduce noise resulting from reflections, thus eliminating the need for an external terminating resistor.

Logic Block Diagram



Product Pin Configuration

NC	1	48	$1\overline{OE}$
1A1	2	47	$2\overline{OE}$
1A2	3	46	1B1
1A3	4	45	1B2
1A4	5	44	1B3
1A5	6	43	1B4
1A6	7	42	1B5
GND	8	41	GND
1A7	9	40	1B6
1A8	10	39	1B7
1A9	11	38	1B8
1A10	12	37	1B9
2A1	13	36	1B10
2A2	14	35	2B1
VCC	15	34	2B2
2A3	16	33	2B3
GND	17	32	GND
2A4	18	31	2B4
2A5	19	30	2B5
2A6	20	29	2B6
2A7	21	28	2B7
2A8	22	27	2B8
2A9	23	26	2B9
2A10	24	25	2B10

Truth Table⁽¹⁾

Inputs		Inputs/Outputs	
$1\overline{OE}$	$2\overline{OE}$	1A,1B	2A,2B
L	L	1A = 1B	2A = 2B
L	H	1A = 1B	Z
H	L	Z	2A = 2B
H	H	Z	Z

Product Pin Description

Pin Name	Description
$1\overline{OE}$, $2\overline{OE}$	Bus Enable Inputs (Active LOW)
1A1-1A10, 2A1-2A10	Bus A
1B1 - 1B10, 2B1 - 2B10	Bus B



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Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–0°C to +85°C
Supply Voltage Range	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.0\text{V}$ to 3.6V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5	—	0.8	V
I_i	Input Current	$V_{CC} = \text{Max. } V_{IN} = V_{CC} \text{ or GND}$	—	—	± 1	μA
		$V_{CC} = 0\text{V}, V_{IN} = V_{CC}$	—	—	± 1	μA
I_{OZH}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$	—	—	10	μA
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min}, I_{IN} = -18\text{mA}$	—	–0.7	–1.2	V
R_{ON}	Switch On Resistance ⁽³⁾	$V_{CC} = 3\text{V}, V_{IN} = 0.0\text{V},$	—	5	8	Ω
		$I_{ON} = 24 \text{ mA}, 64\text{mA}$	—	—	—	
		$V_{CC} = 3\text{V}, V_{IN} = 2.4\text{V},$	—	—	15	Ω
		$I_{ON} = 15\text{mA}$	—	—	—	

Capacitance ($T_A = 25^{\circ}\text{C}$, $f = 1 \text{ MHz}$)

Parameters ⁽⁴⁾	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.0	pF
C_{OFF}	A/B Capacitance, Switch OFF	$V_{IN} = 0\text{V}$	8.5	
C_{ON}	A/B Capacitance, Switch ON	$V_{IN} = 5\text{V}$	17.0	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
4. This parameter is determined by device characterization but is not production tested.



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Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{cc}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}			10	μA
ΔI _{cc}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.0V ⁽³⁾			750	μA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max. A and B Pins Open $\overline{\text{BE}}$ = GND Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 3.3V, +25°C ambient.
3. Per TTL driven input (control inputs only); A and B pins do not contribute to I_{cc}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾		Com.		Units
				Min	Max	
t _{PLH}	Propagation Delay ^(2,3)	C _L = 50pF	16210		0.25	ns
t _{PHL}	Ax to Bx, Bx to Ax	R _L = 500Ω	162210		1.25	
t _{PZH}	Bus Enable Time	C _L = 50pF, R _L = 500Ω, R = 500Ω		1	4.5	
t _{PZL}	$\overline{\text{BE}}$ to Ax or Bx					
t _{PHZ}	Bus Disable Time			1	5.0	
t _{PLZ}	$\overline{\text{BE}}$ to Ax or Bx					

Notes:

1. See test circuit and wave forms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.