

PI74LCX245

Fast CMOS 3.3V 8-Bit Bidirectional Transceiver

Product Features

- Functionally compatible with FCT3, LVT, and 74 series 245 families of products
- · Tri-State outputs
- 5V Tolerant inputs and outputs
- 2.0V-3.6V Vcc supply operation
- Balanced sink and source output drives (24 mA)
- · Low ground bounce outputs
- Supports live insertion
- ESD Protection exceeds 2000V, Human Body Model 200V, Machine Model
- Packages available:
 - -20-pin 209-mil wide plastic SSOP (H)
 - -20-pin 173-mil wide plastic TSSOP(L)
 - -20-pin 150-mil wide plastic QSOP (Q)
 - -20-pin 300-mil wide plastic SOIC (S)

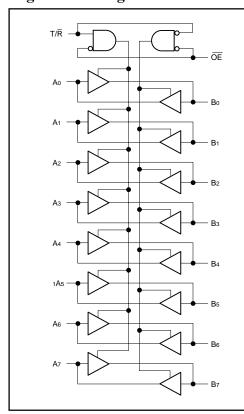
Product Description

Pericom Semiconductor's PI74LCX series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology achieving high speed while maintaining low power operation.

The PI74LCX245 is an 8-bit bidirectional transceiver designed for asynchronous two-way communication between data buses. The transmit/receive input pin (T/\overline{R}) determines the direction of data flow through the bidirectional transceiver. Transmit (active HIGH) enables data from A ports to B ports, and receive (active LOW) from B ports to A ports. The output enable (\overline{OE}) input, when HIGH, disables both A and B ports by placing them in HIGH Z condition.

The PI74LCX245 can be driven from either 3.3V or 5.0V devices allowing this device to be used as a translator in a mixed 3.3V/5.0V system.

Logic Block Diagram



Product Pin Configuration

Product Pin Description

Pin Name	Description
ŌĒ	3-State Output Enable Inputs (Active LOW)
T/R	Direction Control Input
A0-A7	Side A Inputs or 3-State Outputs
В0-В7	Side B Inputs or 3-State Outputs
GND	Ground
Vcc	Power

Truth Table(1)

Inputs ⁽¹⁾		
ŌĒ T/R		Outputs ⁽¹⁾
L	L	Bus B Data to Bus A
L	Н	Bus A Data to Bus B
Н	X	High State Z

Note:

H = High Voltage Level, X = Don't Care,
 L = Low Voltage Level, Z = High Impedance



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only) -0.5V to +7.0V
DC Input Voltage0.5V to +7.0V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Parameter		Min.	Max.	Units
Vcc	Supply Voltage Operating		2.0	3.6	
		Data Retention	1.5	3.6	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage	HIGH or LOW State	0	Vcc	
		TRI-State	0	5.5	
Ioh/Iol	Output Current	Vcc = 3.0V-3.6V	_	±24	4
		Vcc = 2.7V		±12	mA
TA	Free-Air Operating Temperature	Free-Air Operating Temperature		+85	°C
$\Delta t/\Delta V$	Input Edge Rate	V = 0.8V-2.0V, $Vcc = 3.0V$	0	10	ns/V

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DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 2.7\text{V}$ to 3.6V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units	
VIH	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	_	_	
VIL	Input LOW Voltage	Guaranteed Logic LOW L	evel	_	_	0.8	
Voh	Output HIGH Voltage	Vcc = 2.7-3.6	Іон = -0.1mА	Vcc-0.2	_	_	
		Vcc = 2.7	Iон = −12mA	2.2	_	_	
		Vcc = 3.0	Iон = −18mA	2.4	_	_	
			Iон = −24mA	2.2	_	_	V
Vol	Output LOW Voltage	Vcc = 2.7-3.6	Iol = 0.1mA	_	_	0.2	
		Vcc = 2.7	Iol = 12mA	_	_	0.4	
		Vcc = 3.0	Iol = 16mA	_	_	0.4	
			Iol = 24mA	_	_	0.55	
Vik	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$		_	-0.7	-1.2	
Iı	Input Leakage Current	$0 \le V_I \le 5.5V$	Vcc = 2.7-3.6	_	_	±5	
Ioz	Tri-State Output Leakage	$0 \le V_0 \le 5.5V$ VI = VIH or VIL	Vcc = 2.7-3.6	_		±5	
Ioff	Power Down Disable	$V_{CC} = 0V$, V_{IN} or $V_{OUT} \le 5.5V$		_		10 ±10	
Icc	Quiescent Power Supply Current	Vcc = Max.	V _{IN} = GND or V _{CC}	_	0.1	10	μΑ
ΔIcc	Quiescent Power Supply Current TTL Inputs HIGH	Vcc = Max.	$V_{\rm IN} = V_{\rm CC} - 0.6V^{(3)}$	_	_	500	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at Vcc = 3.3V, +25°C ambient.
- 3. Per TTL driven input; all other inputs at Vcc or GND.

Capacitance

Parameters	Description	Test Conditions	Typical	Units
Cin	Input Capacitance	Vcc = Open, VI = 0V or Vcc	7	
Соит	Output Capacitance	$V_{CC} = 3.3V$, $V_I = 0V$ or V_{CC}	8	pF
Срд	Power Dissipation Capacitance	$V_{CC} = 3.3V$, $V_I = 0V$ or V_{CC} , $F = 10$ MHz	25	

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Switching Characteristics over Operating Range

			$V_{\rm CC} = 3.3V \pm 0.3V$		$V_{CC} = 2.7V$		
Parameters	Description	Conditions	Min.	Max.	Min.	Max.	Units
tPLH tPHL	Propagation Delay Dxx to Oxx	$C_L = 50pF$ $R_L = 500\Omega$	1.5	7.0	1.5	8.0	
tpzh tpzl	Output Enable time		1.5	8.5	1.5	9.5	ns
tphz tplz	Output Disable Time		1.5	7.5	1.5	8.5	
tsk(o)	Output Skew ⁽¹⁾		_	1.0	_	_	

Notes:

Dynamic Switching Characteristics (TA = +25°C)

Parameters	Description	Test Conditions ⁽¹⁾	Тур.	Units
VOLP	Dynamic LOW Peak Voltage	VCC = 3.3V, CL = 50pF VIH = 3.3V, VIL = 0V	0.8	V 7
Volv	Dynamic LOW Valley Voltage	Vcc = 3.3V, CL = 50pF Vih = 3.3V, Vil = 0V	0.8	V

Note:

1. Measured with n-1 outputs switching from High-to-Low or Low-to-High. The remaining output is measured in the LOW state.

^{1.} Skew between any two outputs, of the same package, switching in the same direction.

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