
PM45502C

Silicon N-Channel Power MOS FET Module

HITACHI

November 1996

Application

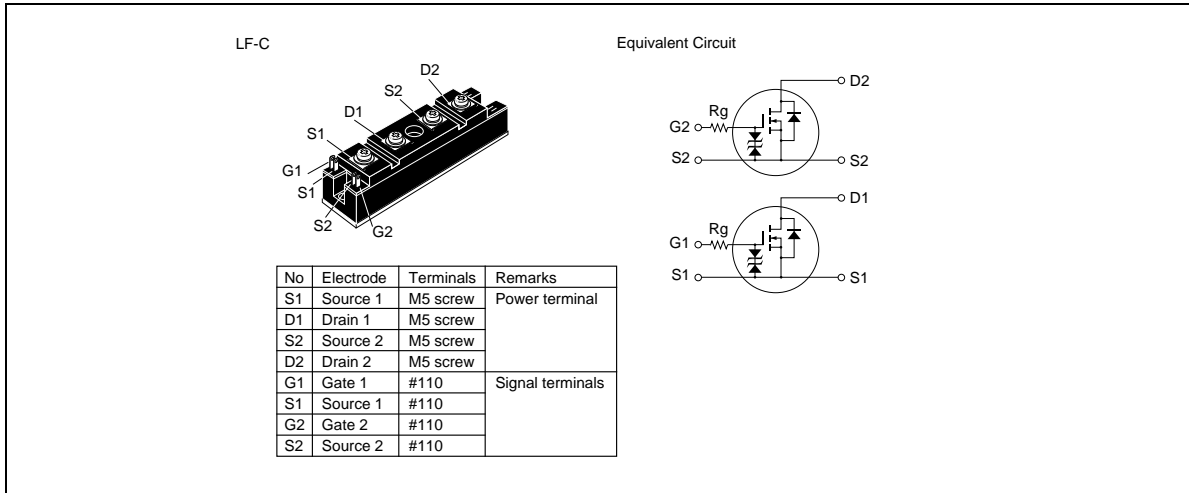
High Speed Power Switching

Features

- Equipped with Power MOS FET
- Low on-resistance
- High speed switching
- Low drive current
- Wide area of safe operation
- Inherent parallel diode between source and drain
- Isolated base from Terminal
- Suitable for motor driver, switching regulator and etc.

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Outline



Absolute Maximum Ratings (Ta = 25°C) (Per FET chip)

Item	Symbol	Rating	Unit
Drain source voltage	V_{DSS}	450	V
Gate source voltage	V_{GSS}	± 20	V
Drain current	I_D	50	A
Drain peak current	$I_{D(peak)}$	100	A
Body to drain diode reverse drain current	I_{DR}	50	A
Body to drain diode reverse drain peak current	$I_{DR(peak)}$	100	A
Channel dissipation	P_{ch}^{*1}	300	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-45 to +125	°C
Insulation dielectric	V_{isol}^{*2}	2000	V

- Notes
1. Value at $T_c = 25^\circ\text{C}$
 2. Base to terminals AC minute

Electrical Characteristics (Ta = 25°C) (Per FET chip)

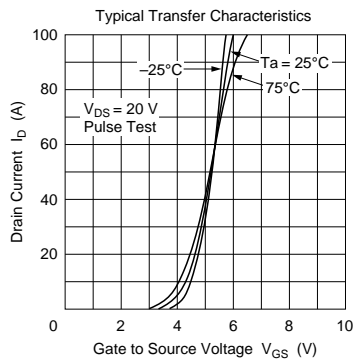
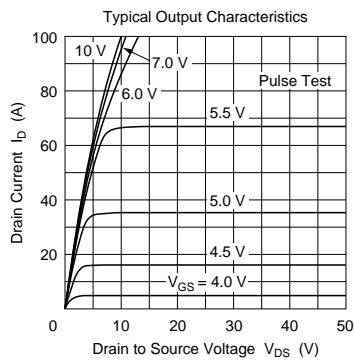
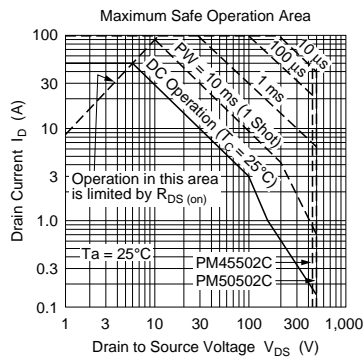
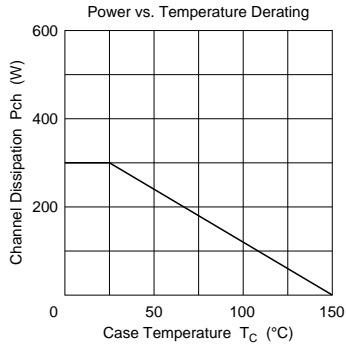
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	450	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	±50	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_G = \pm 100 \text{ μA}, V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	mA	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
Gate to source threshold voltage	$V_{GS(th)}$	1.5	—	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Drain to source saturation voltage	$V_{DS(on)}$	—	2.0	3.0	V	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
Static Drain to source on state resistance	$R_{DS(on)}$	—	0.08	0.12	Ω	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	$ y_{fs} $	25	40	—	S	$I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	—	10250	—	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0$
Output capacitance	Coss	—	3600	—	pF	f = 1 MHz
Reverse transfer capacitance	Crss	—	400	—	pF	
Turn-on delay time	$t_{d(on)}$	—	150	—	ns	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}$
Rise time	t_r	—	700	—	ns	$R_L = 1.2 \text{ Ω}$
Turn-off delay time	$t_{d(off)}$	—	800	—	ns	
Fall time	t_f	—	600	—	ns	
Body to drain diode forward voltage	V_{DF}	—	1.2	—	V	$I_F = 25 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	200	—	ns	$I_F = 25 \text{ A}, V_{GS} = 0$ $diF/dt = 100 \text{ A/μs}$

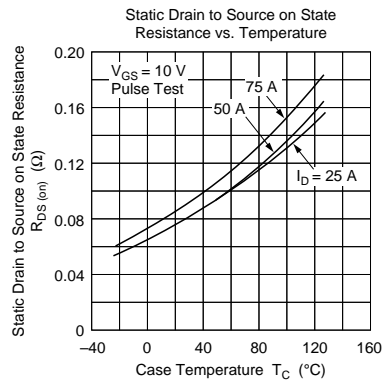
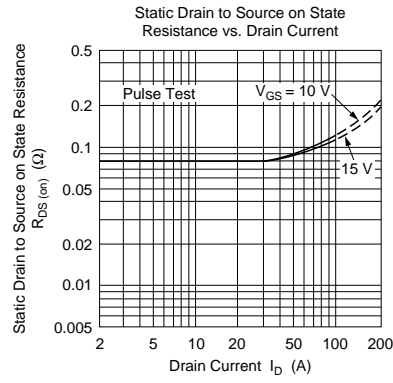
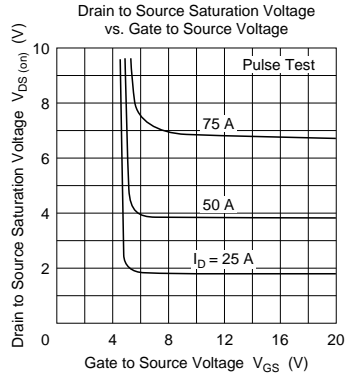
Note 1. Pulse Test

Mechanical Characteristics

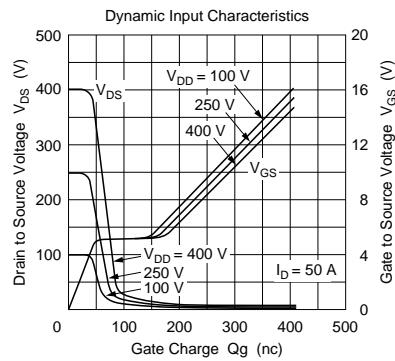
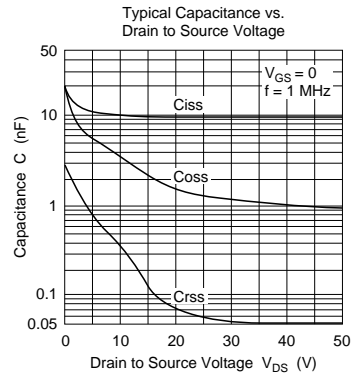
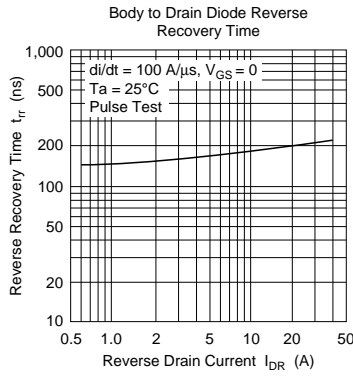
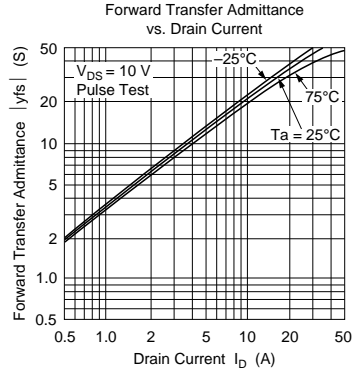
Item	Symbol	Condition	Rating	Unit
Fixing strength	—	Mounting into main-terminal with M5 screw	15 to 20	kg•cm
	—	Mounting into heat sink with M6 screw	20 to 30	kg•cm
Weight	—	Typical value	300	g

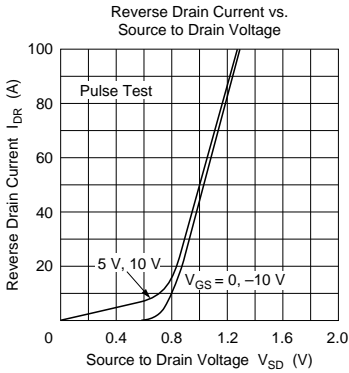
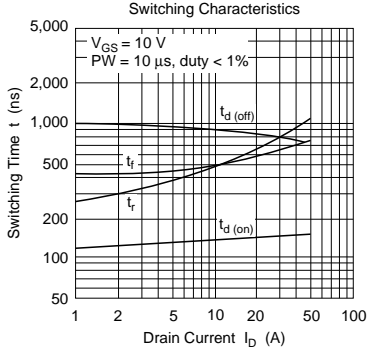
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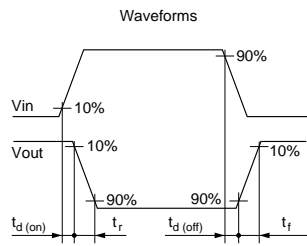
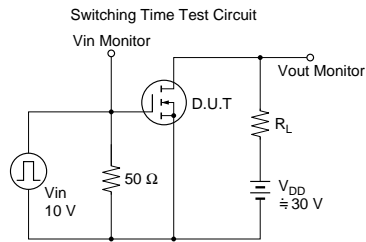
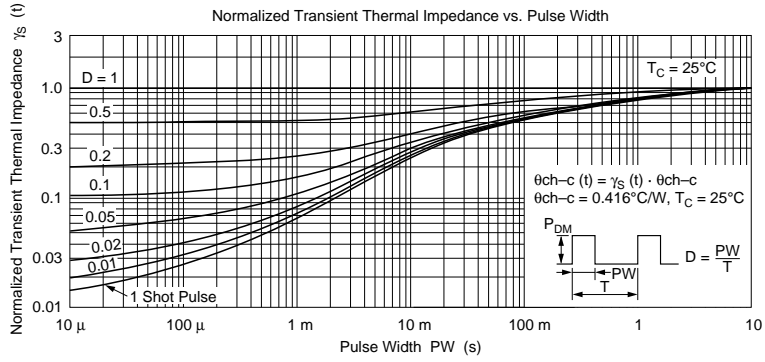


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Hitachi, Ltd.

Semiconductor & IC Div.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan
Tel: Tokyo (03) 3270-2111
Fax: (03) 3270-5109

For further information write to:

Hitachi America, Ltd.
Semiconductor & IC Div.
2000 Sierra Point Parkway
Brisbane, CA. 94005-1835
U S A
Tel: 415-589-8300
Fax: 415-583-4207

Hitachi Europe GmbH
Electronic Components Group
Continental Europe
Dornacher Straße 3
D-85622 Feldkirchen
München
Tel: 089-9 91 80-0
Fax: 089-9 29 30 00

Hitachi Europe Ltd.
Electronic Components Div.
Northern Europe Headquarters
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA
United Kingdom
Tel: 0628-585000
Fax: 0628-778322

Hitachi Asia Pte. Ltd.
16 Collyer Quay #20-00
Hitachi Tower
Singapore 0104
Tel: 535-2100
Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd.
Unit 706, North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon
Hong Kong
Tel: 27359218
Fax: 27306071