DISCRETE SEMICONDUCTORS



PMBFJ111; PMBFJ112; PMBFJ113 N-channel junction FETs

Product specification
File under Discrete Semiconductors, SC07

April 1995







N-channel junction FETs

PMBFJ111; PMBFJ112; PMBFJ113

FEATURES

- High-speed switching
- Interchangeability of drain and source connections
- Low R_{DSon} at zero gate voltage (< 30 Ω for PMBFJ111).

DESCRIPTION

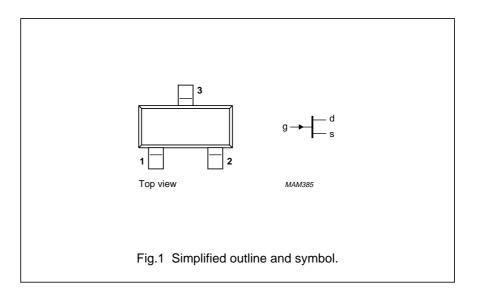
Symmetrical N-channel junction FETs in a surface mount SOT23 envelope. Intended for use in applications such as analog switches, choppers, commutators, multiplexers and thin and thick film hybrids.

PINNING - SOT23

PIN	DESCRIPTION			
1	drain			
2	source			
3	gate			

Note

1. Drain and source are interchangeable.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DS}	drain-source voltage		_	±40	V
V_{GSO}	gate-source voltage		_	-40	V
V_{GDO}	drain-drain voltage		_	-40	V
I _G	forward gate current (DC)		_	50	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C; note 1	_	300	mW
T _{stg}	storage temperature		-65	150	°C
Tj	operating junction temperature		_	150	°C

April 1995 2

Philips Semiconductors Product specification

N-channel junction FETs

PMBFJ111; PMBFJ112; PMBFJ113

THERMAL CHARACTERISTICS

 $T_j = P(R_{th j-t} + R_{th t-s} + R_{th s-a}) + T_{amb}$

SYMBOL	PARAMETER	MAX.	UNIT
R _{th j-a}	from junction to ambient (note 1)	430	K/W
R _{th j-a}	from junction to ambient (note 2)	500	K/W

Notes

- 1. Mounted on a ceramic substrate, $8 \text{ mm} \times 10 \text{ mm} \times 0.7 \text{ mm}$.
- 2. Mounted on printed circuit board.

STATIC CHARACTERISTICS

 $T_j = 25 \, ^{\circ}C$.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
-I _{GSS}	reverse gate current	-V _{GS} = 15 V; V _{DS} = 0	_	1	nA
I _{DSS}	drain current	V _{GS} = 0; V _{DS} = 15 V			
	PMBFJ111		20	_	mA
	PMBFJ112		5	_	
	PMBFJ113		2	_	
-V _{(BR)GSS}	gate-source breakdown voltage	$-I_G = 1 \mu A; V_{DS} = 0$	40	_	V
-V _{GS(off)}	gate-source cut-off voltage	$I_D = 1 \mu A; V_{DS} = 5 V$			
	PMBFJ111		3	10	V
	PMBFJ112		1	5	
	PMBFJ113		0.5	3	
R _{DS(on)}	drain-source on-resistance	$V_{GS} = 0 \text{ V}; V_{DS} = 0.1 \text{ V}$			
	PMBFJ111		_	30	Ω
	PMBFJ112		_	50	
	PMBFJ113		_	100	

April 1995 3

Product specification Philips Semiconductors

N-channel junction FETs

PMBFJ111; PMBFJ112; PMBFJ113

DYNAMIC CHARACTERISTICS

 $T_i = 25 \, ^{\circ}C.$

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
C _{iss}	input capacitance	$V_{DS} = 0$ $-V_{GS} = 10 \text{ V}$ $f = 1 \text{ MHz}$	6	-	pF
		$V_{DS} = 0$ $-V_{GS} = 0$ $f = 1 \text{ MHz}$ $T_{amb} = 25 \text{ °C}$	22	28	pF
C _{rss}	feedback capacitance	$V_{DS} = 0$ $-V_{GS} = 10 \text{ V}$ $f = 1 \text{ MHz}$	3	-	pF
Switching time	es (see Fig.2)		•		
t _r	rise time	note 1	6	_	ns
t _{on}	turn-on time	note 1	13	_	ns
t _f	fall time	note 1	15	_	ns
t _{off}	turn-off time	note 1	35	_	ns

Notes

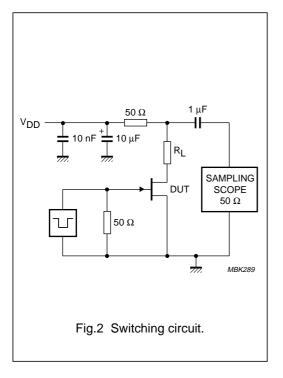
1. Test conditions for switching times are as follows:

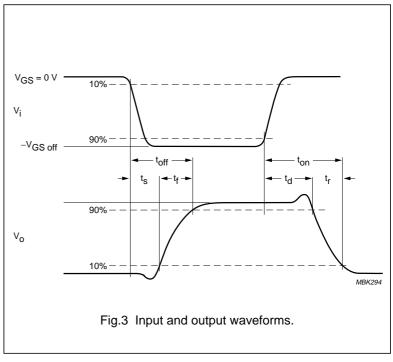
```
\begin{aligned} &\mathsf{V}_{\mathsf{DD}} = 10 \; \mathsf{V}, \; \mathsf{V}_{\mathsf{GS}} = 0 \; \mathsf{to} \; - \! \mathsf{V}_{\mathsf{GS}(\mathsf{off})} \; (\mathsf{all} \; \mathsf{types}); \\ &- \mathsf{V}_{\mathsf{GS}(\mathsf{off})} = 12 \; \mathsf{V}, \; \mathsf{R}_{\mathsf{L}} = 750 \; \Omega \; (\mathsf{PMBFJ111}); \\ &- \mathsf{V}_{\mathsf{GS}(\mathsf{off})} = 7 \; \mathsf{V}, \; \mathsf{R}_{\mathsf{L}} = 1550 \; \Omega \; (\mathsf{PMBFJ112}); \\ &- \mathsf{V}_{\mathsf{GS}(\mathsf{off})} = 5 \; \mathsf{V}, \; \mathsf{R}_{\mathsf{L}} = 3150 \; \Omega \; (\mathsf{PMBFJ113}). \end{aligned}
```

$$-V_{GS(off)} = 12 \text{ V}, R_L = 750 \Omega \text{ (PMBFJ111)};$$

$$-V_{GS(off)} = 7 \text{ V}, R_1 = 1550 \Omega \text{ (PMBFJ112)}$$

$$-V_{GS(off)} = 5 \text{ V}, R_L = 3150 \Omega \text{ (PMBFJ113)}.$$





April 1995 4

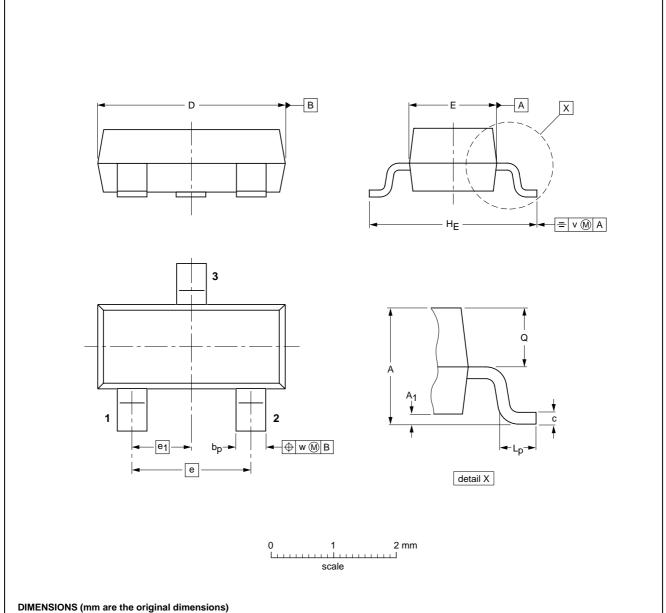
N-channel junction FETs

PMBFJ111; PMBFJ112; PMBFJ113

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



UNIT	Α	A ₁ max.	bp	С	D	E	е	e ₁	HE	Lp	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFERENCES EUROPEAN 100UE D				
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT23						97-02-28

April 1995 5 Philips Semiconductors Product specification

N-channel junction FETs

PMBFJ111; PMBFJ112; PMBFJ113

DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Short-form specification	The data in this specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.			
Limiting values				
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation				

of the device at these or at any other conditions above those given in the Characteristics sections of the specification

Application information

Where application information is given, it is advisory and does not form part of the specification.

is not implied. Exposure to limiting values for extended periods may affect device reliability.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

April 1995 6