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捷多邦,专业PCB打样工厂,24小时加急出货

PR2001 - PR2005

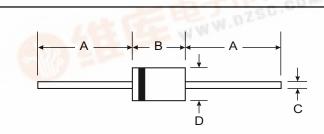
2.0A FAST RECOVERY RECTIFIER

Features

- Diffused Junction
- Fast Switching for High Efficiency
- High Current Capability and Low Forward Voltage Drop
- Surge Overload Rating to 50A Peak
- Low Reverse Leakage Current
- Plastic Material: UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Marking: Type Number
- Weight: 0.4 grams (approx.)



	DO-15						
Dim	Min	Max					
Α	25.40	4 Q. 4V 1					
В	5.50	7.62					
С	0.686	0.889					
D	2.60	3.6					
All Di	All Dimensions in mm						

Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

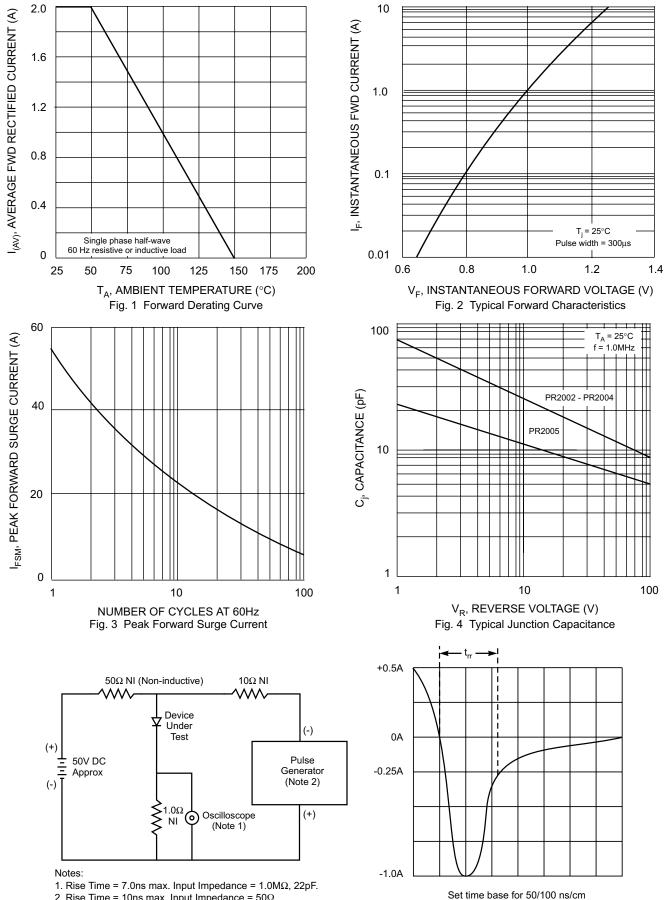
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	PR 2001	PR 2002	PR 2003	PR 2004	PR 2005	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	-13	V _{RRM} V _{RWM} V _R	50	100	200	400	600	V
RMS Reverse Voltage	270 10	V _{R(RMS)}	35	70	140	280	420	V
Average Rectified Output Current (Note 1)	@ T _A = 50°C	Ιο	2.0					
Non-Repetitive Peak Forward Surge Cu 8.3ms Single half sine-wave Superimpo (JEDEC Method)		I _{FSM}	50				A	
Forward Voltage	@ I _F = 2.0A	V _{FM}	1.2				V	
Peak Reverse Current at Rated DC Blocking Voltage	@ $T_A = 25^{\circ}C$ @ $T_A = 100^{\circ}C$	I _{RM}	5.0				50.00	μA
Reverse Recovery Time (Note 3)		t _{rr}	-	1:	50	MALL	250	ns
Typical Junction Capacitance (Note 2)		Cj	190	3	5		15	pF
Typical Thermal Resistance Junction to Ambient		R _{0JA}	50					
Operating and Storage Temperature Range		T _i , T _{STG}	-65 to +150					°C



- 1. Valid provided that leads are maintained at ambient temperature at a distance of 9.5mm from the case.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0 V DC.
- 3. Measured with $I_F = 0.5A$, $I_R = 1.0A$, $I_{rr} = 0.25A$. See figure 5.





2. Rise Time = 10ns max. Input Impedance = 50Ω .

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit