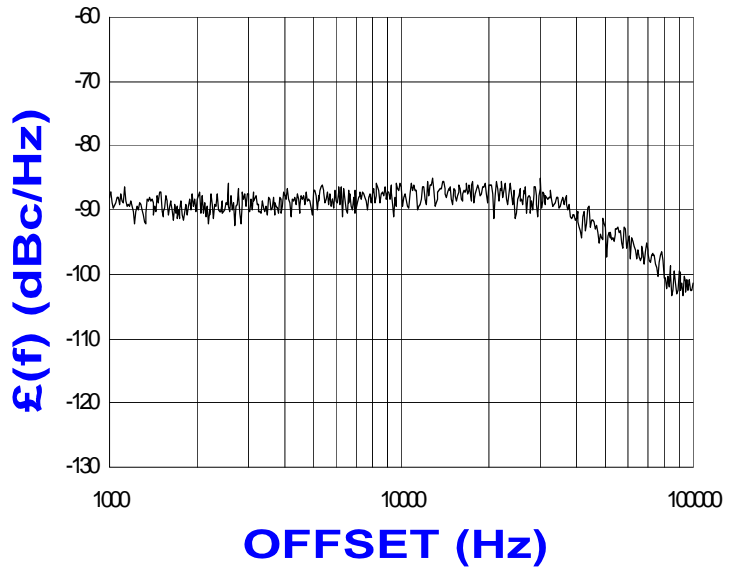


**PHASE NOISE (1 Hz BW, typical)**



**FEATURES**

- Frequency Range: 4890 - 4910 MHz
- Step Size: 1000 KHz
- PLL - Style Package

**APPLICATIONS**

- HIPERLAN
- Microwave Radios
- Wireless Broadband Access

**PERFORMANCE SPECIFICATIONS**

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	4890 - 4910	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-85	dBc/Hz
Harmonic Suppression (2nd, typ.)	-25	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	3±2	dBm
Load Impedance	50	$\Omega$
Step Size	1000	KHz
Charge Pump Output Current	5000	$\mu$ A
Switching Speed (typ., adjacent channel)	0.2	mSec
Startup Lock Time (typ.)	0.5	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	PLL	

**POWER SUPPLY REQUIREMENTS**

Supply Voltage (Vcc, nom.)	3.3	Vdc
Supply Current (Icc, typ.)	35	mA

All specifications are typical unless otherwise noted and subject to change without notice.

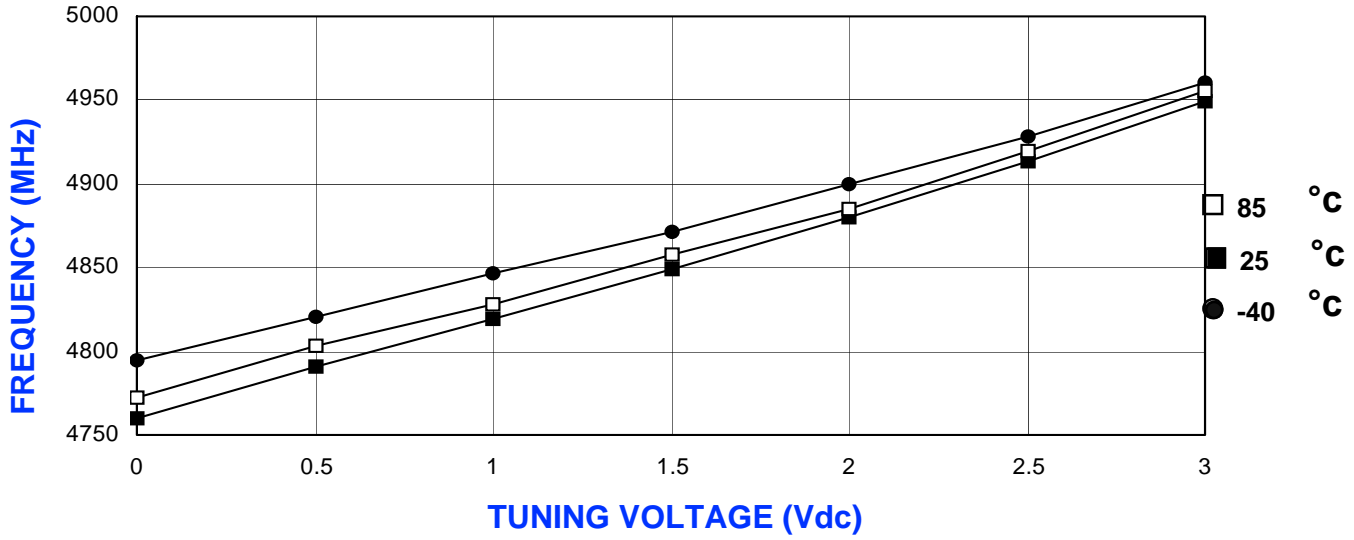
**APPLICATION NOTES**

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals      AN-202 : PLL Functional Description

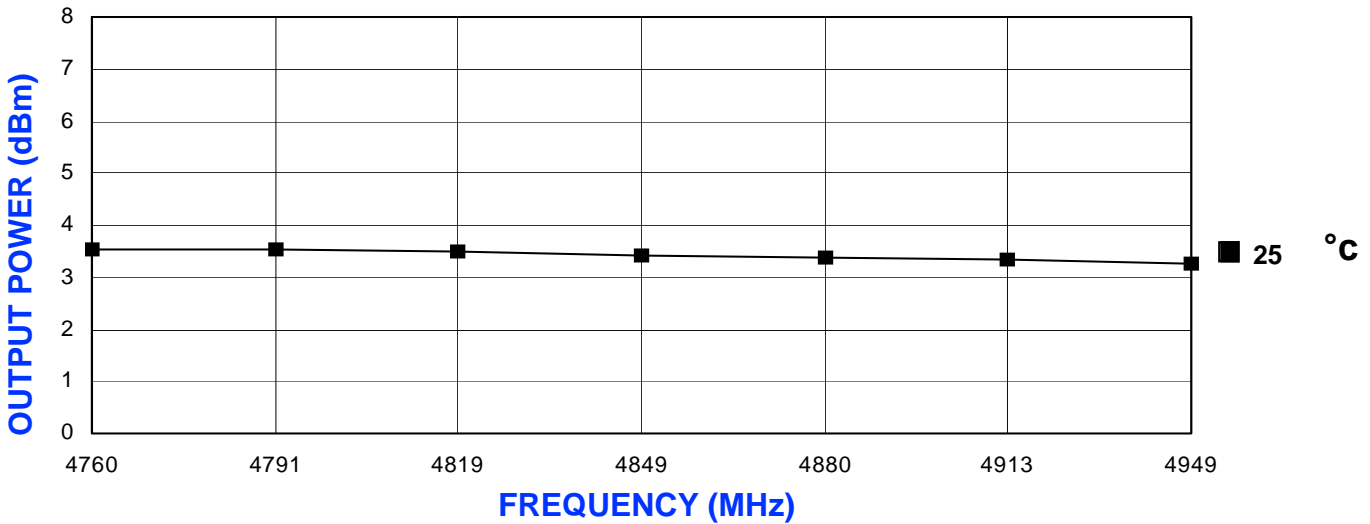
**NOTES:**

Reference Oscillator Signal:  $5 \text{ MHz} < f_{\text{osc}} < 100 \text{ MHz}$       Prescaler: 32  
 Frequency Synthesizer: Analog Devices - ADF4106

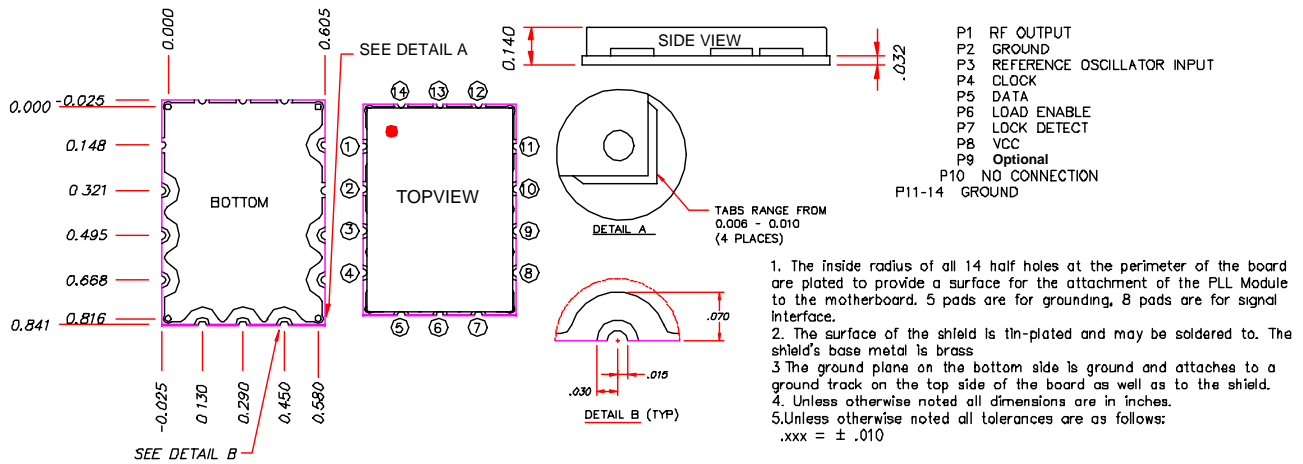
**VCO TUNING CURVE, typ.**



**VCO POWER CURVE, typ.**



**PHYSICAL DIMENSIONS**



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