



查询PSN0930A供应商

Z-Communications, Inc.

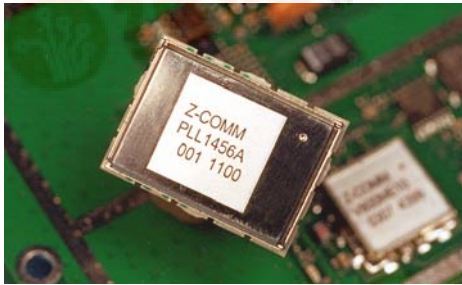
9939 Via Pasar • San Diego, CA 92126
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捷多邦, 专业PCB打样工厂, 24小时加急出货

PSN0930A

PHASE LOCKED LOOP

Rev A1



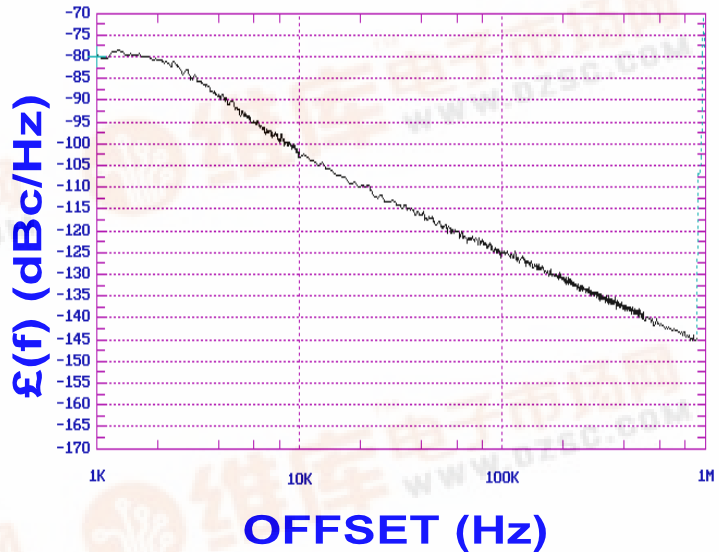
FEATURES

- Frequency Range: 900 - 960 MHz
- Step Size: 100 KHz
- PLL - Style Package

APPLICATIONS

- Basestations
- Mobile Radios
- Satellite Communications

PHASE NOISE (1 Hz BW, typical)



PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	900 - 960	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-100	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	3±2	dBm
Load Impedance	50	Ω
Step Size	100	KHz
Charge Pump Output Current	1000	μA
Switching Speed (typ., adjacent channel)	1	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	40	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

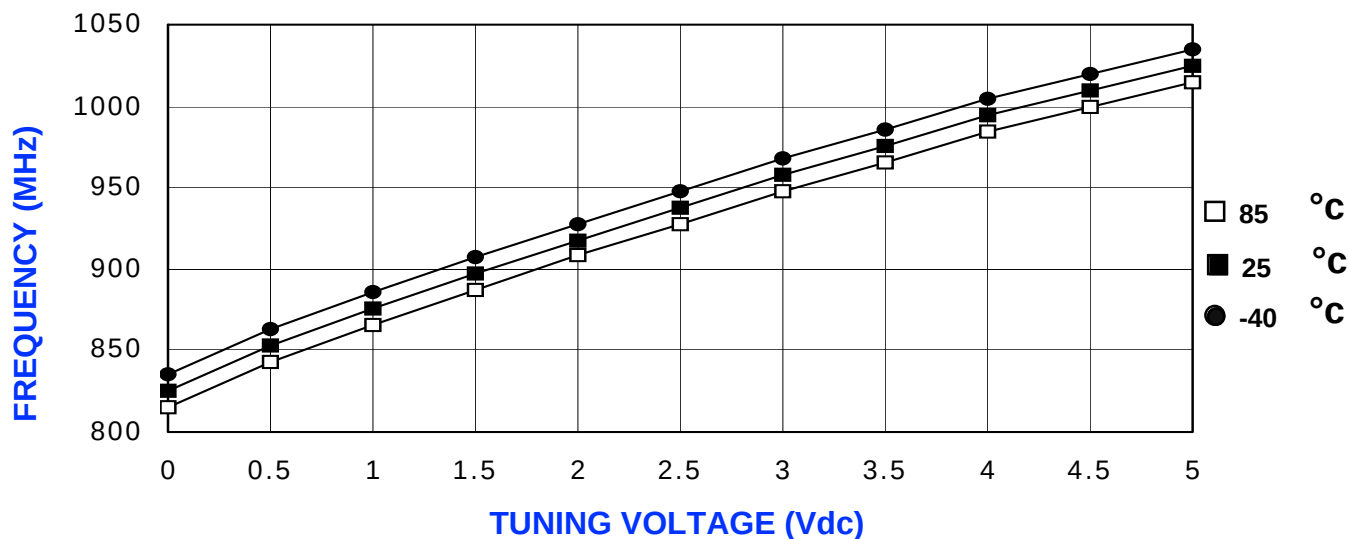
- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

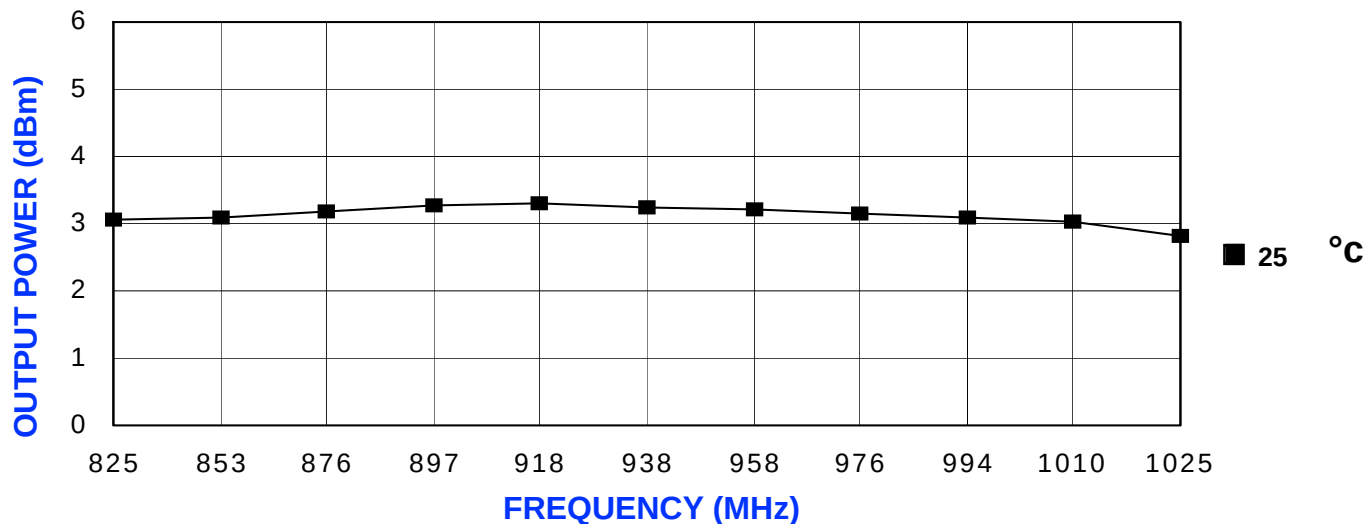
Reference Oscillator: 5 MHz < f_{osc} < 40 MHz

Frequency Synthesizer: National Semiconductor - LMX2316

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

