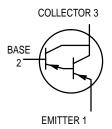
One Watt Darlington Transistors PNP Silicon



MAXIMUM RATINGS

Rating	Symbol	MPSW63 MPSW64	Unit
Collector-Emitter Voltage	VCES	-30	Vdc
Collector-Base Voltage	V _{CBO}	-30	Vdc
Emitter-Base Voltage	V _{EBO}	-10	Vdc
Collector Current — Continuous	IC	-500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	1.0 8.0	Watt mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (I _C = –100 μAdc, V _{BE} = 0)	V(BR)CES	-30	_	Vdc
Collector Cutoff Current (VCB = -30 Vdc, IE = 0)	ICBO	_	-100	nAdc
Emitter Cutoff Current (VEB = -10 Vdc, I _C = 0)	I _{EBO}	_	-100	nAdc

MPSW63 MPSW64*

*Motorola Preferred Device



Preferred devices are Motorola recommended choices for future use and best overall value.



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)		•			
DC Current Gain (I _C = -10 mAdc, V _{CE} = -5.0 Vdc) (I _C = -100 mAdc, V _{CE} = -5.0 Vdc)	MPSW63 MPSW64 MPSW63	hFE	5,000 10,000 10,000	_ _ _	_
Collector–Emitter Saturation Voltage (IC = -100 mAdc, IB = -0.1 mAdc)	MPSW64	VCE(sat)	20,000	-1.5	Vdc
Base–Emitter On Voltage (I _C = -100 mAdc, V _{CE} = -5.0 Vdc)		VBE(on)	_	-2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS		•			
Current-Gain — Bandwidth Product(2) (I _C = -10 mAdc, V _{CE} = -5.0 Vdc, f = 100 MHz)		fΤ	125	_	MHz

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

TYPICAL ELECTRICAL CHARACTERISTICS

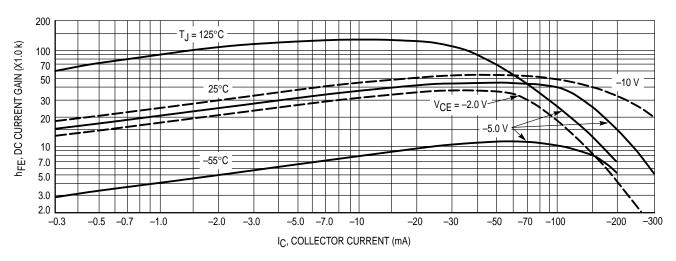


Figure 1. DC Current Gain

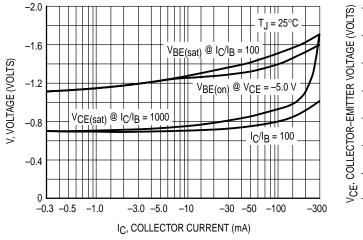


Figure 2. "ON" Voltage

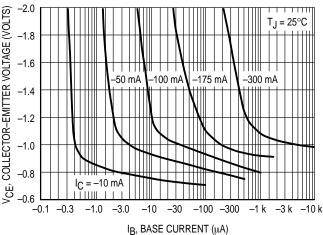


Figure 3. Collector Saturation Region

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

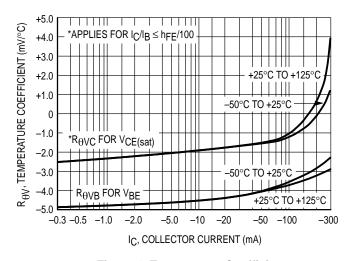


Figure 4. Temperature Coefficients

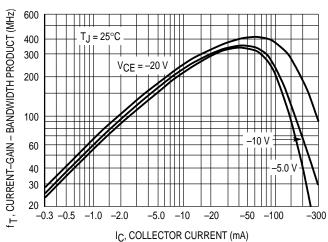


Figure 5. Current-Gain — Bandwidth Product

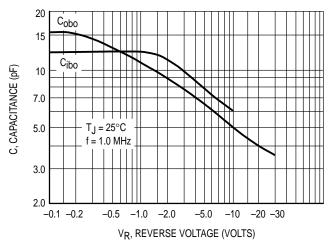


Figure 6. Capacitance

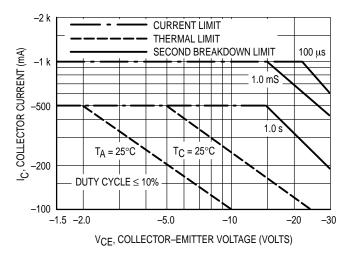
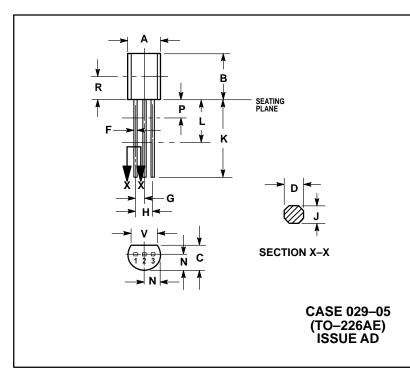


Figure 7. Active Region, Safe Operating Area

PACKAGE DIMENSIONS



- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 714.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	INCHES MILLIMETER		IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	
V	0.135		3.43	

STYLE 1:

PIN 1. EMITTER 2. BASE 3. COLLECTOR

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