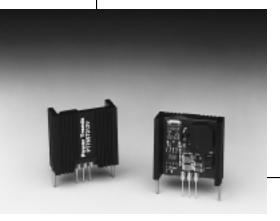
# **Product Selector Guide**

Revised 5/15/98

**Application Notes** 

**Mechanical Outline** 

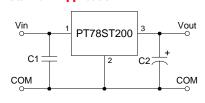
#### 12V 2 AMP POSITIVE STEP-DOWN **INTEGRATED SWITCHING REGULATOR**



- High Efficiency > 87%
- Wide Input Range
- Aluminum Heatsink for Applications with Airflow
- Self-Contained Inductor
- **Short Circuit Protection**
- Over-Temperature Protection
- Pin Compatible with Linear 3-Terminal, "78" Series Regulators
- Small Footprint

The Power Trends' PT78ST200 is a new 3-terminal Integrated Switching Regulator (ISR) that can supply up to 24 watts of regulated 12V power. With a surge capability of 3 Amps and an output voltage that is laser trimmed, it is ideal for inductive load applications such as disk drive motors.

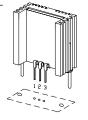
### **Standard Application**



 $C_1$  = Optional 1 $\mu$ F ceramic C<sub>2</sub> = Required 100µF electrolytic

#### **Pin-Out Information**

Pin	Function
1	$V_{in}$
2	GND
3	$V_{out}$



SUGGESTED BOARD LAYOUT Pkg Style 600

**Ordering Information** 

PT78ST2 | XX | | Y Output Voltage Package Suffix **12** = 12.0 Volts V = Vertical Mount

#### **Specifications**

Characteristics (T <sub>a</sub> = 25°C unless noted)	Symbols	Conditions	PT78ST200 SERIES			
			Min	Тур	Max	Units
Output Current	$I_{o}$	Over V <sub>in</sub> range With forced air cooling	0.1*	_	2.0	A
Short Circuit Current	$I_{sc}$	V <sub>in</sub> = V <sub>in</sub> min	_	5.0	_	Apk
Input Voltage Range	$ m V_{in}$	$0.1 \le I_o \le 2.0A$	16	_	28	V
Output Voltage Tolerance	$\Delta  m V_o$	Over $V_{in}$ range, $I_{o}$ = 2.0A $T_{a}$ = 0°C to +60°C	_	±1.0	±2.0	%Vo
Line Regulation	Reg <sub>line</sub>	Over V <sub>in</sub> range	_	±0.4	±0.8	$%V_{o}$
Load Regulation	Regload	$0.1 \le I_o \le 2.0A$		±0.2	±0.4	$%V_{o}$
Vo Ripple/Noise	$V_n$	$V_{in}$ =17V, $I_{o}$ =2.0A, $V_{o}$ =12V	_	120	_	$\mathrm{mV_{pp}}$
Transient Response (with 100μF output cap)	t <sub>tr</sub>	50% load change V <sub>o</sub> over/undershoot		100 5.0	_	μSec %V <sub>o</sub>
Efficiency	η	$V_{in}=17V, I_{o}=2.0A$	_	87	_	%
Switching Frequency	$f_{o}$	Over V <sub>in</sub> and I <sub>o</sub> ranges	0.95	1.0	1.05	MHz
Absolute Maximum Operating Temperature Range	$T_a$	_	-40	_	+65	°C
Recommended Operating Temperature Range	$T_a$	Free Air Convection, (40-60LFM) at V <sub>in</sub> = 24V, I <sub>o</sub> =2A	-40		+55**	°C
Thermal Resistance	$\theta_{ja}$	Free Air Convection, (40-60LFM)	_	35	_	°C/W
Storage Temperature	$T_s$	_	-40	_	+125	°C
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3	_	500	_	G's
Mechanical Vibration	_	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, Soldered in a PC board	_	10	_	G's
Weight	_	_		11		Gram

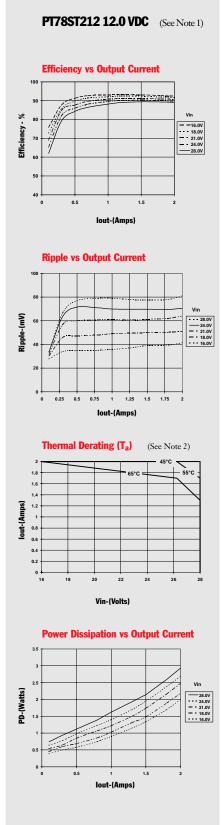
\*ISR will operate down to no load with reduced specifications.

\*\*See Thermal Derating chart.

 $\textbf{Note:}\ The\ PT78ST200\ Series\ requires\ a\ 100\mu F\ electrolytic\ or\ tantalum\ output\ capacitor\ for\ proper\ operation\ in\ all\ applications.$ 

DATA SHEETS

#### CHARACTERISTIC DATA



Note 1: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note 2: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)

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