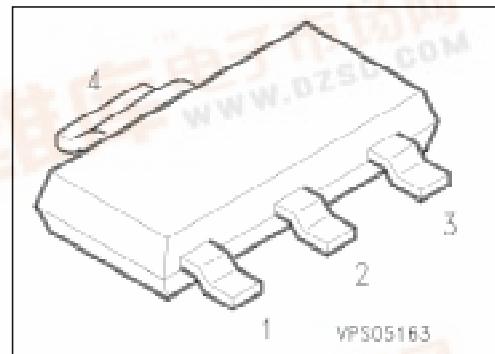


SIEMENS**PNP Silicon High-Voltage Transistors****PZTA 92
PZTA 93**

- High breakdown voltage
- Low collector-emitter saturation voltage
- Complementary types: PZTA 42, PZTA 43 (NPN)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration	Package ¹⁾
			1 2 3 4	
PZTA 92	PZTA 92	Q62702-Z2037	B	C
PZTA 93	PZTA 93	Q62702-Z2038	E	C

Maximum Ratings

Parameter	Symbol	Values		Unit
		PZTA 92	PZTA 93	
Collector-emitter voltage	V_{CE0}	300	200	V
Collector-base voltage	V_{CBO}	300	200	
Emitter-base voltage	V_{EBO}		5	
Collector current	I_C		500	mA
Base current	I_B		100	
Total power dissipation, $T_s = 124^\circ\text{C}$	P_{tot}		1.5	W
Junction temperature	T_j		150	$^\circ\text{C}$
Storage temperature range	T_{stg}	$-65 \dots +150$		

Thermal Resistance

Junction - ambient ²⁾	$R_{th JA}$	≤ 72	K/W
Junction - soldering point	$R_{th JS}$	≤ 17	

¹⁾ For detailed information see chapter Package Outlines.²⁾ Package mounted on epoxy pcb 40 mm \times 40 mm \times 1.5 mm/6 cm² Cu.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

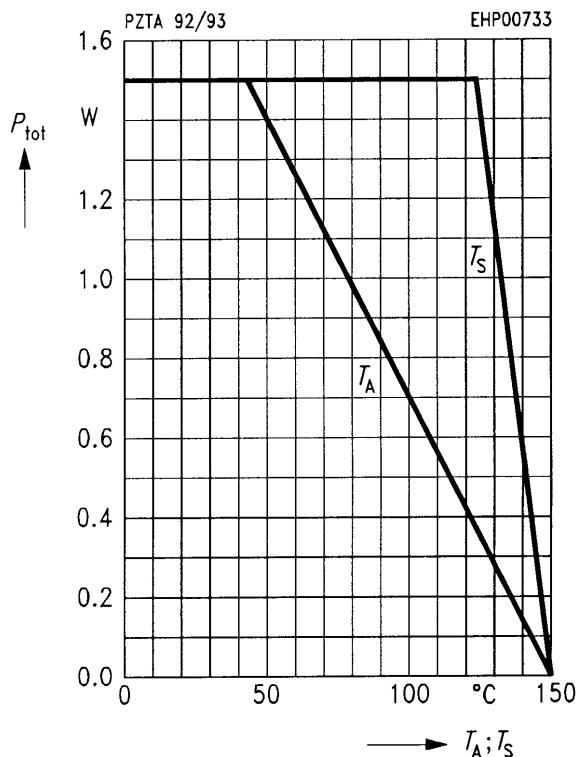
Collector-emitter breakdown voltage $I_C = 1 \text{ mA}, I_B = 0$	$V_{(\text{BR})\text{CE}0}$	300 200	— —	— —	V
Collector-base breakdown voltage $I_C = 100 \mu\text{A}, I_B = 0$	$V_{(\text{BR})\text{CB}0}$	300 200	— —	— —	
Emitter-base breakdown voltage $I_E = 100 \mu\text{A}, I_C = 0$	$V_{(\text{BR})\text{EB}0}$	5	—	—	
Collector-base cutoff current $V_{CB} = 200 \text{ V}$	I_{CB0}	—	—	250	nA
$V_{CB} = 160 \text{ V}$		—	—	250	nA
$V_{CB} = 200 \text{ V}, T_A = 150^\circ\text{C}$	PZTA 92	—	—	20	μA
$V_{CB} = 160 \text{ V}, T_A = 150^\circ\text{C}$	PZTA 93	—	—	20	μA
Emitter-base cutoff current $V_{EB} = 3 \text{ V}, I_C = 0$	I_{EB0}	—	—	100	nA
DC current gain ¹⁾ $I_C = 1 \text{ mA}, V_{CE} = 10 \text{ V}$	h_{FE}	25 40 25	— — —	— — —	—
$I_C = 10 \text{ mA}, V_{CE} = 10 \text{ V}$					
$I_C = 30 \text{ mA}, V_{CE} = 10 \text{ V}$					
Collector-emitter saturation voltage ¹⁾ $I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$	$V_{CE\text{sat}}$	— —	— —	0.5 0.4	V
Base-emitter saturation voltage ¹⁾ $I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$	$V_{BE\text{sat}}$	—	—	0.9	

AC characteristics

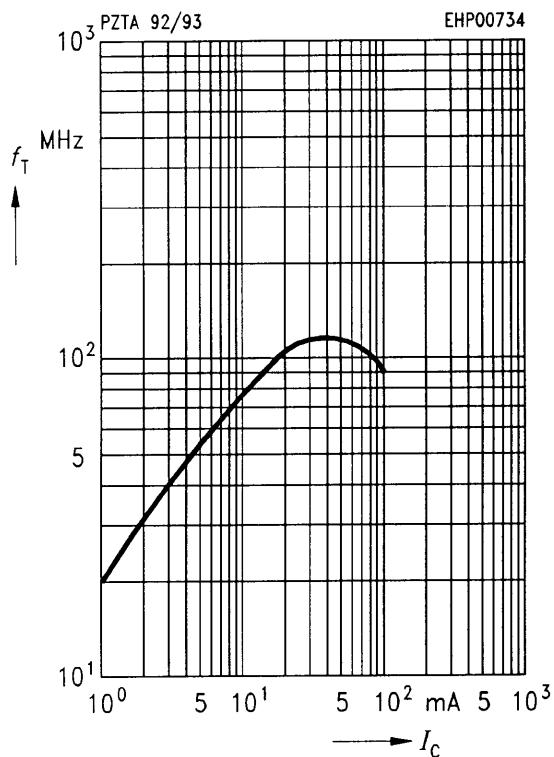
Transition frequency $I_C = 20 \text{ mA}, V_{CE} = 10 \text{ V}, f = 100 \text{ MHz}$	f	—	100	—	MHz
Collector-base capacitance $V_{CB} = 20 \text{ V}, f = 1 \text{ MHz}$	C_{cbo}	— —	— —	6 8	pF

¹⁾ Pulse test conditions: $t \leq 300 \mu\text{s}$, $D = 2\%$.

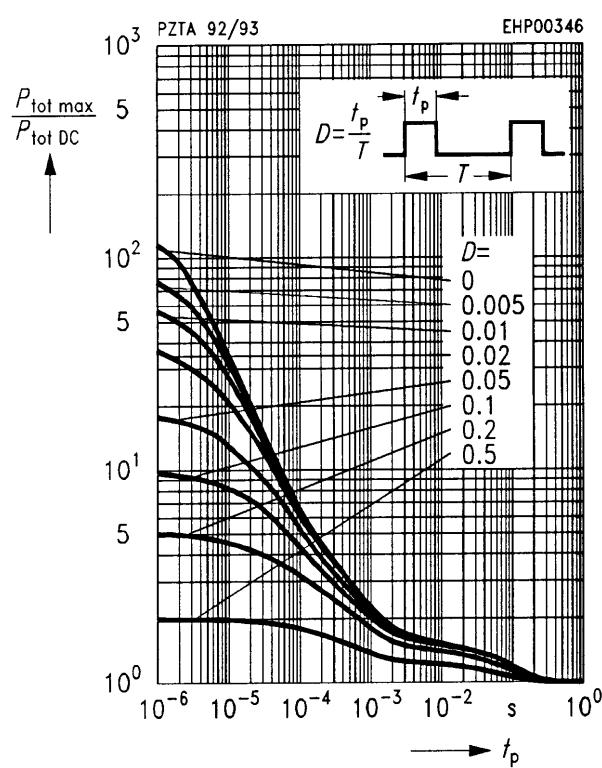
Total power dissipation $P_{\text{tot}} = f(T_A^*; T_S)$
 * Package mounted on epoxy



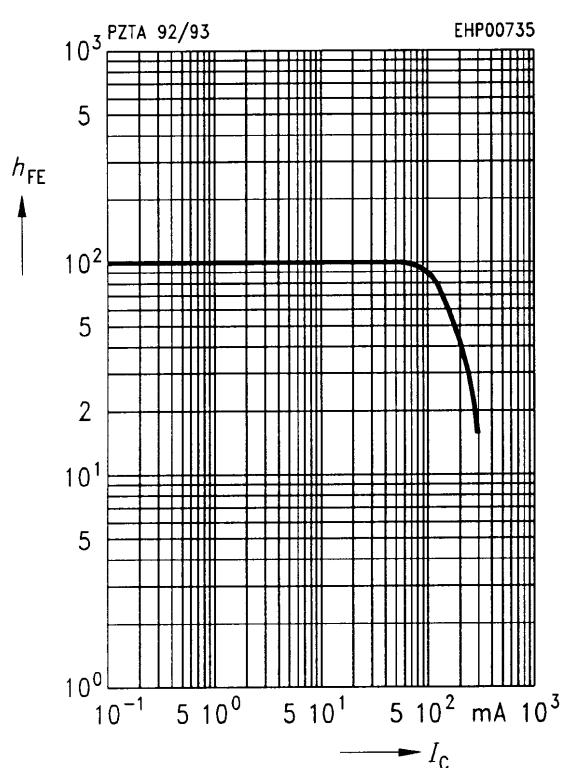
Transition frequency $f_T = f(I_C)$
 $V_{\text{CE}} = 10\text{ V}, f = 100\text{ MHz}$



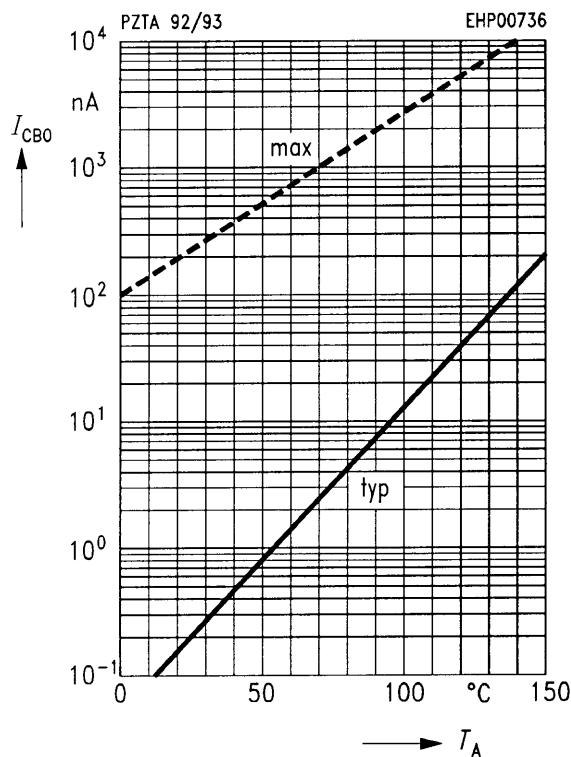
Permissible pulse load $P_{\text{tot max}} / P_{\text{tot DC}} = f(t_p)$



DC current gain $h_{\text{FE}} = f(I_C)$
 $V_{\text{CE}} = 10\text{ V}$



Collector cutoff current $I_{CBO} = f(T_A)$
 $V_{CB} = 160 \text{ V}$



Collector current $I_C = f(V_{BE})$
 $V_{CE} = 10 \text{ V}$

