



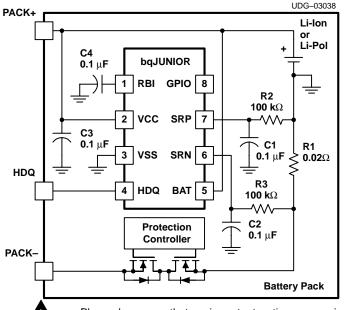
SLUS556 - FEBRUARY 2003

SINGLE-CELL LI-ION AND LI-POL BATTERY GAS GAUGE IC FOR PORTABLE APPLICATIONS (bqJUNIOR™)

FEATURES

- Reports Accurate Time-to-Empty in Li-Ion and Li-Pol Cells, No System Processor Calculations Needed
- Reports Cell Temperature, Voltage and Average Current
- High-Accuracy Coulometric Charge and Discharge Current Integration with Automatic Offset Cancellation
- Requires No Offset Calibration
- Programmable Input/Output Port
- Internal Time-Base Eliminates External Crystal Oscillator
- Four Automatic Low-Power Operating Modes
 - Active: < 100 μASleep: < 5 μA
 - Ship: < 2 μA
 - Hibernate: < 500 nA
- Small 8-Pin TSSOP Package

TYPICAL APPLICATION



APPLICATIONS

- PDAs
- Smart Phones
- MP3 Players
- Digital Cameras
- Internet Appliances
- Handheld Devices

DESCRIPTION

The bqJUNIOR series are highly accurate standalone single-cell Li-Ion and Li-Pol battery capacity monitoring and reporting devices targeted at space limited portable applications. The device monitors a voltage drop across a small current sense resistor connected in series with the battery to determine charge and discharge activity of the battery. Compensations for battery temperature, self-discharge, and rate of discharge are applied to the charge counter to provide available time-to-empty and time-to-full information across a wide range of operating conditions. Battery capacity is automatically recalibrated, or learned, in the course of a discharge cycle from full to empty. Internal registers include available time-to-empty, cell temperature and voltage, average current, and other status and control registers.

The bqJUNIOR can operate directly from single-cell Li-Ion and Li-Pol batteries and communicates to the system over a simple one-wire bi-directional serial interface. The 5-kbits/s HDQ bus interfaces reduces communications overhead in the external microcontroller.

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ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range unless otherwise noted(1) (2)

		bq27000 bq27010
Supply voltage rang	e, V _{CC} (all with respect to V _{SS})	–0.3 V to 7.0 V
Input voltage range	at SRP, SRN, RBI, and BAT (all with respect to V _{SS})	-0.3 V to V _{CC} + 0.3 V
Input voltage	HDQ, GPIO (with respect to VSS)	–0.3 V to 7.0 V
	GPIO (with respect to VSS) during EEPROM programming only	-0.3 V to 22.0 V
Output sink current a	t GPIO, HDQ	5 mA
Operating free-air temperature range, T _A		−20°C to 70°C
Storage temperature range, T _{Stg}		−65°C to 150°C
Junction temperature range, T _J		-40°C to 125°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds		300°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to Absolute Maximum Rated conditions for extended periods may affect device reliability

RECOMMENDED OPERATING CONDITIONS

	MIN	NOM MA	X	UNIT
Supply voltage, V _{CC}	2.6	4.5	5	V
Operating free-air temperature, T _J	-20	70	,	°C

ELECTRICAL CHARACTERISTICS

 $T_J = -20^{\circ}\text{C}$ to 70°C , $T_J = T_{A_1}$ 2.6 V \leq V_{CC} \leq 4.5 V (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP MAX	UNIT	
INPUT CURRENTS						
ICC(VCC)	Input current, V _{CC}	VCC > VCC(min)		100		
ICC(SLP)	Sleep current			5	μΑ	
ICC(SHP)	Ship current			2		
ICC(POR)	Hibernate current	0 V < V _{CC} < V _(POR)		500	nA	
	EEPROM programming current	VPROGRAM = 21 V		15	mA	
	RBI current	RBI pin only, VCC < V(POR)		20	nA	
V(POR)	POR threshold		2.0	2.5		
	RBI data retention voltage		1.2		V	
	Input impedance on BAT pin		10		140	
	Input impedance on SRR, SRN pins		10		ΜΩ	
VOLTAGE	MEASUREMENT	•				
	Measurement range	$V_{CC} = V_{I(BAT)}$	2.6	4.5	V	
	Reported voltage resolution			2.7		
	Reported accuracy		-25	25	mV	
	Voltage update time			2	s	
VOLTAGE	MEASUREMENT		-			
	Reported temperature resolution			0.25	214	
	Reported temperature accuracy		-3	3	°K	
	Temperature update time			2	S	



ELECTRICAL CHARACTERISTICS (continued)

 $T_J = -20^{\circ}\text{C}$ to 70°C , $T_J = T_{A,}$ 2.6 V \leq V_{CC} \leq 4.5 V (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP MAX	UNIT		
EEPROM PROGRAMMING VOLTAGE							
^t RISE	Programming voltage rise time		0.5	1.5			
	Programming voltage high time		10	100	ms		
^t FALL	Programming voltage fall time		0.5	1.5	1		
	Programming voltage	Applied to GPIO pin	20	22	V		
IO PORT ((GPIO) AND SERIAL INTERFACE (HDQ	(1)	•				
		V _{CC} < 4.2 V	1.7				
VIH	High-level input voltage	V _{CC} > 4.2 V	1.9		V		
V _{IL}	Low-level input voltage			0.7			
loL	Low-level output current	V _{OL} > 0.4 V		1	mA		
	RD SERIAL COMMUNICATION (HDQ) T	IMING ⁽¹⁾	•				
T _(B)	Break timing		190				
T _(BR)	Break recovery time		40				
T _(CYCH)	Host bit window timing		190				
T _(HW1)	Host sends 1 time		5	50			
T _(HW0)	Host sends 0 time		100	145	μs		
T _(RSPS)	bq27000 to host response time		190	320			
T _(CYCB)	bq27000 bit window timing		190	250	1		
T _(DW1)	bq27000 sends 1 time		32	50	1		
T _(DW0)	bq27000 sends 0 time		80	145			

(1) See Figure 1.

The following timing diagrams describe break and break recovery timing (a), host transmitted bit timing (b), bqJUNIOR transmitted bit timing (c), and bqJUNIOR to host response timing (d).

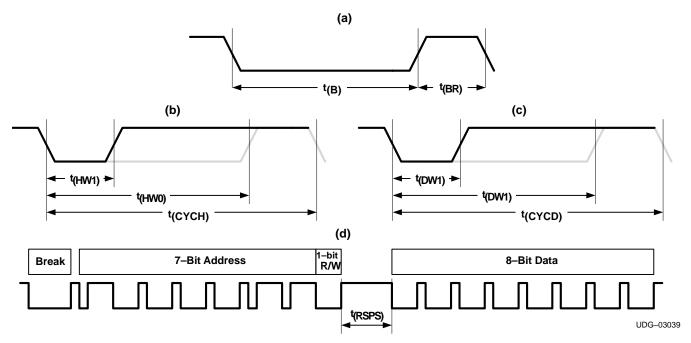


Figure 1. HDQ Bit Timing Diagrams



PIN ASSIGNMENTS

TERMIN	ERMINAL		DECORPTION
NAME	NO.	1/0	DESCRIPTION
BAT	5	I	Battery voltage sense input
GPIO	8	I/O	General-purpose input/output port
HDQ	4	I/O	Single-wire HDQ serial interface
RBI	1	I	Register back-up input
SRN	6	I	Current sense input (positive)
SRP	7	I	Current sense input (negative)
VCC	2	I	V _{CC} supply input
VSS	3	I	Ground input

PW PACKAGE (TOP VIEW)

кві □	10 8	☐ GPIO
vcc □	2 7	□□ SRP
VSS 🞞	3 6	□□ SRN
HDQ \Box	4 5	□□ BAT

AVAILABLE OPTIONS

TA	ADDITIONAL FUNCTIONS	PACKAGED DEVICES	MARKINGS
-20°C to 70°C	TTECP, AP, SAE, MLTTE, MLI, STTE, SI, ARTTE, and AR	bq27000PW	
		bq27010PW	

[†] The PW package is available taped and reeled. Add R suffix to device type (e.g. bq27000PWR) to order quantities of 2,000 devices per reel.

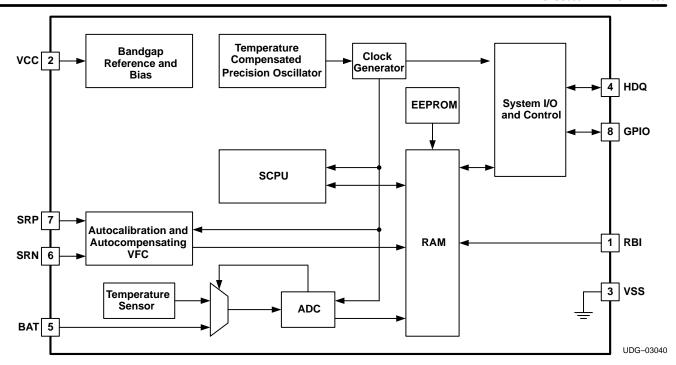
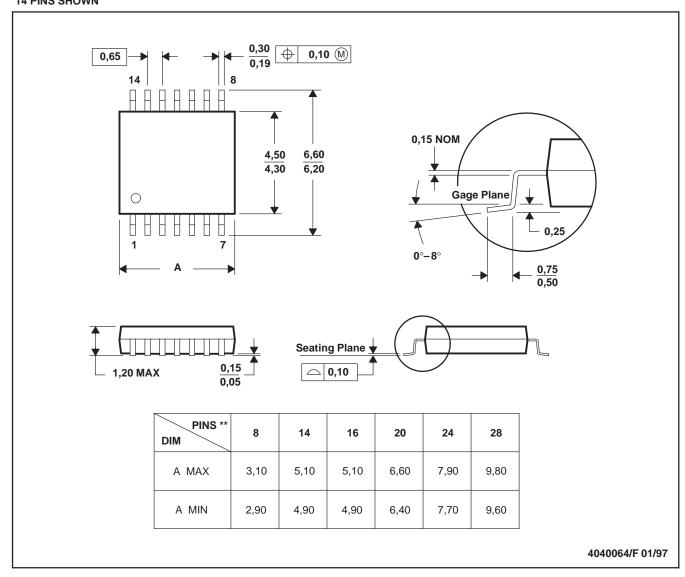


Figure 2. Functional Block Diagram

PW (R-PDSO-G**)

14 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

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