

HEXFET® POWER MOSFET

IRFN450

N-CHANNEL

500 Volt, 0.415Ω HEXFET

HEXFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low on-state resistance combined with high transconductance.

HEXFET transistors also feature all of the well-establish advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, and high energy pulse circuits.

The Surface Mount Device (SMD-1) package represents another step in the continual evolution of surface mount technology. The SMD-1 will give designers the extra flexibility they need to increase circuit board density. International Rectifier has engineered the SMD-1 package to meet the specific needs of the power market by increasing the size of the termination pads, thereby enhancing thermal and electrical performance.

Product Summary

Part Number	BVDSS	RDS(on)	lb
IRFN450	500V	0.415Ω	12A

Features:

- Avalanche Energy Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Light-weight

Absolute Maximum Ratings

	Parameter	IRFN450	Units
ID @ VGS = 10V, TC = 25°C Continuous Drain Current		12.0	
ID @ VGS = 10V, TC = 100°C Continuous Drain Current		8.0	A
IDM	Pulsed Drain Current ①	48	
P _D @ T _C = 25°C	Max. Power Dissipation	150	W
	Linear Derating Factor	1.2	W/K ®
VGS	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	750	mJ
IAR	Avalanche Current ①	12.0	А
EAR	Repetitive Avalanche Energy ①	15.0	mJ
dv/dt	Peak Diode Recovery dv/dt ®	3.5	V/ns
TJ	Operating Junction	-55 to 150	
TSTG	Storage Temperature Range		°C
	Package Mounting Surface Temperature	300 (for 5 seconds)	
	Weight	2.6 (typical)	g

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
BVDSS	Drain-to-Source Breakdown Voltage	500	_	_	V	VGS = 0V, ID = 1.0mA	
ΔBV _{DSS} /ΔT _J	Temperature Coefficient of Breakdown Voltage	_	0.78	_	V/°C	Reference to 25°C, I _D = 1.0mA	
RDS(on)	Static Drain-to-Source		_	0.415		VGS = 10V, ID = 8A 4	
	On-State Resistance	_	_	0.515	Ω	VGS = 10V, ID = 12A	
VGS(th)	Gate Threshold Voltage	2.0	_	4.0	V	VDS = VGS, ID = 250μA	
gfs	Forward Transconductance	5.5	_	_	S (U)	VDS > 15V, IDS = 8A @	
IDSS	Zero Gate Voltage Drain Current	_	_	25		VDS = 0.8 x Max Rating, VGS = 0V	
		_	_	250	μΑ	VDS = 0.8 x Max Rating	
						VGS = 0V, TJ = 125°C	
IGSS	Gate-to-Source Leakage Forward		_	100	nA	VGS = 20V	
IGSS	Gate-to-Source Leakage Reverse	_	_	-100	11/	VGS = -20V	
Qg	Total Gate Charge	55	_	120		VGS =10V, ID = 12A	
Qgs	Gate-to-Source Charge	5.0	_	19	nC	V _{DS} = Max. Rating x 0.5	
Qgd	Gate-to-Drain ("Miller") Charge	27	_	70		see figures 6 and 13	
td(on)	Turn-On Delay Time		_	35		VDD = 250V, ID = 12A,	
tr	Rise Time	_	_	190	ns	$RG = 2.35\Omega$, $VGS = 10V$	
td(off)	Turn-Off Delay Time	_	_	170	115		
tf	Fall Time	_	_	130		see figure 10	
LD	Internal Drain Inductance	_	2.0	_	nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die. Modified MOSFET symbol showing the internal inductances.	
LS	Internal Source Inductance	_	6.5	_	NΗ	Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.	
C _{iss}	Input Capacitance		2700	_		VGS = 0V, VDS = 25V	
Coss	Output Capacitance		600		pF	f = 1.0 MHz	
C _{rss}	Reverse Transfer Capacitance		240			see figure 5	

Source-Drain Diode Ratings and Characteristics

	Parameter		Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current (Body Diode)		_	12	Α	Modified MOSFET symbol showing the
ISM	Pulse Source Current (Body Diode) ①		—	48	,	integral reverse p-n junction rectifier.
VSD	Diode Forward Voltage		_	1.7	V	Tj = 25°C, IS = 12A, VGS = 0V 4
t _{rr}	Reverse Recovery Time	_	_	1600	ns	Tj = 25°C, IF = 12A, di/dt ≤ 100A/μs
QRR	Reverse Recovery Charge	T -	_	14	μC	V _{DD} ≤ 50V ④
ton	Forward Turn-On Time Intrinsic turn	n-On Time Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D .				

Thermal Resistance

	Parameter	Min.	Тур.	Max.	Units	Test Conditions
R _{th} JC	Junction-to-Case	_	_	0.83		
R _{th} J-PCB	Junction-to-PC Board	_	TBD	_	K/W	Soldered to a copper clad PC board

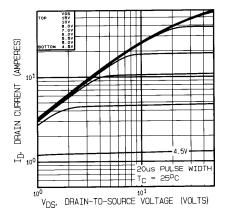


Fig. 1 — Typical Output Characteristics $T_C = 25^{\circ}C$

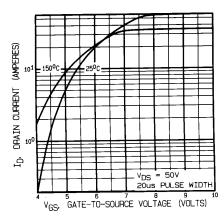


Fig. 3 — Typical Transfer Characteristics

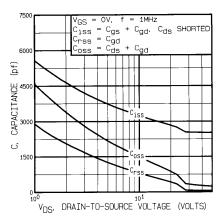


Fig. 5 — Typical Capacitance Vs. Drain-to-Source Voltage

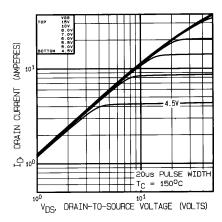


Fig. 2 — Typical Output Characteristics $T_C = 150^{\circ}C$

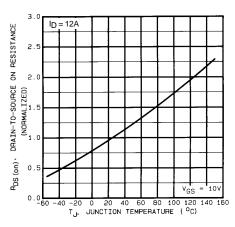


Fig. 4 — Normalized On-Resistance Vs.Temperature

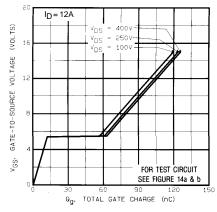


Fig. 6 — Typical Gate Charge Vs. Gate-to-Source Voltage

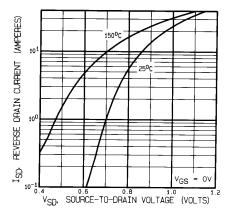


Fig. 7 — Typical Source-to-Drain Diode Forward Voltage

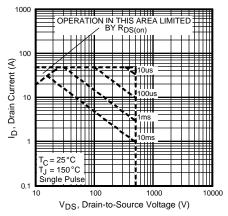


Fig. 8 — Maximum Safe Operating Area

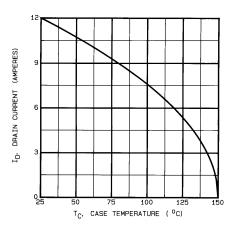


Fig. 9 — Maximum Drain Current Vs. Case Temperature

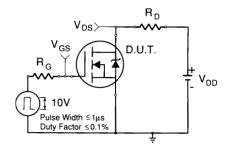


Fig. 10a — Switching Time Test Circuit

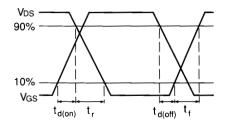


Fig. 10b — Switching Time Waveforms

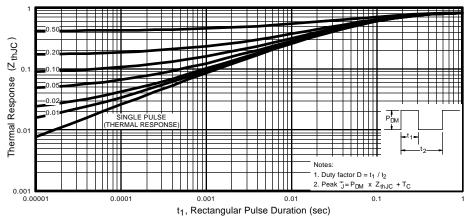


Fig. 11 — Maximum Effective Transient Thermal Impedance, Junction-to-Case Vs. Pulse Duration

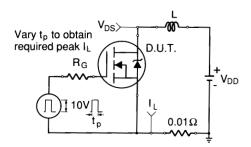


Fig. 12a — Unclamped Inductive Test Circuit

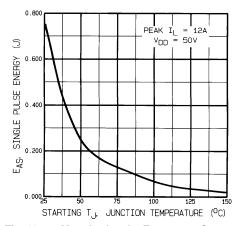


Fig. 12c — Max. Avalanche Energy vs. Current

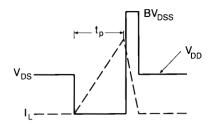


Fig. 12b — Unclamped Inductive Waveforms

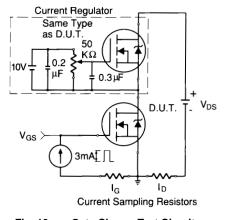


Fig. 13a — Gate Charge Test Circuit

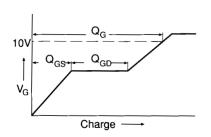
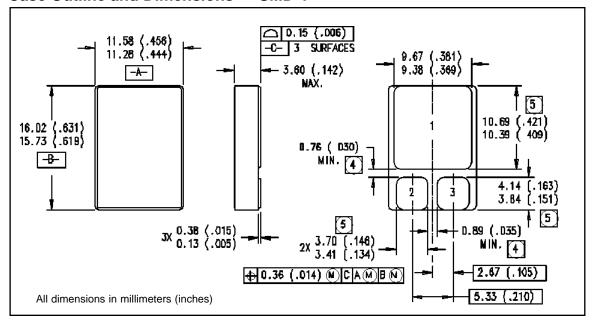


Fig. 13b — Basic Gate Charge Waveform

- 1 Repetitive Rating; Pulse width limited by maximum junction temperature. (see figure 11)
- ② @ Vnn = 50V, Starting T_{.1} = 25°C, $E_{AS} = [0.5 * L * (I_1^2) * [BV_{DSS}/(BV_{DSS}-V_{DD})]$ Peak IL = 12A, VGS = 10V, $25 \le R_G \le 200\Omega$
- ③ ISD ≤ 12A, di/dt ≤ 130 A/ μ s, VDD ≤ BVDSS, TJ ≤ 150°C
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ K/W = °C/W $W/K = W/^{\circ}C$

Case Outline and Dimensions — SMD-1



International IOR Rectifier

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