

4A, 1200V Hyperfast Diodes

The RHRD4120 and RHRD4120S are hyperfast diodes with soft recovery characteristics ($t_{rr} < 60\text{ns}$). They have half the recovery time of ultrafast diodes and are silicon nitride passivated ion-implanted epitaxial planar construction.

These devices are intended for use as freewheeling/clamping diodes and rectifiers in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits, reducing power loss in the switching transistors

Formerly development type TA49056.

Ordering Information

PART NUMBER	PACKAGE	BRAND
RHRD4120	TO-251	HR4120
RHRD4120S	TO-252	HR4120

NOTE: When ordering, use the entire part number. Add the suffix 9A to obtain the TO-252AA variant in the tape and reel, i.e., RHRD4120S9A.

Symbol



Features

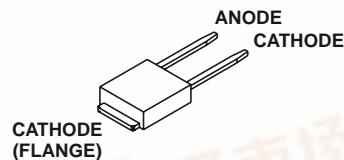
- Hyperfast with Soft Recovery <60ns
- Operating Temperature 175°C
- Reverse Voltage 1200V
- Avalanche Energy Rated
- Planar Construction

Applications

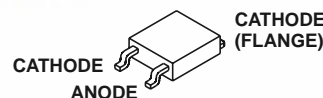
- Switching Power Supplies
- Power Switching Circuits
- General Purpose

Packaging

JEDEC STYLE TO-251



JEDEC STYLE TO-252



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

	RHRD4120, RHRD4120S	UNITS
Peak Repetitive Reverse Voltage	V_{RRM} 1200	V
Working Peak Reverse Voltage	V_{RWM} 1200	V
DC Blocking Voltage	V_R 1200	V
Average Rectified Forward Current ($T_C = 147.5^\circ\text{C}$)	$I_{F(AV)}$ 4	A
Repetitive Peak Surge Current (Square Wave, 20kHz)	I_{FRM} 8	A
Nonrepetitive Peak Surge Current (Halfwave, 1 Phase, 60Hz)	I_{FSM} 40	A
Maximum Power Dissipation	P_D 50	W
Avalanche Energy (See Figures 9 and 10)	E_{AVL} 10	mJ
Operating and Storage Temperature	T_{STG}, T_J -65 to 175	°C
Maximum Lead Temperature for Soldering (Leads at 0.063 in. (1.6mm) from case for 10s)	T_L 300	°C
Package Body for 10s, see Tech Brief 334.	T_{PKG} 260	°C



RHRD4120, RHRD4120S

Electrical Specifications $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V_F	$I_F = 4\text{A}$	-	-	3.2	V
	$I_F = 4\text{A}, T_C = 150^\circ\text{C}$	-	-	2.6	V
I_R	$V_R = 1200\text{V}$	-	-	100	μA
	$V_R = 1200\text{V}, T_C = 150^\circ\text{C}$	-	-	500	μA
t_{rr}	$I_F = 1\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	-	60	ns
	$I_F = 4\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	-	70	ns
t_a	$I_F = 4\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	40	-	ns
t_b	$I_F = 4\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	25	-	ns
Q_{RR}	$I_F = 4\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	140	-	nC
C_J	$V_R = 10\text{V}, I_F = 0\text{A}$	-	15	-	pF
$R_{\theta JC}$		-	-	3	$^\circ\text{C}/\text{W}$

DEFINITIONS

V_F = Instantaneous forward voltage ($p_w = 300\mu\text{s}$, $D = 2\%$).

I_R = Instantaneous reverse current.

t_{rr} = Reverse recovery time (See Figure 8), summation of $t_a + t_b$.

t_a = Time to reach peak reverse current (See Figure 8).

t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 8).

Q_{RR} = Reverse recovery charge.

C_J = Junction Capacitance.

$R_{\theta JC}$ = Thermal resistance junction to case.

p_w = pulse width.

D = duty cycle.

Typical Performance Curves

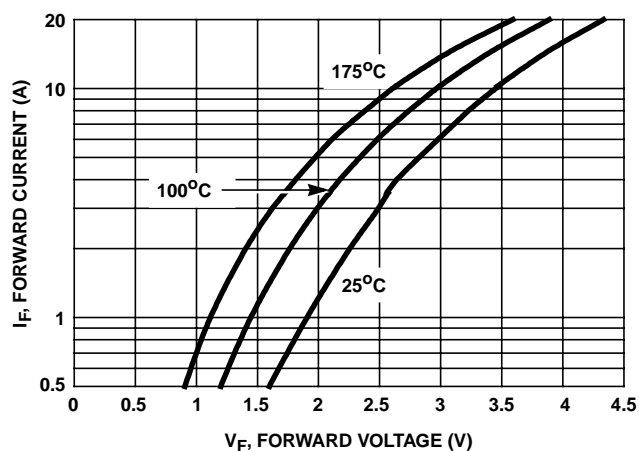


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

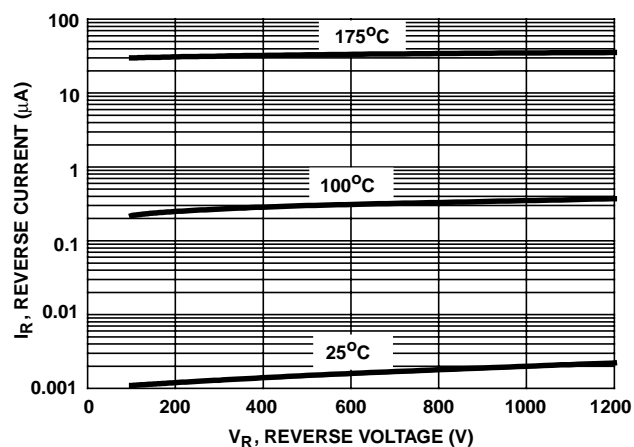


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

RHRD4120, RHRD4120S

Typical Performance Curves (Continued)

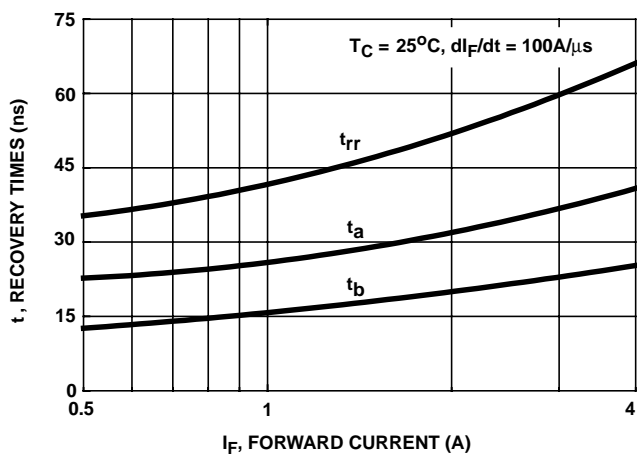


FIGURE 3. t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

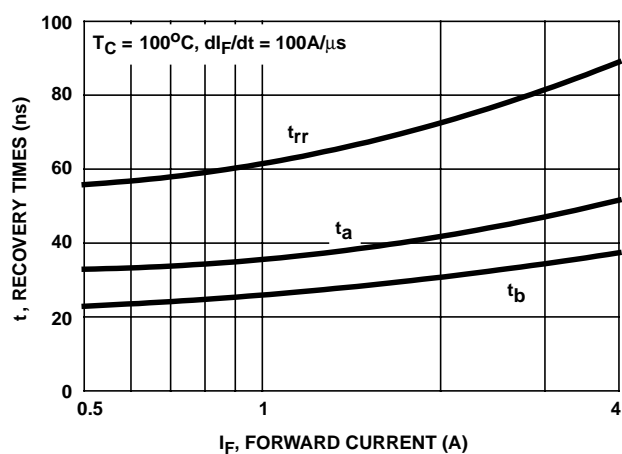


FIGURE 4. t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

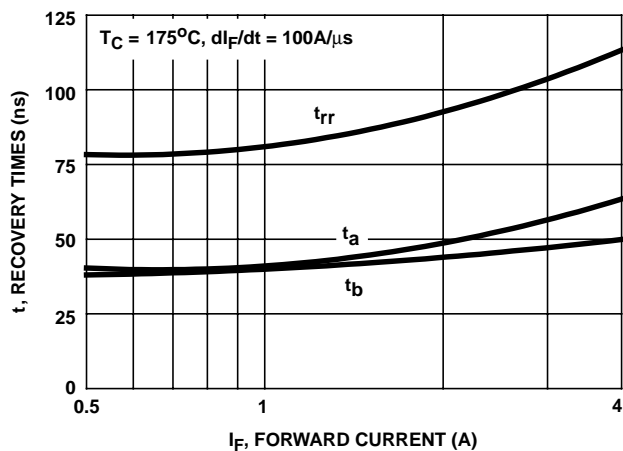


FIGURE 5. t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

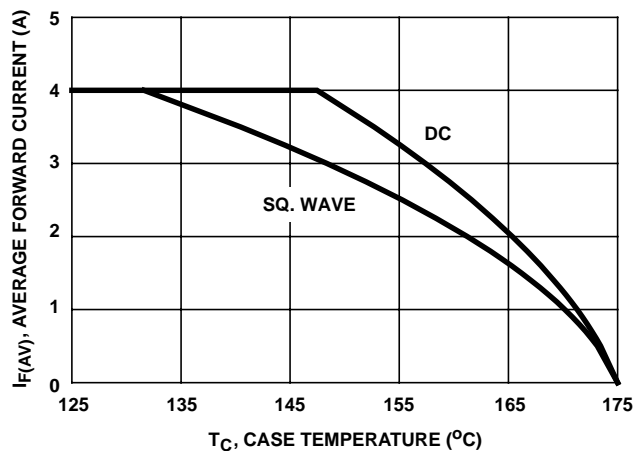


FIGURE 6. CURRENT DERATING CURVE

Test Circuits and Waveforms

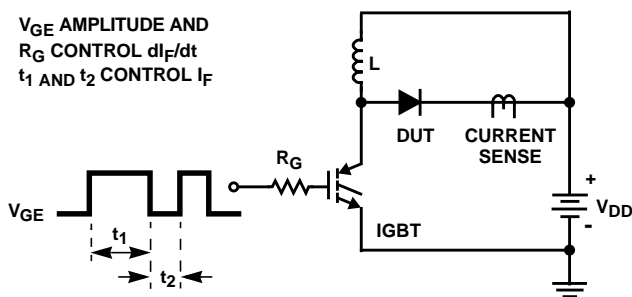


FIGURE 7. t_{rr} TEST CIRCUIT

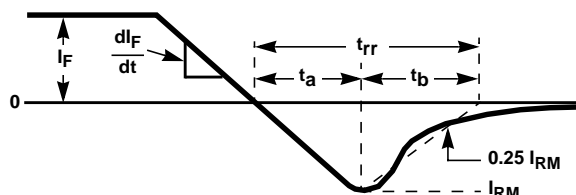


FIGURE 8. t_{rr} WAVEFORMS AND DEFINITIONS

Test Circuits and Waveforms (Continued)

$I_{MAX} = 1A$
 $L = 20mH$
 $R < 0.1\Omega$
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
 $Q_1 = IGBT (BV_{CES} > DUT V_{R(AVL)})$

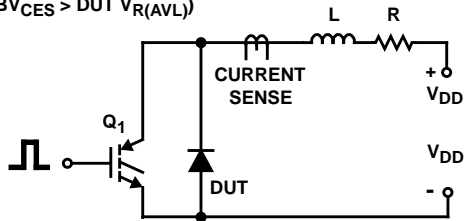


FIGURE 9. AVALANCHE ENERGY TEST CIRCUIT

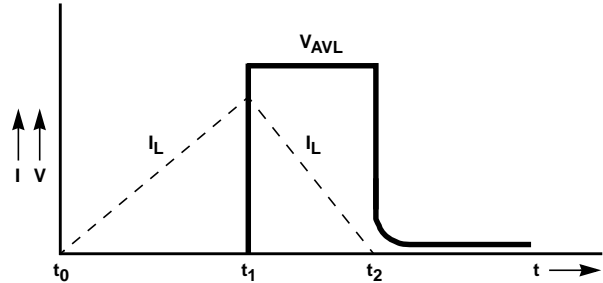


FIGURE 10. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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