2N5550, 2N5551

Preferred Device

Amplifier Transistors

NPN Silicon

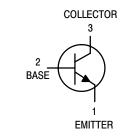
Features

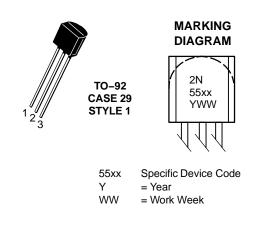
- Pb–Free Packages are Available*
- Device Marking: Device Type, e.g., 2N5550, Date Code



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ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS

Rating	Symbol	2N5550	2N5551	Unit
Collector – Emitter Voltage	V _{CEO}	140	160	Vdc
Collector – Base Voltage	V _{CBO}	160	180	Vdc
Emitter – Base Voltage	V _{EBO}	6.0		Vdc
Collector Current – Continuous	۱ _C	600		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0		mW mW/ºC
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12		W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	–55 to +150 °C		°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction–to–Case	$R_{\theta JC}$	83.3	°C/W

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

2N5550, 2N5551

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Мах	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage (Note 1)		V _{(BR)CEO}			Vdc
(I _C = 1.0 mAdc, I _B = 0)	2N5550	(51)020	140	-	
	2N5551		160	-	
Collector-Base Breakdown Voltage		V _{(BR)CBO}			Vdc
(I _C = 100 μAdc, I _E = 0)	2N5550	(511)050	160	_	
	2N5551		180	-	
Emitter-Base Breakdown Voltage		V _{(BR)EBO}	6.0	_	Vdc
$(I_E = 10 \ \mu Adc, I_C = 0)$		()			
Collector Cutoff Current		I _{CBO}			
$(V_{CB} = 100 \text{ Vdc}, I_{E} = 0)$	2N5550		_	100	nAdc
$(V_{CB} = 120 \text{ Vdc}, I_{E} = 0)$	2N5551		_	50	
$(V_{CB} = 100 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	2N5550		_	100	μAdc
$(V_{CB} = 120 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	2N5551		-	50	
Emitter Cutoff Current		I _{EBO}	_	50	nAdc
$(V_{EB} = 4.0 \text{ Vdc}, I_{C} = 0)$		Lbo			
ON CHARACTERISTICS (Note 1)				ļ	1
DC Current Gain		h _{FE}			_
$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	2N5550		60	_	
$(10 - 1.0 \text{ m/dd}, V_{\text{E}} - 0.0 \text{ Vdd})$	2N5551		80	_	
	2103301		00		
(I _C = 10 mAdc, V _{CE} = 5.0 Vdc)	2N5550		60	250	
	2N5551		80	250	
(I = 50 m/d s)/(I = 50 s)/(I = 50			20		
$(I_{C} = 50 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	2N5550 2N5551		20 30	_	
Collector-Emitter Saturation Voltage		Ver			Vdc
	Dath Turaa	V _{CE(sat)}		0.15	vuc
$(I_{C} = 10 \text{ mAdc}, I_{B} = 1.0 \text{ mAdc})$	Both Types		-	0.15	
(I _C = 50 mAdc, I _B = 5.0 mAdc)	2N5550		_	0.25	
	2N5551		_	0.20	
Base-Emitter Saturation Voltage		V _{BE(sat)}			Vdc
$(I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc})$	Both Types	· DE(Sal)	_	1.0	
(10 - 10 m/dd), 10 - 1.0 m/dd)	Dour types			1.0	
(I _C = 50 mAdc, I _B = 5.0 mAdc)	2N5550		_	1.2	
	2N5551		-	1.0	
SMALL-SIGNAL CHARACTERISTICS					÷
Current-Gain — Bandwidth Product		f _T	100	300	MHz
$(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 100 \text{ MHz})$					
Output Capacitance		C _{obo}	_	6.0	pF
$(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$					
Input Capacitance		C _{ibo}		1	pF
(V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	2N5550		_	30	
	2N5551		-	20	
Small–Signal Current Gain		h _{fe}	50	200	<u> </u>
$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$					
Noise Figure		NF			dB
$(I_{C} = 250 \ \mu \text{Adc}, V_{CE} = 5.0 \ \text{Vdc}, R_{S} = 1.0 \ \text{k}\Omega,$	2N5550		_	10	
f = 1.0 kHz	2N5551		_	8.0	
	2110001			0.0	

1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]	
2N5550	TO-92	5,000 Unit / Bulk	
2N5550RLRA	TO-92	2,000 Tape & Reel	
2N5550RLRP	TO-92	2,000 Tape & Ammo Box	
2N5550RLRPG	TO-92 (Pb-Free)	2,000 Tape & Ammo Box	
2N5551	TO-92	5,000 Unit / Bulk	
2N5551G	TO-92 (Pb-Free)	5,000 Unit / Bulk	
2N5551RL1	TO-92	2,000 Tape & Reel	
2N5551RLRA	TO-92	2,000 Tape & Reel	
2N5551RLRM	TO-92	2,000 Tape & Ammo Box	
2N5551RLRP	TO-92	2,000 Tape & Ammo Box	
2N55551ZL1	TO-92	2,000 Tape & Ammo Box	

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

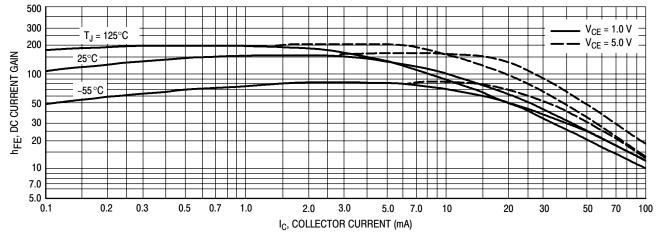
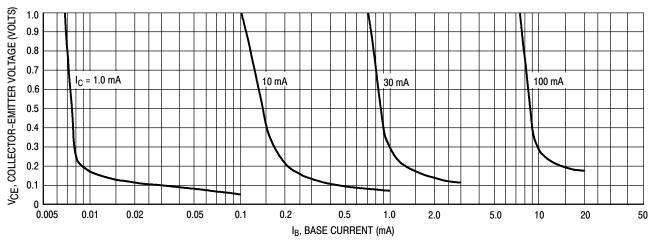
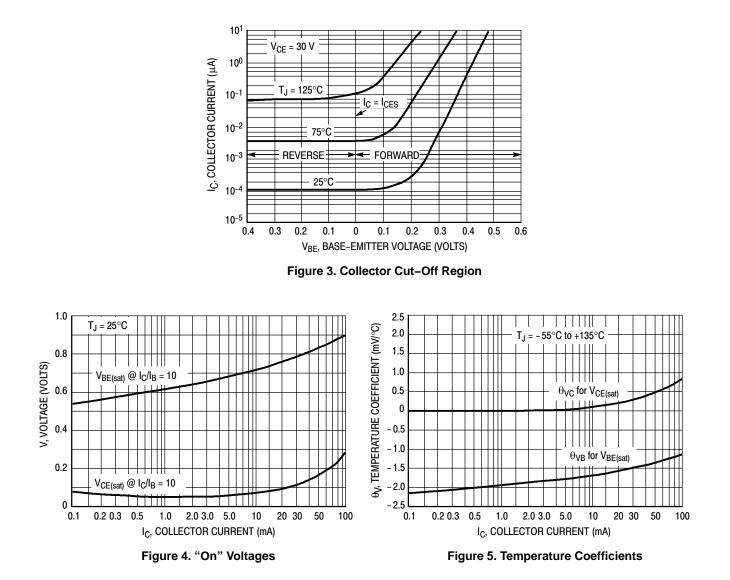


Figure 1. DC Current Gain

2N5550, 2N5551







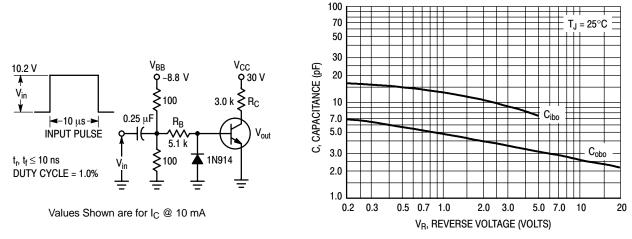
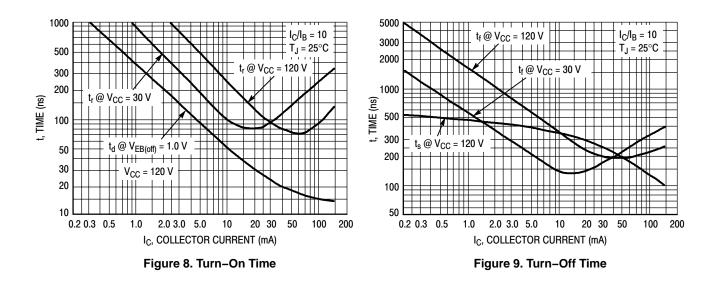


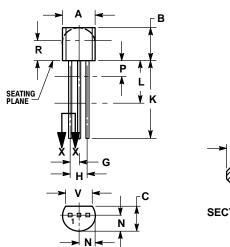
Figure 6. Switching Time Test Circuit





PACKAGE DIMENSIONS

TO-92 **TO-226AA** CASE 29-11 **ISSUE AL**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- TIA-3M, 1982. CONTROLLING DIMENSION: INCH. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. LEAD DIMENSION IS UNCONTROLLED IN P AND 2. 3.
- 4
- BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
ſ	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

STYLE 1: PIN 1. EMITTER

2. BASE

3. COLLECTOR

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