RT1N141X SERIES

(Transistor)

UNIT: mm

0.5

Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

DESCRIPTION

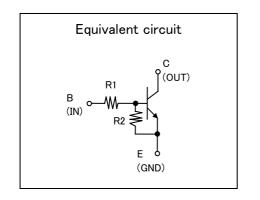
RT1N141X is a one chip transistor with built-in bias resistor,PNP type is RT1P141X.

FEATURE

•Built-in bias resistor (R1=10k Ω ,R2=10k Ω).

APPLICATION

Inverted circuit, switching circuit, interface circuit, driver circuit.



RT1N141S

OUTLINE DRAWING

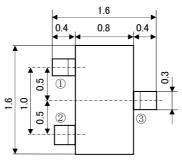
RT1N141C

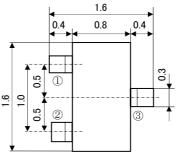
0.5

1.90

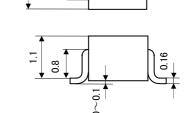
2.5

1.5





RT1N141U



JEITA: -JEDEC: -

Terminal Connector

①:Base

0

2: Emitter

3: Collector

JEITA: SC-59 JEDEC: Similar to TO-236

Terminal Connector

①:Base

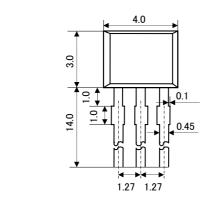
2: Emitter

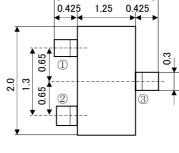
3: Collector

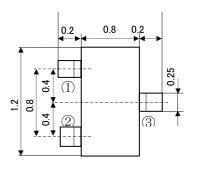
RT1N141M

2.1

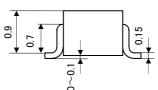
RT1N141T

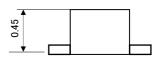












JEITA: -JEDEC:

Terminal Connector

1: Emitter

2: Collector

3:Base

JEITA: SC-70 JEDEC: -

Terminal Connector

1:Base

2: Emitter

3: Collector

JEITA: -JEDEC: -

Terminal Connector

(1):Base

2: Emitter

3: Collector

RT1N141X SERIES

(Transistor)

Transistor With Resistor
For Switching Application
Silicon NPN Epitaxial Type

MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING					LINIT
		RT1N141T	RT1N141U	RT1N141M	RT1N141C	RT1N141S	UNIT
V _{CBO}	Collector to Base voltage	50					V
V _{EBO}	Emitter to Base voltage	10					
V_{CEO}	Collector to Emitter voltage	50					V
Ιc	Collector current	100					mA
I _{CM}	Peak Collector current	200					mA
P _c	Collector dissipation(Ta=25°C)	125(※)	125	15	50	450	mW
Tj	Junction temperature	+125		+150			°C
Tstg	Storage temperature	−55 ~ +125		−55 ~ +150			°C

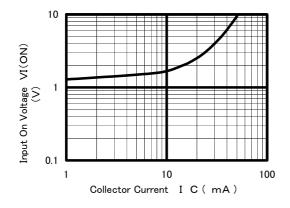
($\mbox{\@month{\@month{\times}}}$) package mounted on 9mm $\mbox{\ensuremath{\times}}$ 19mm $\mbox{\ensuremath{\times}}$ 1mm glass-epoxy substrate.

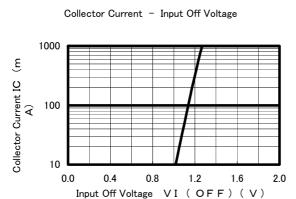
ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TECT CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E break down voltage	I _C =100 μ A, R _{BE} =∞	50			V
I _{CBO}	Collector cut off current	$V_{CB}=50V$, $I_{E}=0$			0.1	μΑ
h _{FE}	DC forward current gain	V_{CE} =5V, I $_{C}$ =10mA	50			_
$V_{CE(sat)}$	C to E saturation voltage	$I_{C} = 10$ mA, $I_{B} = 0.5$ mA		0.1	0.3	V
$V_{I(ON)}$	Input on voltage	V_{CE} =0.2V, I $_{C}$ =5mA		1.5	3.0	V
$V_{I(OFF)}$	Input off voltage	V_{CE} =5V, I $_{C}$ =100 μ A	0.8	1.1		V
R ₁	Input resistance		7.0	10	13	kΩ
R ₂ /R ₁	Resistance ratio		0.9	1.0	1.1	
f _⊤	Gain band width product	$V_{CE}=6V$, $I_{E}=-10mA$		200		MHz

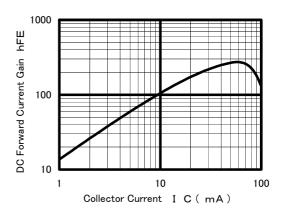
TYPICAL CHARACTERISTICS

Input On Voltage - Collector Current





DC Forward Current Gain - Collector Current





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