## **MOTOROLO**供应商 SEMICONDUCTOR TECHNICAL DATA

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by S2800/D

# **Silicon Controlled Rectifiers Reverse Blocking Triode Thyristors**

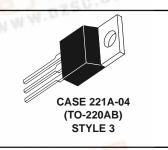
... designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supplies; or wherever half-wave silicon gate-controlled, solid-state devices are needed.

- Glass Passivated Junctions with Center Gate Fire for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat ٠ Dissipation and Durability
- Blocking Voltage to 800 Volts ٠



**SCRs 10 AMPERES RMS** 50 thru 800 VOLTS





#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blocking Voltage <sup>(1)</sup> (T <sub>J</sub> = 25 to 100°C, Gate Open) F A B D M N	VRRM VDRM	50 100 200 400 600 800	Volts
Peak Non-repetitive Reverse Voltage and Non-Repetitive Off-State Voltage(1) F A B S2800 D M N	VRSM VDSM	75 125 250 500 700 900	Volts
RMS Forward Current (All Conduction Angles)	<sup>I</sup> T(RMS)	10	Amps
Peak Forward Surge Current (1 Cycle, Sine Wave, 60 Hz, $T_{C}$ = 80°C)	ITSM	100	Amps
Circuit Fusing Considerations (t = 8.3 ms)	l <sup>2</sup> t	40	A <sup>2</sup> s
Forward Peak Gate Power (t ≤ 10 μs)	P <sub>GM</sub>	16	Watts
Forward Average Gate Power	PG(AV)	0.5	Watt
Operating Junction Temperature Range	Тј	-40 to +100	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C

WDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded. df.dzsc.com



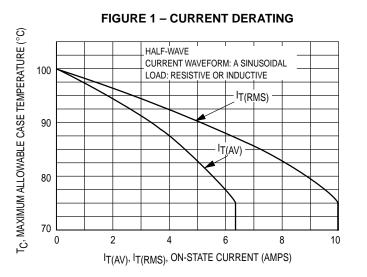
## S2800 Series

#### THERMAL CHARACTERISTICS

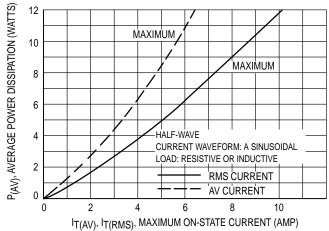
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	2	°C/W

ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

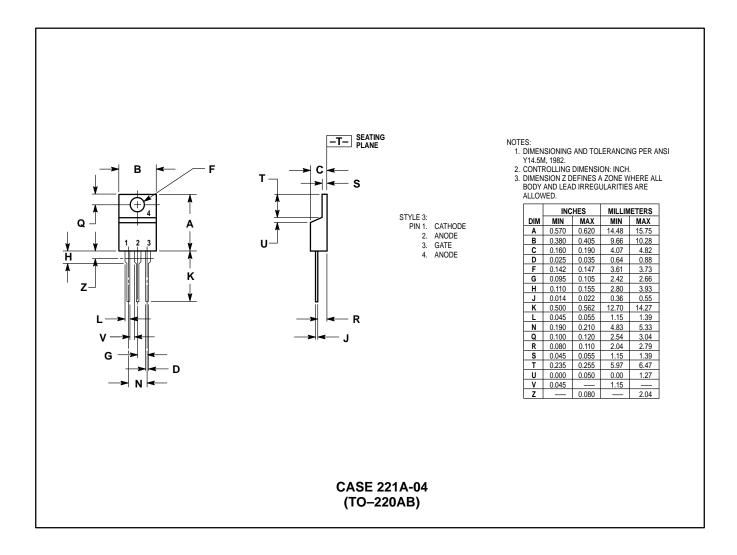
Characteristic	Symbol	Min	Тур	Max	Unit
Peak Forward or Reverse Blocking Current $(V_{AK} = Rated V_{DRM} \text{ or } V_{RRM}, Gate Open)$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	IDRM, IRRM	_	_	10 2	μA mA
Instantaneous On-State Voltage, ( $I_{TM}$ = 30 A Peak, Pulse Width $\leq$ 1 ms, Duty Cycle $\leq$ 2%)	VT	-	1.7	2	Volts
Gate Trigger Current (Continuous dc) $(V_D = 12 \text{ Vdc}, R_L = 30 \text{ Ohms})$	IGT	-	8	15	mA
Gate Trigger Voltage (Continuous dc) (V <sub>D</sub> = 12 Vdc, R <sub>L</sub> = 30 Ohms)	VGT	-	0.9	1.5	Volts
Holding Current (Gate Open, V <sub>D</sub> = 12 Vdc, I <sub>T</sub> = 150 mA)	ΙΗ	-	10	20	mA
Gate Controlled Turn-On Time (V <sub>D</sub> = Rated V <sub>DRM</sub> , I <sub>TM</sub> = 2 A, I <sub>GR</sub> = 80 mA)	tgt	-	1.6	-	μs
Circuit Commutated Turn-Off Time ( $V_D = V_{DRM}$ , $I_{TM} = 2 A$ , Pulse Width = 50 µs, dv/dt = 200 V/µs, di/dt = 10 A/µs, $T_C = 75^{\circ}C$ )	tq	_	25	-	μs
Critical Rate-of-Rise of Off-State Voltage (V <sub>D</sub> = Rated V <sub>DRM</sub> , Exponential Rise, T <sub>C</sub> = 100°C)	dv/dt	_	100	—	V/µs



#### **FIGURE 2 – POWER DISSIPATION**



## PACKAGE DIMENSIONS



#### S2800 Series

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