



S4580
Dual OP AMP

Description

S4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application. Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

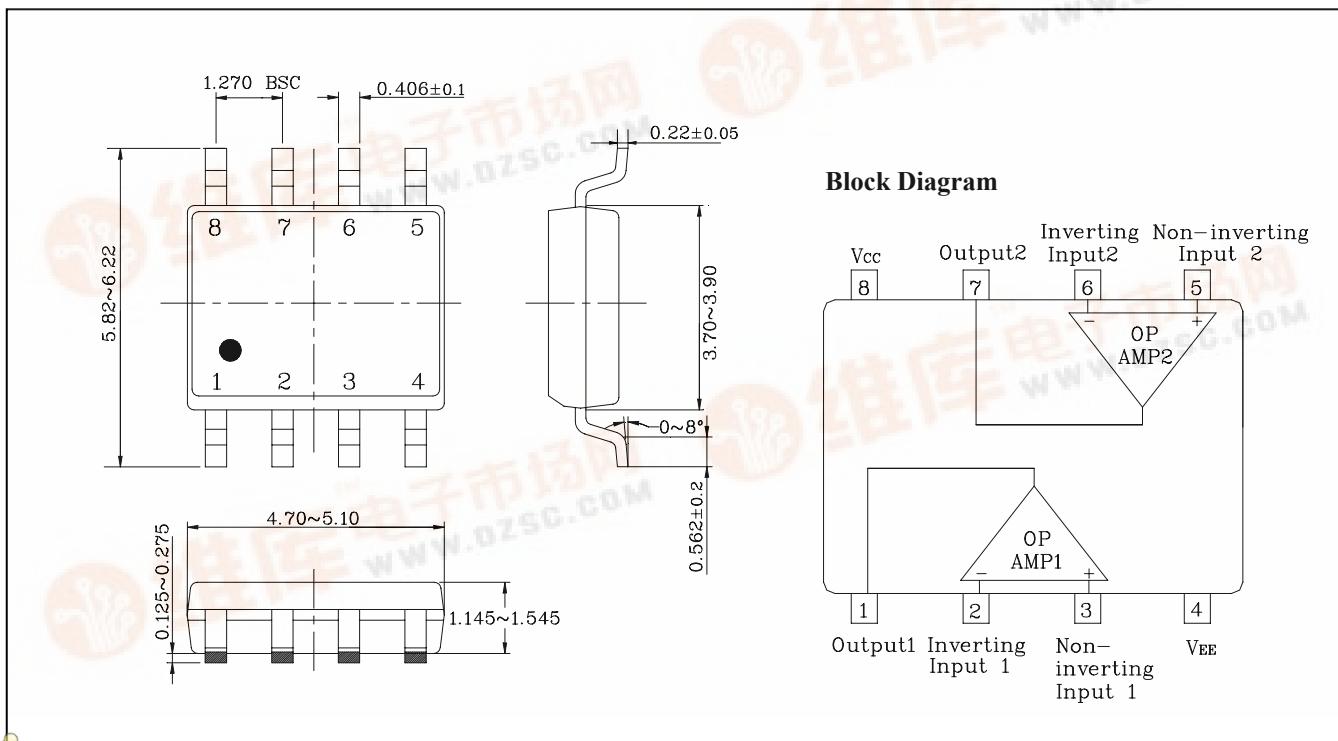
Features

- Operating voltage. ($\pm 2V \sim \pm 18V$)
- Low input noise voltage. ($0.8 \mu V$ rms typ.)
- Wide gain bandwidth product. (15MHz typ.)
- Low distortion. (0.0005% typ.)
- Slew rate. ($5V/\mu s$ typ.)

Ordering Information

| Type NO. | Marking | Package Code |
|----------|---------|--------------|
| S4580 | S4580 | SOP-8 |

Outline Dimensions



Absolute maximum ratings

Ta = 25 °C

| Characteristic | Symbol | Ratings | Unit |
|----------------------------|------------------------|------------|------|
| Supply voltage | V _{CC} | ±18 | V |
| Differential input voltage | V _{ID} | ±30 | V |
| Input voltage | V _{IC} (note) | ±15 | V |
| Output current | I _O | ±50 | mA |
| Power Dissipation | P _D | 300 | mW |
| Operating temperature | T _{opr} | -40 ~ +85 | °C |
| Storage temperature | T _{stg} | -40 ~ +125 | °C |

(note) For supply voltage less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

Electrical Characteristics(Unless otherwise specified. V_{CC} = +15V, V_{EE} = -15V and Ta = 25 °C)

| Characteristic | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|--------------------------------------|------------------|---|------|--------|------|-------|
| Input offset voltage | V _{IOS} | R _g ≤ 10 kΩ | - | 0.5 | 3 | mV |
| Input offset current | I _{IOS} | - | - | 5 | 200 | nA |
| Input bias current | I _{IB} | - | - | 100 | 500 | nA |
| Input common mode Voltage Range | V _{ICR} | - | ±12 | ±13.5 | - | V |
| Maximum Output Voltage | V _{OM} | R _L ≥ 2 kΩ | ±12 | ±13.5 | - | V |
| Large signal Voltage Gain | G _V | V _{out} = ±10V, R _L ≥ 2 kΩ | 90 | 110 | - | dB |
| Common mode rejection ratio | CMRR | R _g ≤ 10 kΩ | 80 | 110 | - | dB |
| Power supply voltage rejection ratio | PSRR | R _g ≤ 10 kΩ | 80 | 110 | - | dB |
| Slew Rate | SR | R _L ≥ 2 kΩ | - | 5.0 | - | V/us |
| Supply Current | I _{CC} | - | - | 6.0 | 9.0 | mA |
| Equivalent input noise voltage | V _{NI} | RIAA, R _S = 2.2 kΩ, f = 30 kHz LPF | - | 0.8 | - | uVRms |
| Total harmonic distortion | THD | A _v = 20dB, V _O = 5V, R _L = 2 kΩ f = 1KHz | - | 0.0005 | - | % |
| Gain bandwidth product | GB | f = 10KHz | - | 15 | - | MHz |

Electrical Characteristic Curves

Fig. 1 G_V - f

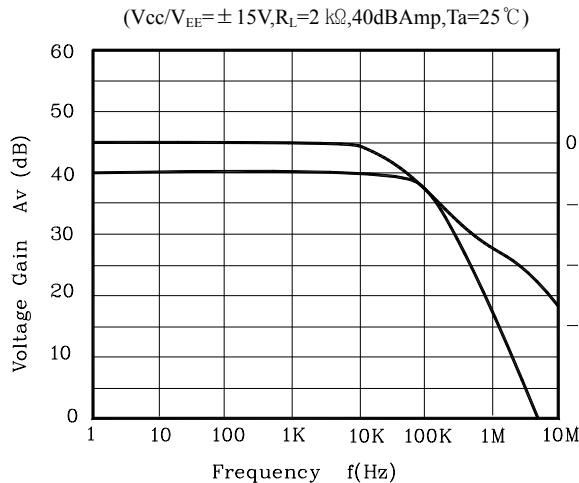


Fig. 2 V_{OP-P} - f

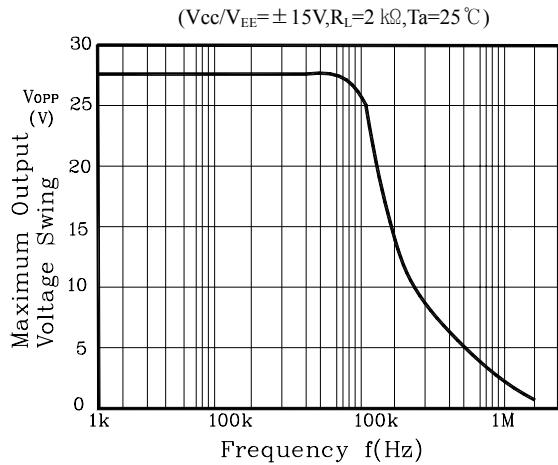


Fig. 3 I_{IB} - T_a

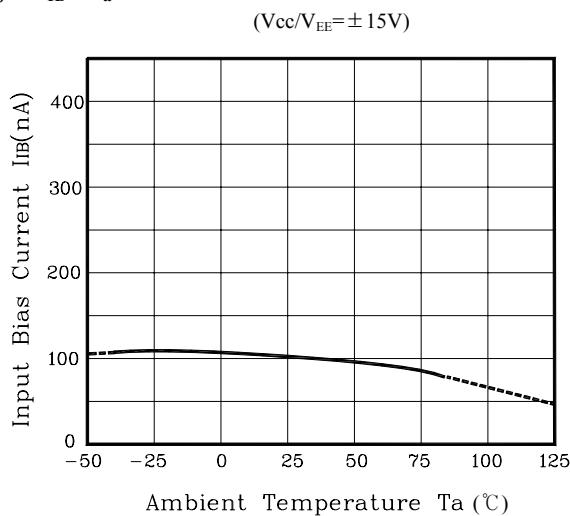


Fig. 4 V_{OM} - V_{CC}/V_{EE}

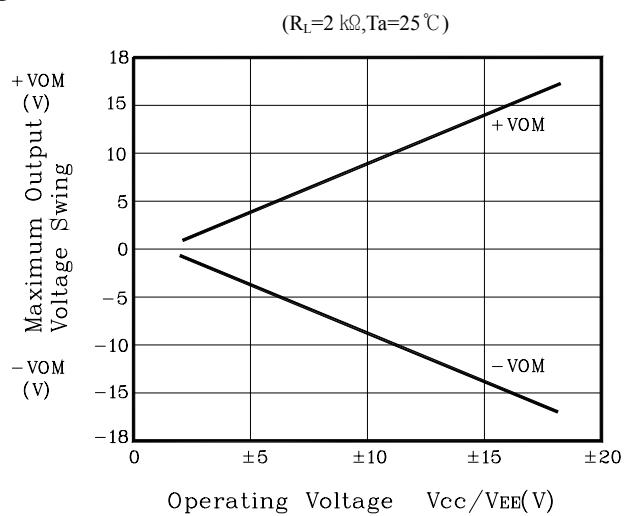


Fig. 5 V_{OM} - R_L

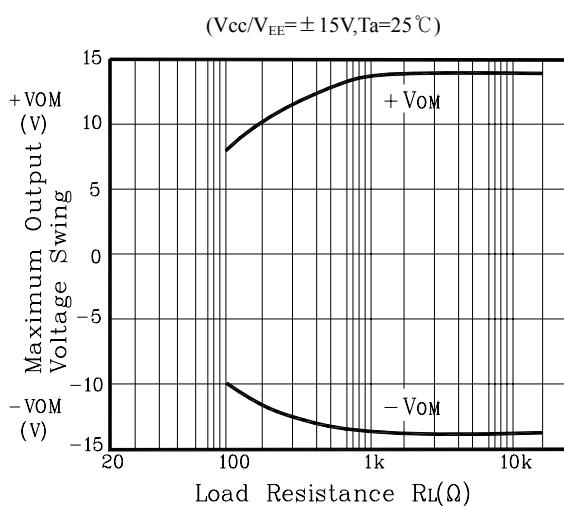
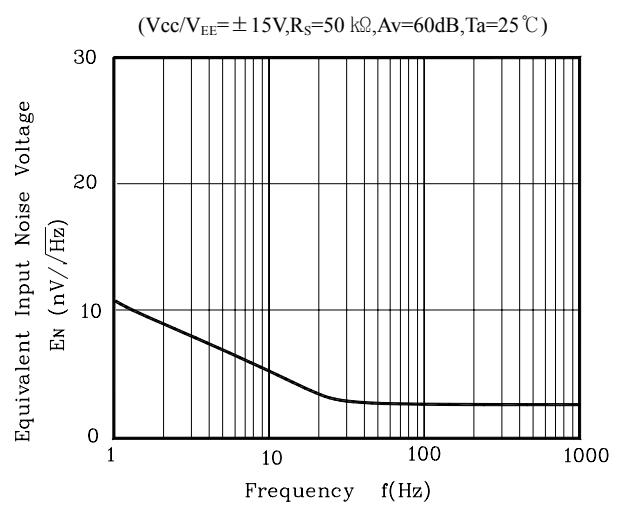
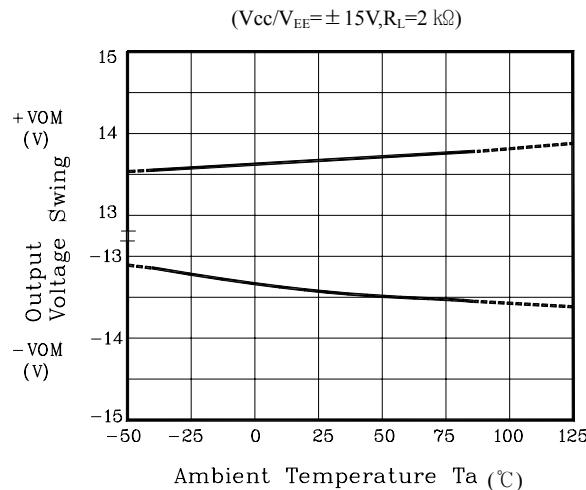
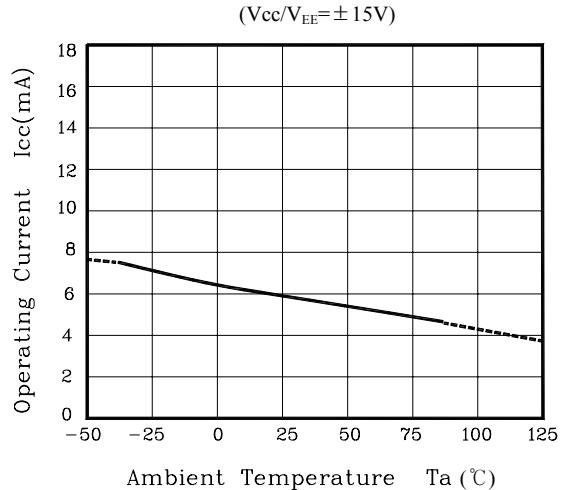
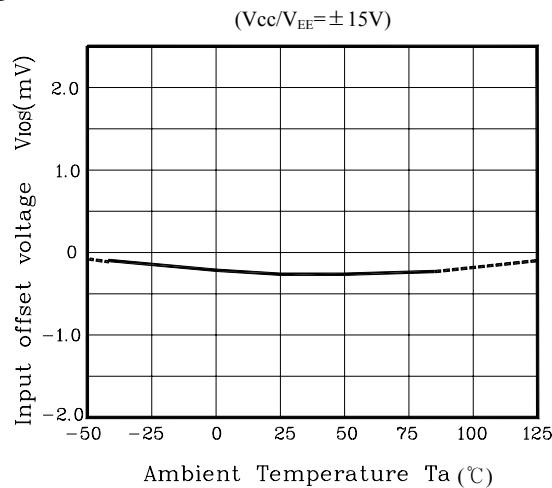
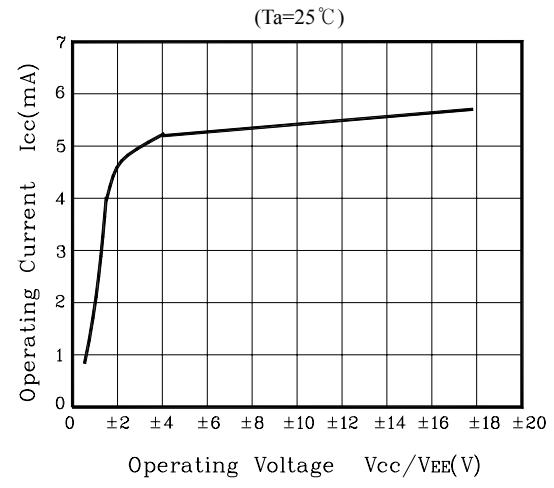


Fig. 6 E_N - f



Electrical Characteristic Curves

Fig. 7 V_{OM} - T_a **Fig. 8** I_{CC} - T_a **Fig. 9** V_{IOS} - T_a **Fig. 10** I_{CC} - V_{CC} 

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