



PULSE WIDTH MODULATION AMPLIFIER

SA55

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PRELIMINARY

FEATURES

- DELIVERS UP TO 5A CONTINUOUS OUTPUT
- OPERATES AT SUPPLY VOLTAGES TO 55V
- TTL AND CMOS COMPATIBLE INPUTS
- NO "SHOOT-THROUGH" CURRENT
- THERMAL WARNING FLAG OUTPUT AT 145°C
- THERMAL SHUTDOWN (OUTPUTS OFF) AT 160°C
- INTERNAL CLAMP DIODES
- SHORTED LOAD PROTECTION
- INTERNAL CHARGE PUMP WITH EXTERNAL BOOTSTRAP CAPABILITY



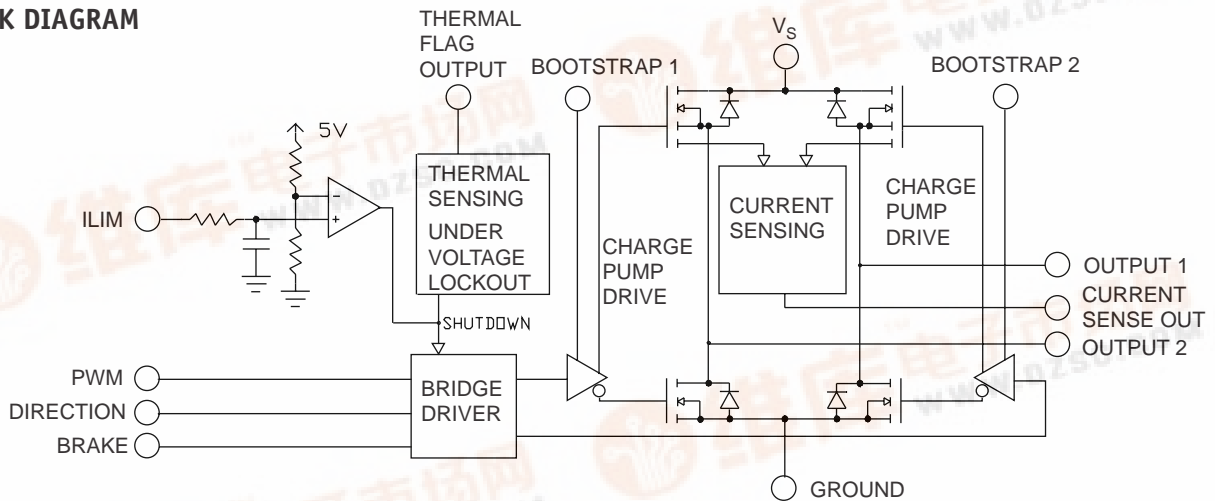
APPLICATIONS

- DC AND STEPPER MOTOR DRIVES
- POSITION AND VELOCITY SERVOMECHANISMS
- FACTORY AUTOMATION ROBOTS
- NUMERICALLY CONTROLLED MACHINERY
- COMPUTER PRINTERS AND PLOTTERS

DESCRIPTION

The SA55 is a 5A H-Bridge designed for motion control applications. The device is built using a multi-technology process which combines bipolar and CMOS control circuitry with DMOS power devices on the same monolithic structure. Ideal for driving DC and stepper motors; the SA55 accommodates peak output currents up to 10A. An innovative circuit which facilitates low-loss sensing of the output current has been implemented.

BLOCK DIAGRAM



SA55

ABSOLUTE MAXIMUM RATINGS SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

SUPPLY VOLTAGE, V_S , Pin 6	60V
VOLTAGE at Pins 3, 4, 5, 8 and 9	12V
VOLTAGE at Bootstrap Pins (Pins 1 and 11)	$V_{OUT} \pm 16V$
PEAK OUTPUT CURRENT (200mS)	10A
CONTINUOUS OUTPUT CURRENT (Note 2)	5A
POWER DISSIPATION (Note 3)	25W
POWER DISSIPATION ($T_A = 25^\circ\text{C}$, Free Air)	3W
JUNCTION TEMPERATURE, $T_{J(\text{MAX})}$	150°C
ESD SUSCEPTIBILITY (Note 4)	1500V
STORAGE TEMPERATURE, T_{STG}	-40°C to +150°C
LEAD TEMPERATURE (Soldering, 10 sec.)	300°C
JUNCTION TEMPERATURE, T_J	-40°C to +125°C
V_S SUPPLY VOLTAGE	+12V to +55V

SPECIFICATIONS

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
SWITCH ON RESISTANCE, $R_{DS(\text{ON})}$	Output Current = 5A		0.33	0.6	Ω
CLAMP DIODE FORWARD DROP, V_{CLAMP}	Clamp Current = 5A		1.2	1.5	V
LOGIC LOW INPUT VOLTAGE, V_{IL}	Pins 3, 4, 5			-0.1	V
LOGIC LOW INPUT CURRENT, I_{IL}	$V_{\text{IN}} = -0.1\text{V}$, Pins = 3, 4, 5			0.8	V
LOGIC HIGH INPUT VOLTAGE, V_{IH}	Pins 3, 4, 5			-10	μA
LOGIC HIGH INPUT CURRENT, I_{IH}	$V_{\text{IN}} = 12\text{V}$, Pins = 3, 4, 5			2	V
CURRENT SENSE OUTPUT	$V_{\text{IN}} = 12\text{V}$, Pins = 3, 4, 5 $I_{\text{OUT}} = 1\text{A}$		485	12	V
CURRENT SENSE LINEARITY	$1\text{A} \leq I_{\text{OUT}} \leq 5\text{A}$		± 6	10	μA
UNDERVOLTAGE LOCKOUT	Outputs Turn OFF			560	μA
UNDERVOLTAGE LOCKOUT	Outputs Turn OFF			9	V
WARNING FLAG TEMPERATURE, T_{JW}	Pin 9 $\leq 0.8\text{V}$, $I_L = 2\text{mA}$		145		%
FLAG OUTPUT SATURATION VOLTAGE, $V_{\text{F(ON)}}$	$T_J = T_{\text{JW}}$, $I_L = 2\text{mA}$		0.15		$^\circ\text{C}$
FLAG OUTPUT LEAKAGE, $I_{\text{F(OFF)}}$	$V_{\text{F}} = 12\text{V}$		0.2	10	V
SHUTDOWN TEMPERATURE, T_{JSD}	Outputs Turn OFF		160	175	μA
QUIESCENT SUPPLY CURRENT, I_{S}	All Logic Inputs Low		13	25	$^\circ\text{C}$
OUTPUT TURN-ON DELAY TIME, t_{Don}	Sourcing Outputs, $I_{\text{OUT}} = 5\text{A}$		300		mA
OUTPUT TURN-ON SWITCHING TIME, t_{on}	Sinking Outputs, $I_{\text{OUT}} = 5\text{A}$		300		ns
OUTPUT TURN-OFF DELAY TIMES, t_{Doff}	Bootstrap Capacitor = 10 nF Sourcing Outputs, $I_{\text{OUT}} = 5\text{A}$		100		ns
OUTPUT TURN-OFF SWITCHING TIME, t_{off}	Sinking Outputs, $I_{\text{OUT}} = 5\text{A}$		80		ns
MINIMUM INPUT PULSE WIDTH, t_{pw}	Sourcing Outputs, $I_{\text{OUT}} = 5\text{A}$		200		ns
CHARGE PUMP RISE TIME, t_{cpr}	Sinking Outputs, $I_{\text{OUT}} = 5\text{A}$		200		ns
	Bootstrap Capacitor = 10 nF				ns
	Sourcing Outputs, $I_{\text{OUT}} = 5\text{A}$		75		ns
	Sinking Outputs, $I_{\text{OUT}} = 5\text{A}$		70		ns
	Pins 3, 4, 5		1		μs
	No Bootstrap Capacitor		20		μs

NOTE: These specifications apply for $V_S = 42\text{V}$, unless otherwise specified.