



# SBL3030PT - SBL3060PT

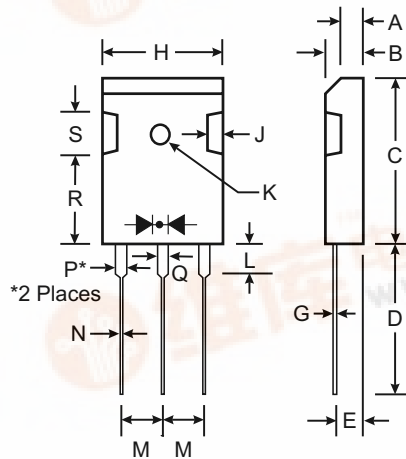
## 30A SCHOTTKY BARRIER RECTIFIER

### Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Plastic Material : UL Flammability Classification Rating 94V-0

### Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Body
- Marking: Type Number
- Weight: 2.24 grams (approx.)
- Mounting Position: Any



TO-3P		
Dim	Min	Max
A	3.20	3.50
B	4.59	5.16
C	20.80	21.30
D	19.70	20.20
E	2.10	2.40
G	0.51	0.76
H	15.90	16.40
J	1.70	2.70
K	3.10 $\varnothing$	3.30 $\varnothing$
L	3.50	4.51
M	5.20	5.70
N	1.12	1.22
P	1.93	2.18
Q	2.97	3.22
R	11.70	12.80
S	4.30 Typical	
All Dimensions in mm		

### Maximum Ratings and Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	SBL 3030PT	SBL 3035PT	SBL 3040PT	SBL 3045PT	SBL 3050PT	SBL 3060PT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	35	40	45	50	60	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	24.5	28	31.5	35	42	V
Average Rectified Output Current @ T <sub>C</sub> = 95°C (Note 1)	I <sub>O</sub>	30						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	275						A
Forward Voltage Drop @ I <sub>F</sub> = 15A, T <sub>C</sub> = 25°C	V <sub>FM</sub>	0.55				0.70		V
Peak Reverse Current @ T <sub>C</sub> = 25°C at Rated DC Blocking Voltage @ T <sub>C</sub> = 100°C	I <sub>RM</sub>					1.0 75		mA
Typical Junction Capacitance (Note 2)	C <sub>j</sub>	1100						pF
Typical Thermal Resistance Junction to Case (Note 1)	R <sub>θJc</sub>	2.0						K/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-65 to +150						°C

- Notes: 1. Thermal resistance junction to case mounted on heatsink.  
2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.



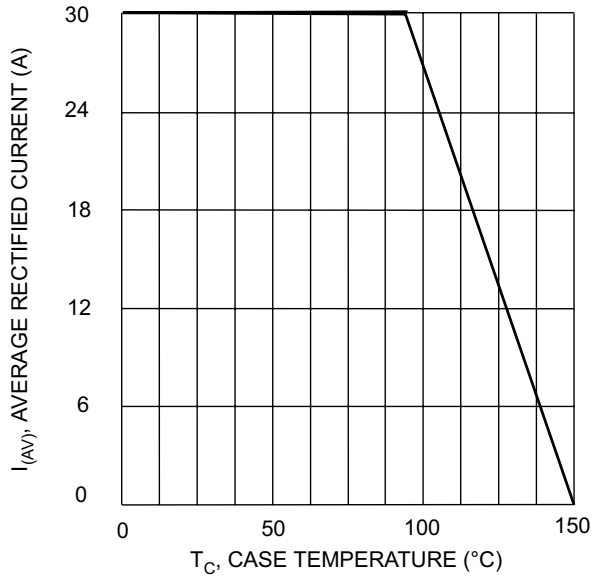


Fig. 1 Forward Derating Curve

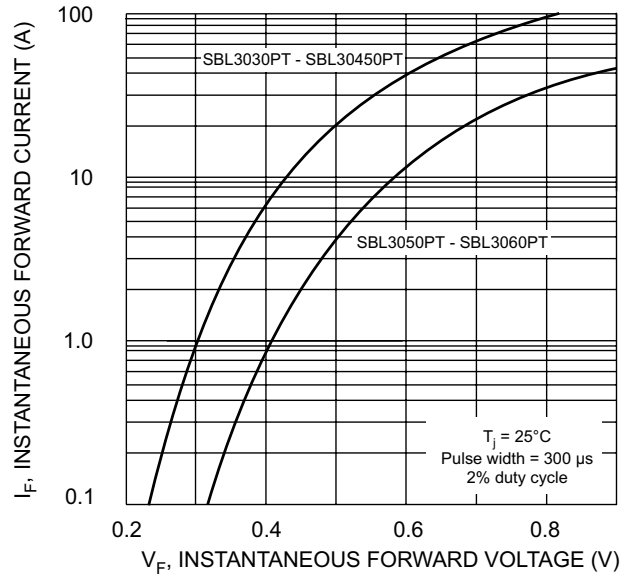


Fig. 2 Typical Fwd Characteristics per Element

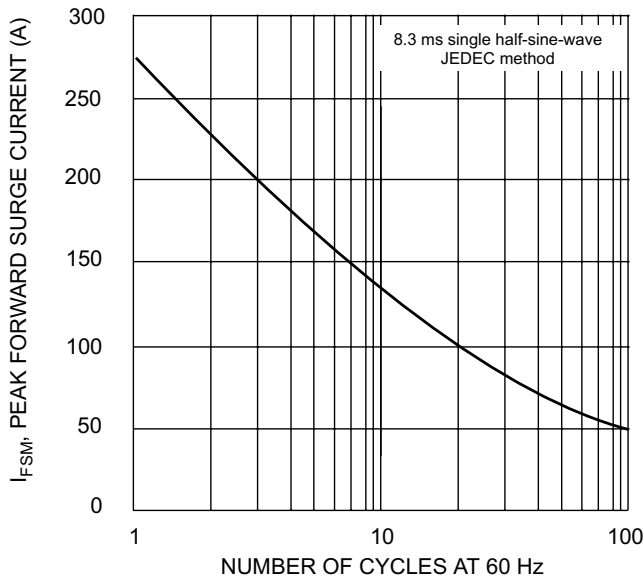


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

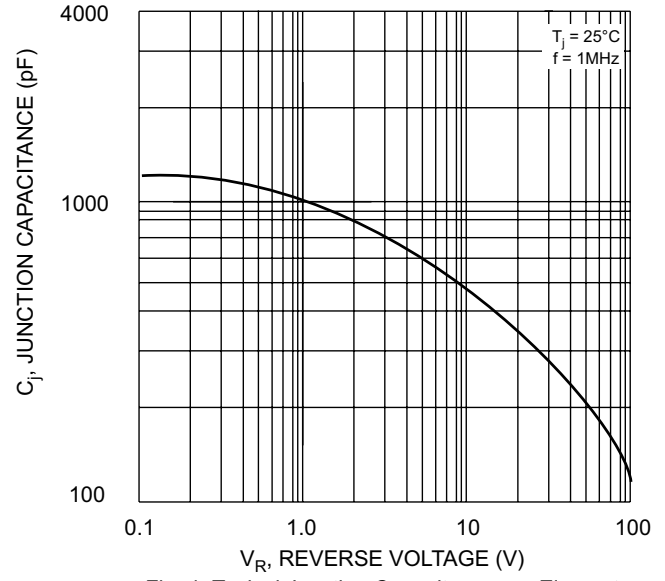


Fig. 4 Typical Junction Capacitance per Element

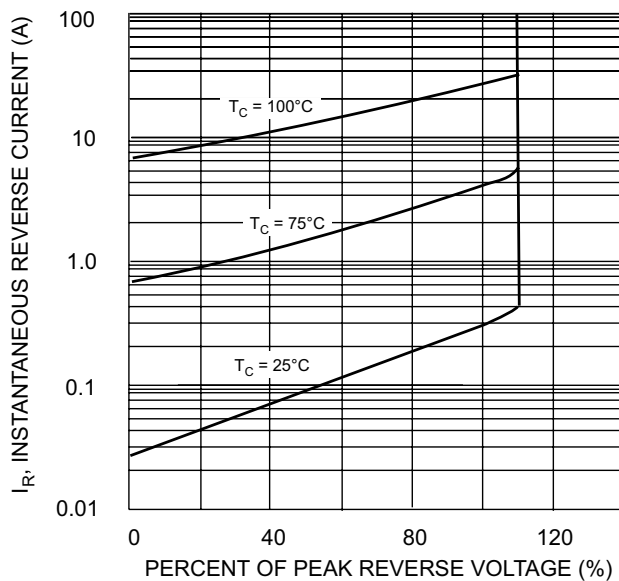


Fig. 5 Typical Reverse Characteristics per Element