

Ordering number : ENN7532

P-Channel Silicon MOSFET



SCH1302

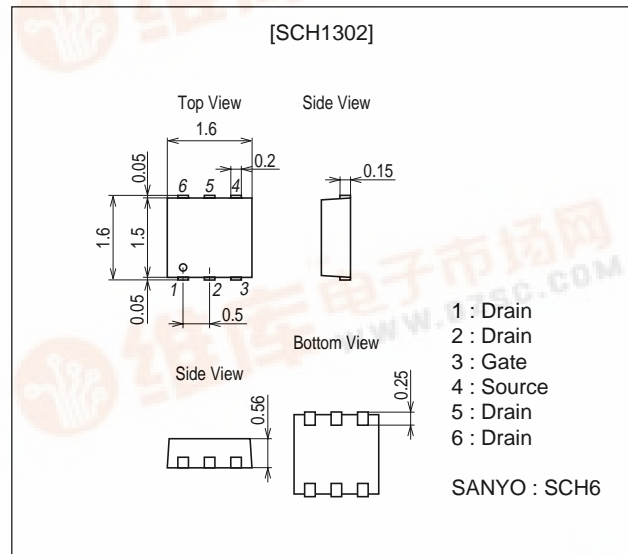
Ultrahigh-Speed Switching Applications

Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 1.8V drive.

Package Dimensions

unit : mm
2221



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-20	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		-2	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	-8	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm²X0.8mm)	0.8	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-20			V
Zero-Gate Voltage Drain Current	I _{DSS1}	V _{DS} =-20V, V _{GS} =0			-10	μA
	I _{DSS2}	V _{DS} =-4V, V _{GS} =0			-1	μA
Gate-to-Source Leakage Current	I _{GSS1}	V _{GS} =±8V, V _{DS} =0			±10	μA
	I _{GSS2}	V _{GS} =±4V, V _{DS} =0			±1	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-10V, I _D =-1mA	-0.3		-1.0	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-10V, I _D =-1A	1.8	3.0		S

Marking : JB

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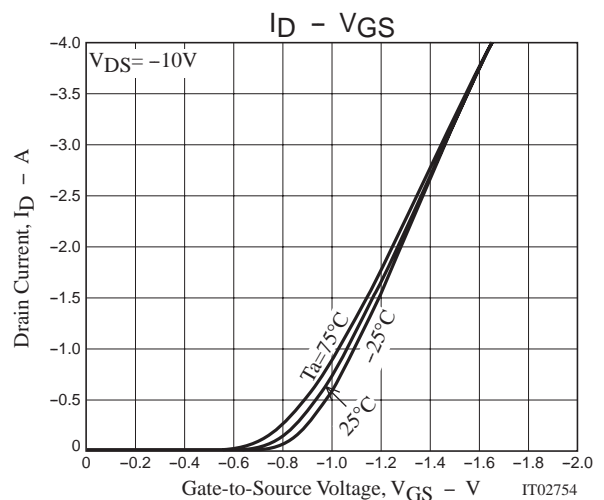
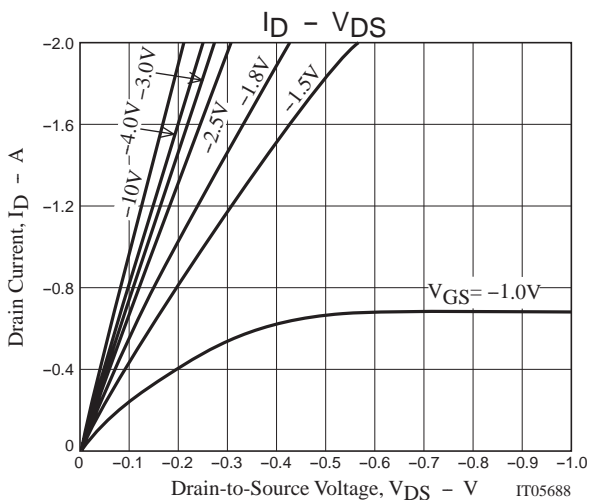
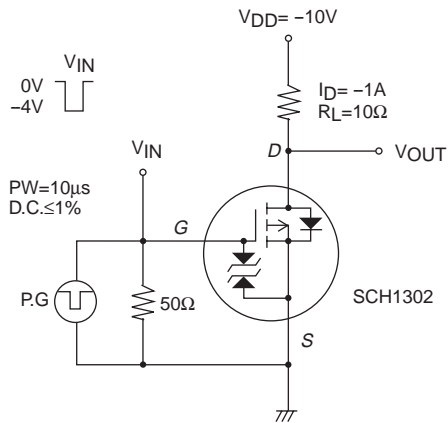


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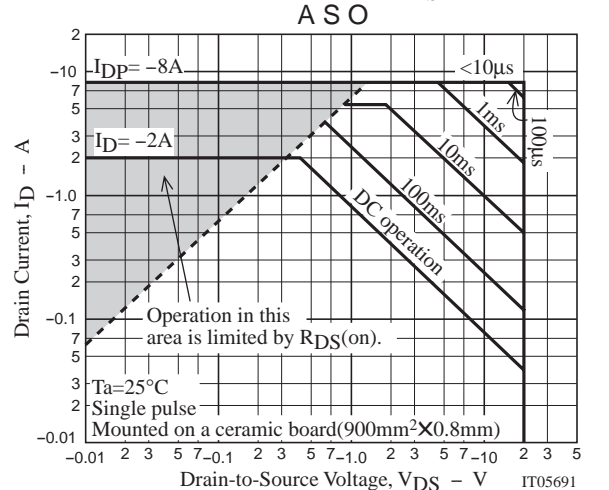
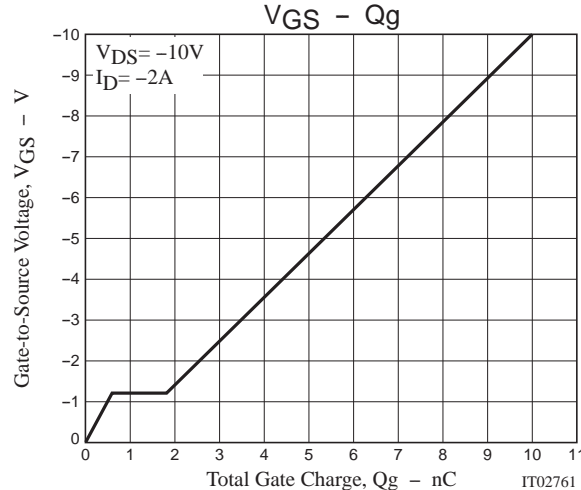
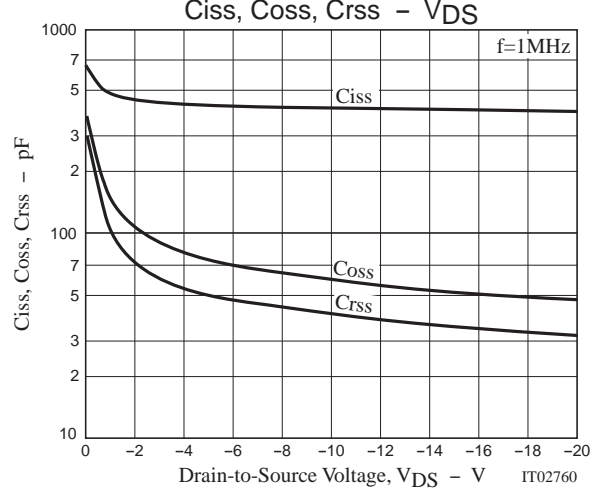
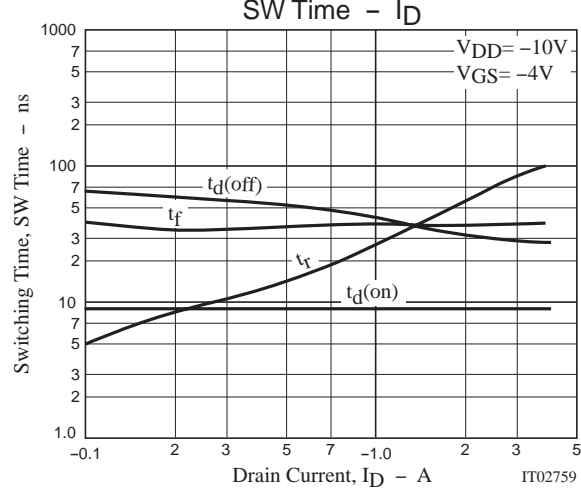
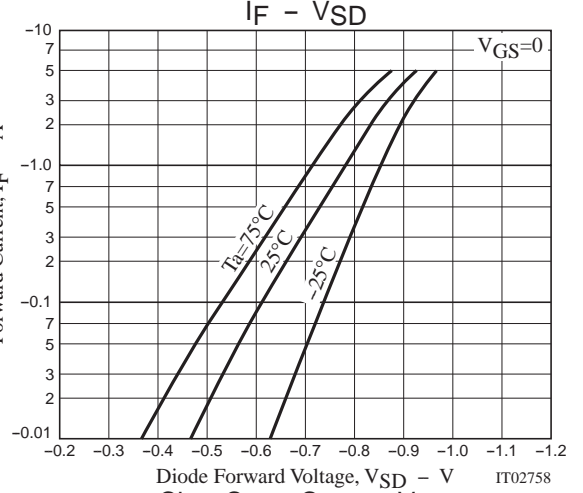
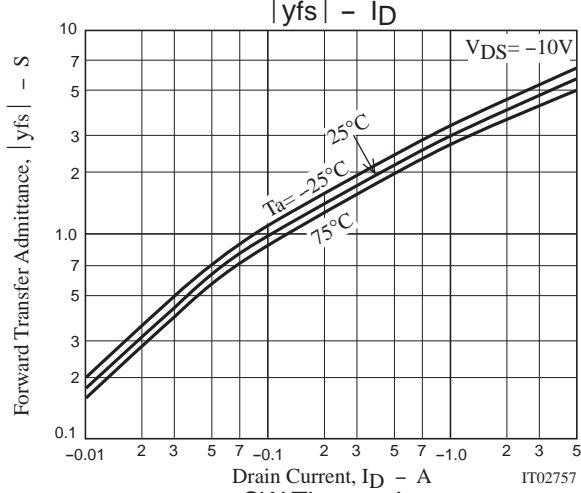
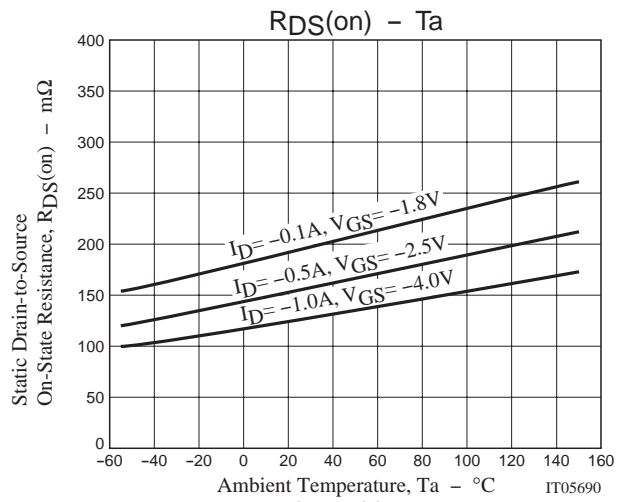
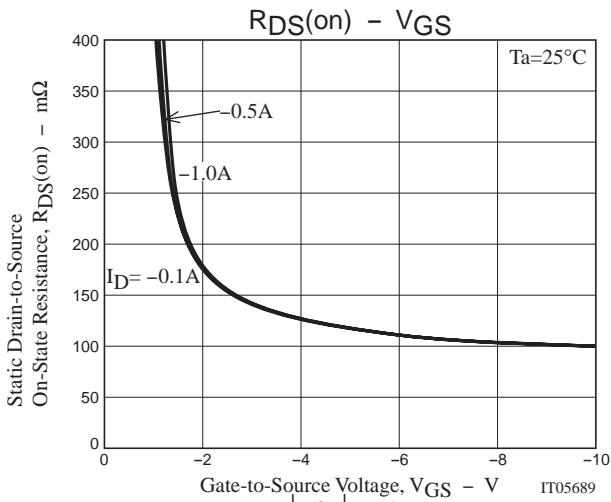
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =-1A, V _{GS} =-4V		125	165	mΩ
	R _{DS(on)2}	I _D =-0.5A, V _{GS} =-2.5V		155	220	mΩ
	R _{DS(on)3}	I _D =-0.1A, V _{GS} =-1.8V		195	280	mΩ
Input Capacitance	C _{iss}	V _{DS} =-10V, f=1MHz		410		pF
Output Capacitance	C _{oss}	V _{DS} =-10V, f=1MHz		60		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =-10V, f=1MHz		40		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		9		ns
Rise Time	t _r	See specified Test Circuit.		27		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.		42		ns
Fall Time	t _f	See specified Test Circuit.		38		ns
Total Gate Charge	Q _g	V _{DS} =-10V, V _{GS} =-10V, I _D =-2A		10		nC
Gate-to-Source Charge	Q _{gs}	V _{DS} =-10V, V _{GS} =-10V, I _D =-2A		0.6		nC
Gate-to-Drain "Miller" Charge	Q _{gd}	V _{DS} =-10V, V _{GS} =-10V, I _D =-2A		1.2		nC
Diode Forward Voltage	V _{SD}	I _S =-2A, V _{GS} =0		-0.88	-1.2	V

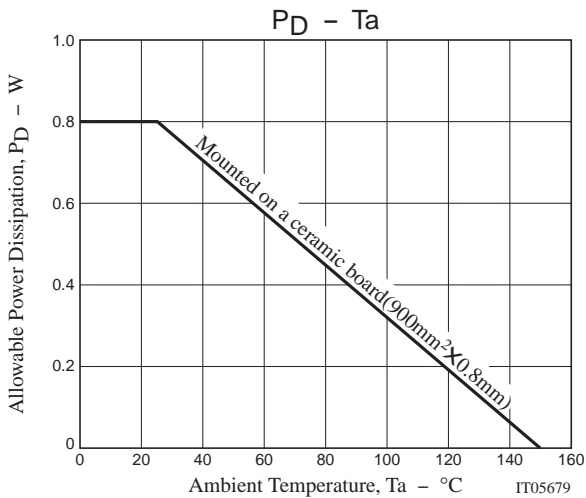
Switching Time Test Circuit



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