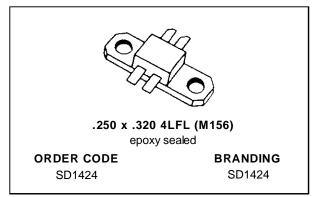
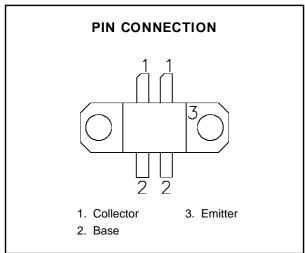


SD1424

RF & MICROWAVE TRANSISTORS 800-900 MHz BASE STATION APPLICATIONS

- 800 900 MHz
- 24 VOLTS
- **COMMON EMITTER**
- GOLD METALLIZATION
- INTERNAL INPUT MATCHING
- CLASS AB LINEAR OPERATION
- Pout = 30 W MIN. WITH 7.5 dB GAIN





DESCRIPTION

The SD1424 is a gold metallized epitaxial silicon NPN planar transistor using diffused emitter ballast resistors for high linearity Class AB operation in cellular base station application.

ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter Va		Unit
Vcво	Collector-Base Voltage	48	V
V _{CES}	Collector-Emitter Voltage 45		V
V _{EBO}	Emitter-Base Voltage	4.0	V
Ic	Device Current	4	А
Poiss	Power Dissipation	87.5	W
TJ	Junction Temperature +200		°C
T _{STG}	Storage Temperature	- 65 to +150	°C

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance	2.0	°C/W
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ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.			
BV _{CBO}	I _C = 50mA	$I_{E} = 0mA$		48	50	_	V
BV _{CEO}	I _C = 20mA	$I_B = 0mA$		25	30	_	V
BV _{EBO}	I _E = 5mA	I _C = 0mA		3.5	4.0	_	V
Ісво	Vcb = 24V	IE = 0mA		_	_	1.0	mA
h _{FE}	V _{CE} = 10V	$I_C = 100 \text{mA}$		20	_	100	_

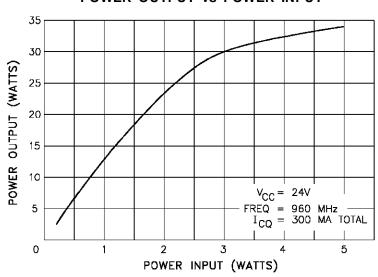
DYNAMIC

Symbol	Test Conditions			Value			Unit
Symbol				Min.	Тур.	Max.	Unit
Pout	f = 960 MHz	$P_{IN} = 5.3 \text{ W}$	$V_{CC} = 24 \text{ V}$	30	_		W
G _P	f = 960 MHz	$P_{OUT} = 30 \text{ W}$	$V_{CC} = 24 \text{ V}$	7.5	_	_	dB
ης	f = 960 MHz	Pout = 30 W	V _C C = 24 V	45	50	_	%
Сов	f = 1 MHz	V _{CB} = 24 V	(each side)	_	20	24	pF

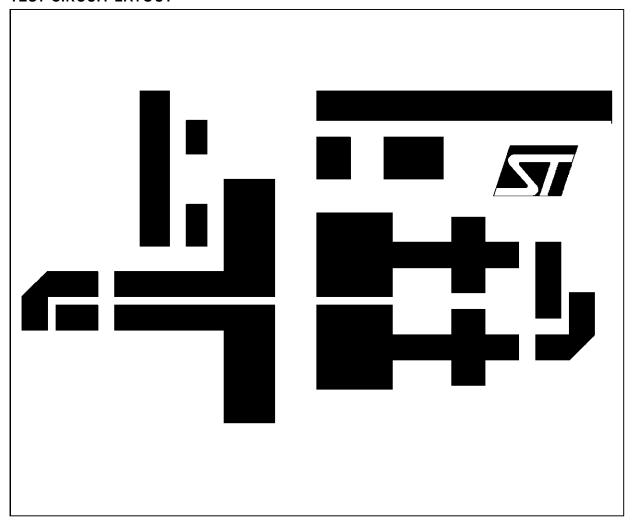
Note: $I_{CQ} = 150 \text{mA}$

TYPICAL PERFORMANCE

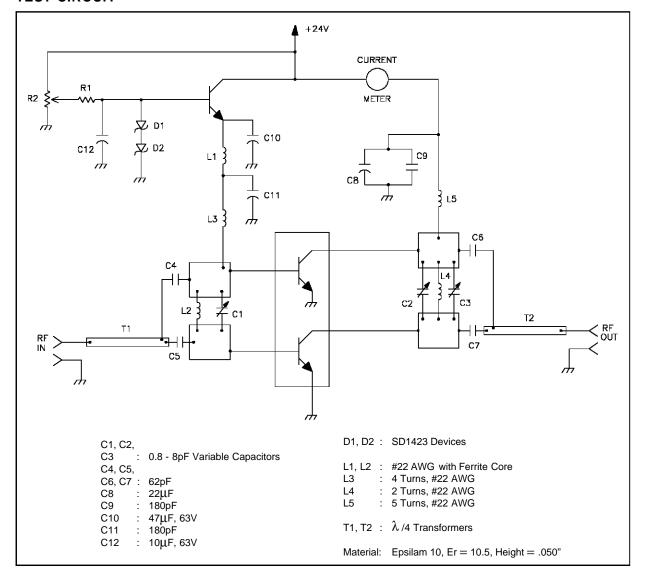
POWER OUTPUT vs POWER INPUT



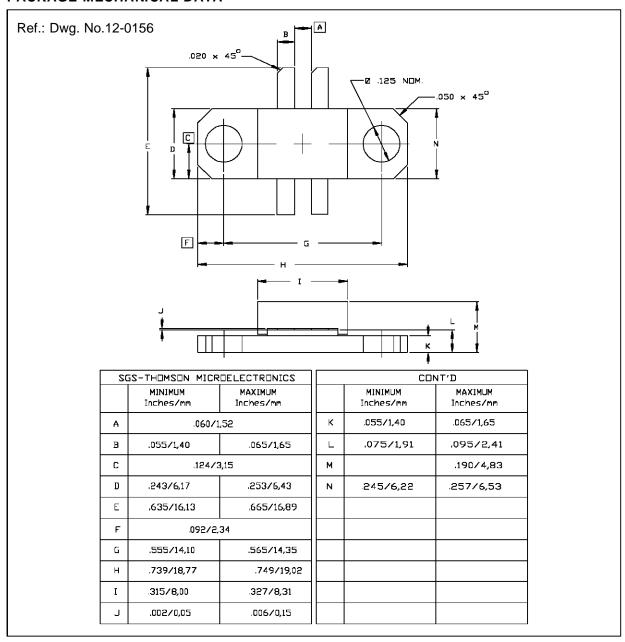
TEST CIRCUIT LAYOUT



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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