TOSHIBA THYRISTOR SILICON PLANAR TYPE

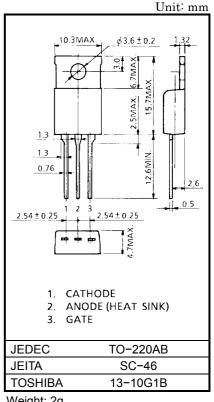
SF10G41A,SF10J41A

MEDIUM POWER CONTROL APPLICATIONS

• Repetitive Peak Off–State Voltage : $V_{DRM} = 400,600V$ Repetitive Peak Reverse Voltage $: V_{RRM} = 400,600V$ • Average On-State Current $: I_{T}(AV) = 10A$ Gate Trigger Current : IGT = 15mA (Max.)

MAXIMUM RATINGS

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage and Repetitive Peak Reverse Voltage	SF10G41A	V_{DRM}	400	V	
	SF10J41A	V_{RRM}	600		
Non-Repetitive Peak Reverse Voltage (Non-Repetitive<5ms, T _j = 0~125°C)	SF10G41A	V _{RSM}	500	V	
	SF10J41A		720	v	
Average On-State Current (Half Sine Waveform Tc = 79°C)		I _{T (AV)}	10	А	
R.M.S On-State Current		I _{T (RMS)}	16	Α	
Peak One Cycle Surge On-State Current (Non-Repetitive)		I _{TSM}	160 (50Hz)	Α	
			176 (60Hz)		
I ² t Limit Value		I ² t	125	A ² s	
Critical Rate of Rise of On-State Curret		di / dt	100	A / μs	
Peak Gate Power Dissipation		P_{GM}	5	W	
Average Gate Power Dissipation		P _{G (AV)}	0.5	W	
Peak Forward Gate Voltage		V_{FGM}	10	V	
Peak Reverse Gate Voltage		V_{RGM}	-5	٧	
Peak Forward Gate Current		I _{GM}	2	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Ra	ange	T _{stg}	-40~125	°C	



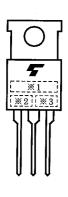
Weight: 2g



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

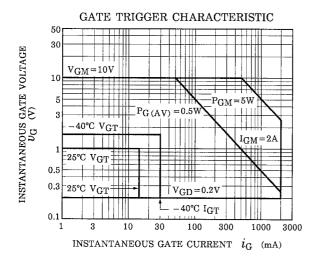
CHARACTERISTIC	SYMBOL	TEST CONDITION		MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated		10	μA
Peak On-State Voltage	V _{TM}	I _{TM} = 30A		1.6	V
Gate Trigger Voltage	V_{GT}	$V_D = 6V, R_I = 10\Omega$	_	1.0	V
Gate Trigger Current	I _{GT}	ν _D = 0ν, κ _L = 10Ω	_	15	mA
Gate Non-Trigger Voltage	V_{GD}	V _D = Rated × 2 / 3, Tc = 125°C	0.2	_	V
Critical Rate of Rise of Off-State Voltage	dv / dt	V _{DRM} = Rated × 2 / 3, Tc = 125°C Exponential Rise		_	V / µs
Holding Current	lΗ	V _D = 6V, I _{TM} = 1A		40	mA
Latching Current	ΙL	V _D = 6V, f = 50Hz, t _{gw} = 50μS, i _G = 30mA		60	mA
Thermal Resistance	R _{th (j-c)}	Junction to Case	_	2.0	°C/W

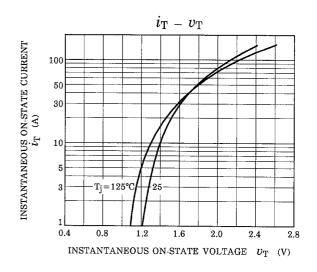
MARKING

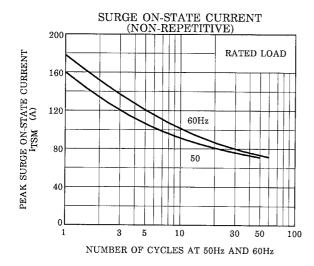


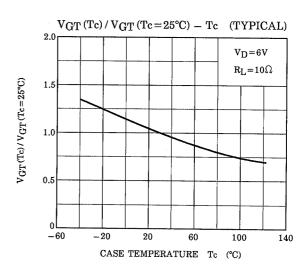
NUMBER	SYMBOL		MARK
*1		SF10G41A	SF10G41
	TYPE	SF10J41A	SF10J41
*2		SF10G41A, SF10J41A	A
*3		(Starting from Alphabet A) (Last Decimal Digit of the Current Year)	Example 8A : January 1998 8B : February 1998 8L : December 1998

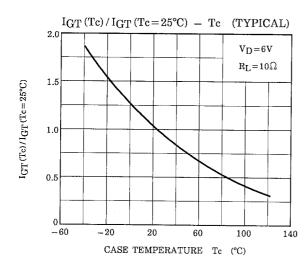
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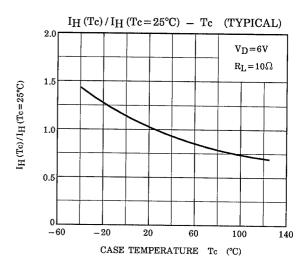


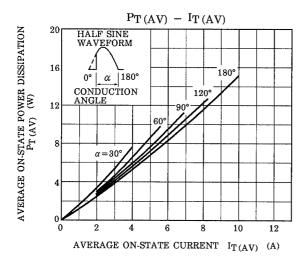


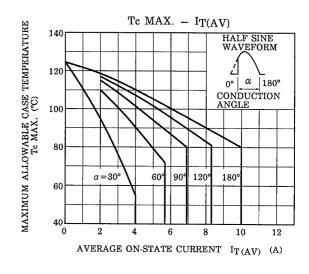


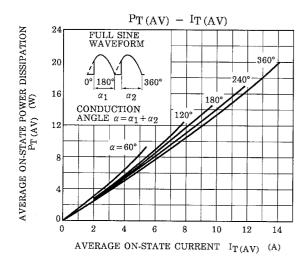


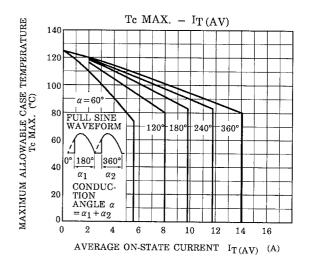


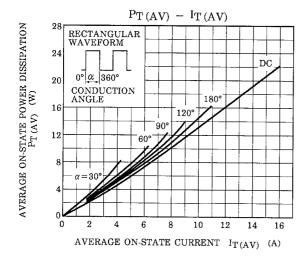


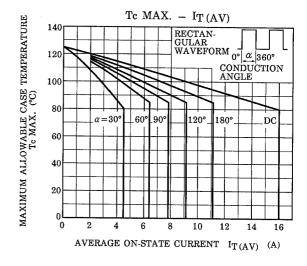




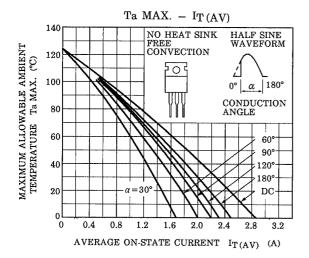


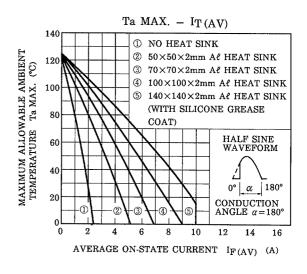


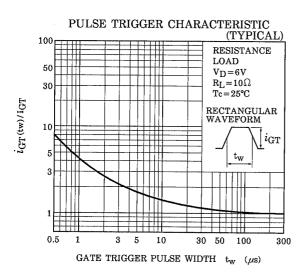


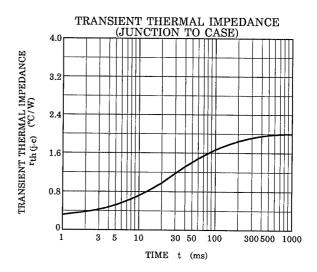


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