

PRELIMINARY SFH054

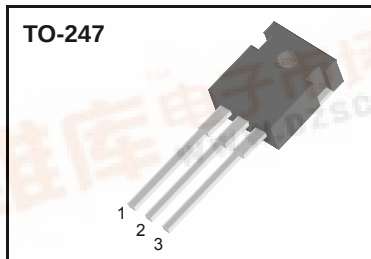
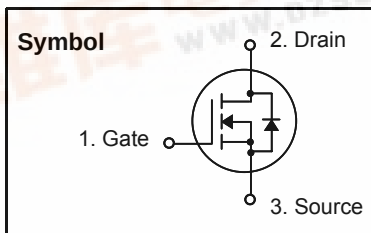
N-Channel MOSFET

Features

- Low $R_{DS(on)}$ (0.014Ω) @ $V_{GS}=10V$
- Low Gate Charge (Typical 70nC)
- Low C_{rss} (Typical 160pF)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Maximum Junction Temperature Range ($175^{\circ}C$)

General Description

This Power MOSFET is produced using SemiWell's advanced planar stripe, DMOS technology. This latest technology has been especially designed to minimize on-state resistance, have a low gate charge with superior switching performance, and rugged avalanche characteristics. This Power MOSFET is well suited for synchronous DC-DC Converters and Power Management in portable and battery operated products.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain to Source Voltage	60	V
I_D	Continuous Drain Current(@ $T_C = 25^{\circ}C$)	70*	A
	Continuous Drain Current(@ $T_C = 100^{\circ}C$)	64	A
I_{DM}	Drain Current Pulsed (Note 1)	360	A
V_{GS}	Gate to Source Voltage	± 25	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	800	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	7.0	V/ns
P_D	Total Power Dissipation(@ $T_C = 25^{\circ}C$)	230	W
	Derating Factor above $25^{\circ}C$	1.5	W/ $^{\circ}C$
T_{STG}, T_J	Operating Junction Temperature & Storage Temperature	- 55 ~ 175	$^{\circ}C$
T_L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.	300	$^{\circ}C$

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	-	-	0.65	$^{\circ}C/W$
$R_{\theta CS}$	Thermal Resistance, Case to Sink	-	0.24	-	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	-	-	40	$^{\circ}C/W$

SFH054

Electrical Characteristics (T_C = 25 °C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250uA	60	-	-	V
Δ BV _{DSS} / Δ T _J	Breakdown Voltage Temperature coefficient	I _D = 1mA, referenced to 25 °C	-	0.056	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1	uA
		V _{DS} = 48V, T _C = 150 °C	-	-	10	uA
I _{GSS}	Gate-Source Leakage, Forward	V _{GS} = 25V, V _{DS} = 0V			100	nA
	Gate-Source Leakage, Reverse	V _{GS} = -25V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	2.0	-	4.0	V
R _{DS(ON)}	Static Drain-Source On-state Resistance	V _{GS} =10 V, I _D = 35A	-	-	0.014	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0 V, V _{DS} =25V, f = 1MHz	-	2350	3050	pF
C _{oss}	Output Capacitance		-	690	890	
C _{rss}	Reverse Transfer Capacitance		-	160	200	
Dynamic Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =30V, I _D =35A, R _G =50Ω * see fig. 13. (Note 4, 5)	-	30	70	ns
t _r	Rise Time		-	60	130	
t _{d(off)}	Turn-off Delay Time		-	125	260	
t _f	Fall Time		-	95	200	
Q _g	Total Gate Charge	V _{DS} =48V, V _{GS} =10V, I _D =70A * see fig. 12. (Note 4, 5)	-	70	90	nC
Q _{gs}	Gate-Source Charge		-	18	-	
Q _{gd}	Gate-Drain Charge(Miller Charge)		-	24	-	

Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit.
I _S	Continuous Source Current	Integral Reverse p-n Junction Diode in the MOSFET	-	-	70	A
I _{SM}	Pulsed Source Current		-	-	360	
V _{SD}	Diode Forward Voltage	I _S = 70A, V _{GS} = 0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time	I _S = 70A, V _{GS} = 0V, dI _F /dt = 100A/us	-	62	-	ns
Q _{rr}	Reverse Recovery Charge		-	110	-	nC

* NOTES

1. Repeatability rating : pulse width limited by junction temperature
2. L = 250 uH, I_{AS} = 70A, V_{DD} = 25V, R_G = 0Ω, Starting T_J = 25°C
3. I_{SD} ≤ 70A, di/dt ≤ 300A/us, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse Width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially independent of operating temperature.

* Current limited by package, (Die Current=90A)



Fig 1. On-State Characteristics

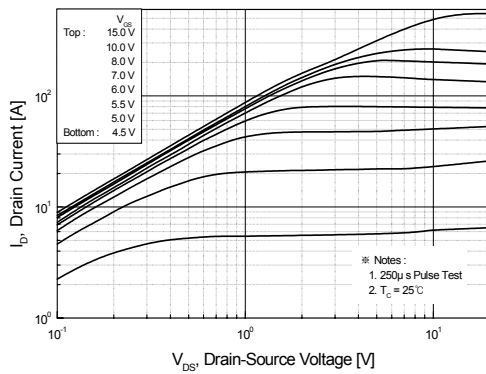


Fig 2. Transfer Characteristics

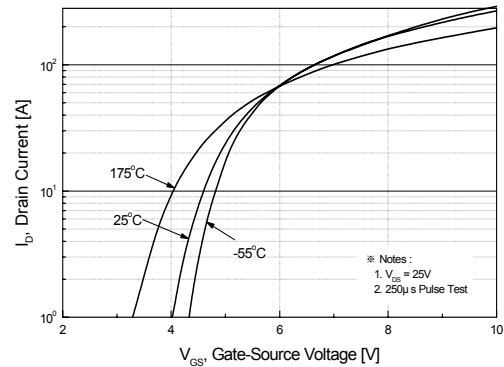


Fig 3. On Resistance Variation vs. Drain Current and Gate Voltage

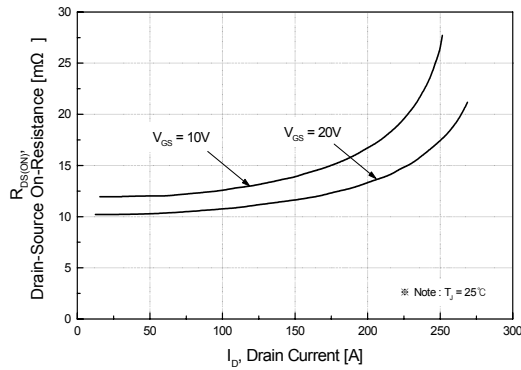


Fig 4. On State Current vs. Allowable Case Temperature

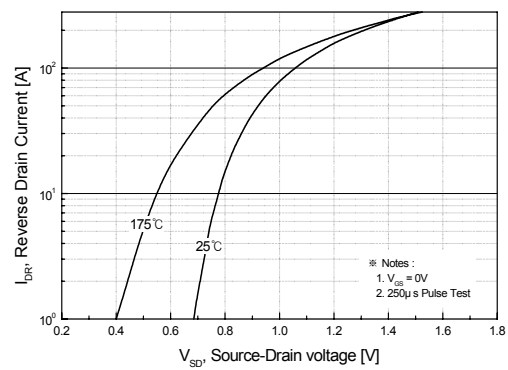


Fig 5. Capacitance Characteristics

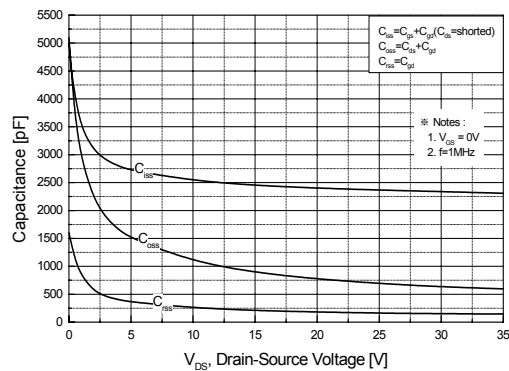
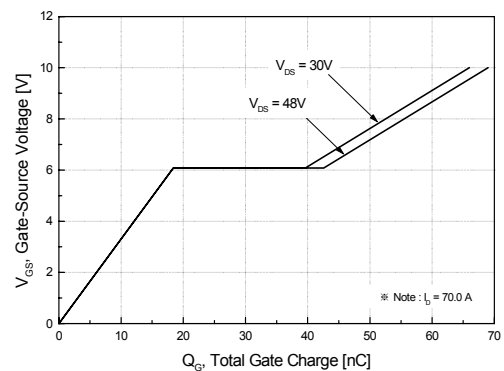


Fig 6. Gate Charge Characteristics



SFH054

Fig 7. Breakdown Voltage Variation vs. Junction Temperature

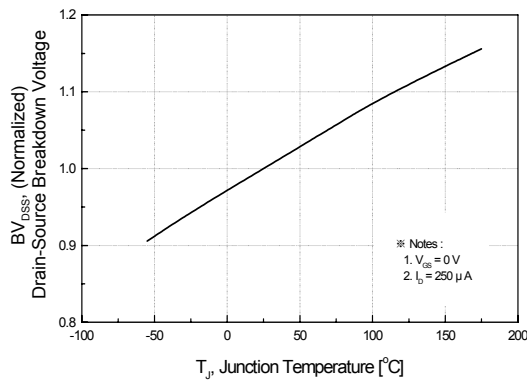


Fig 8. On-Resistance Variation vs. Junction Temperature

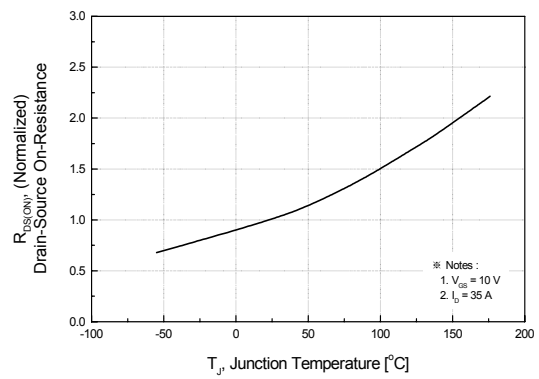


Fig 9. Maximum Safe Operating Area

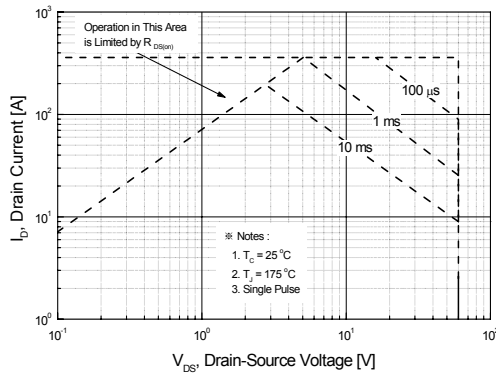


Fig 10. Maximum Drain Current vs. Case Temperature

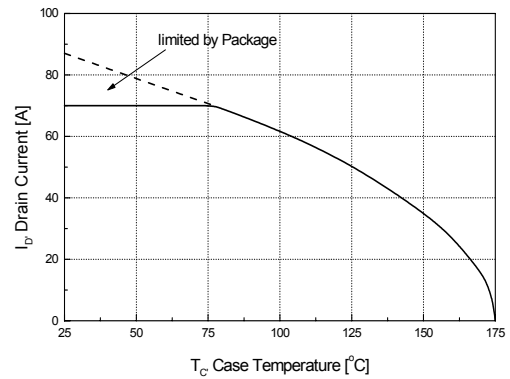


Fig 11. Transient Thermal Response Curve

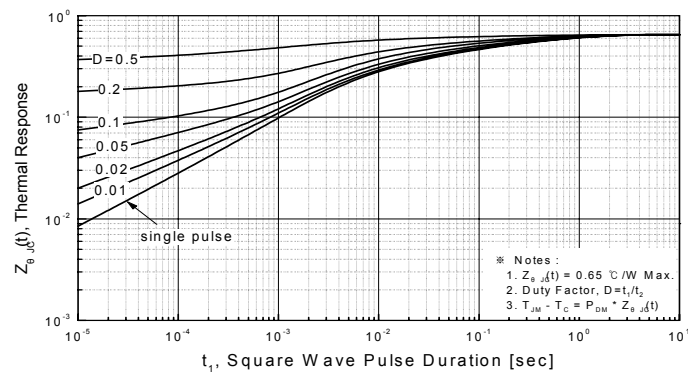


Fig. 12. Gate Charge Test Circuit & Waveforms

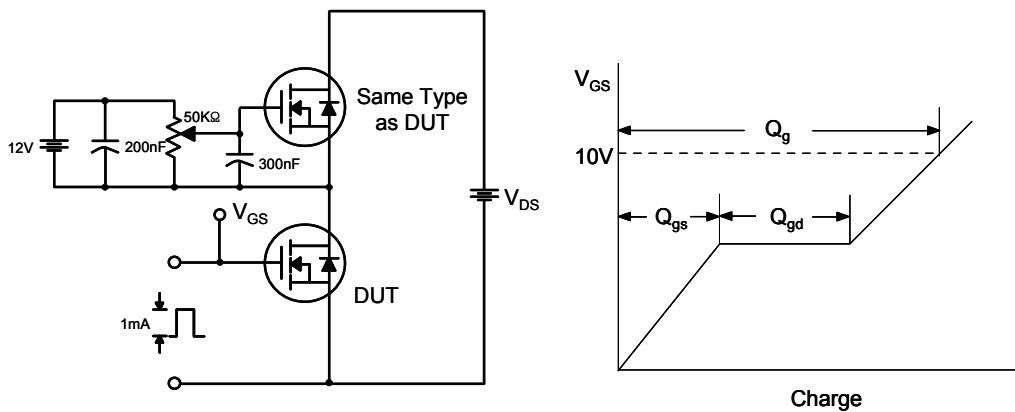


Fig 13. Switching Time Test Circuit & Waveforms

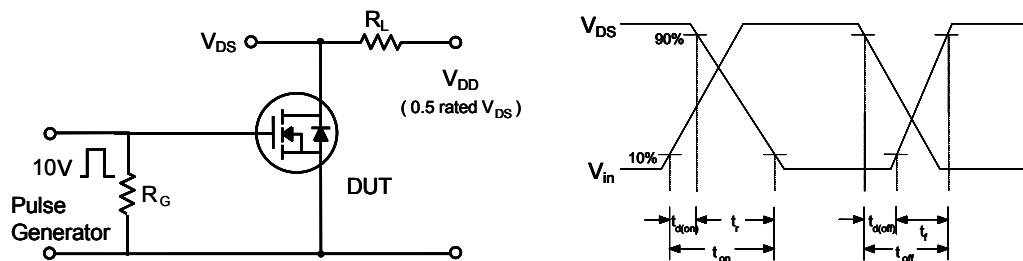
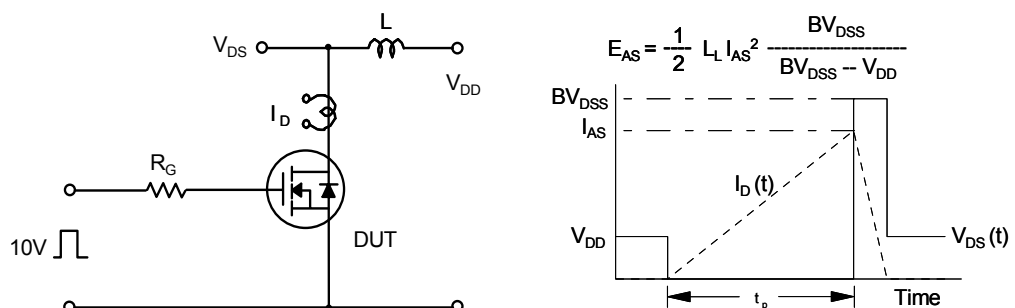
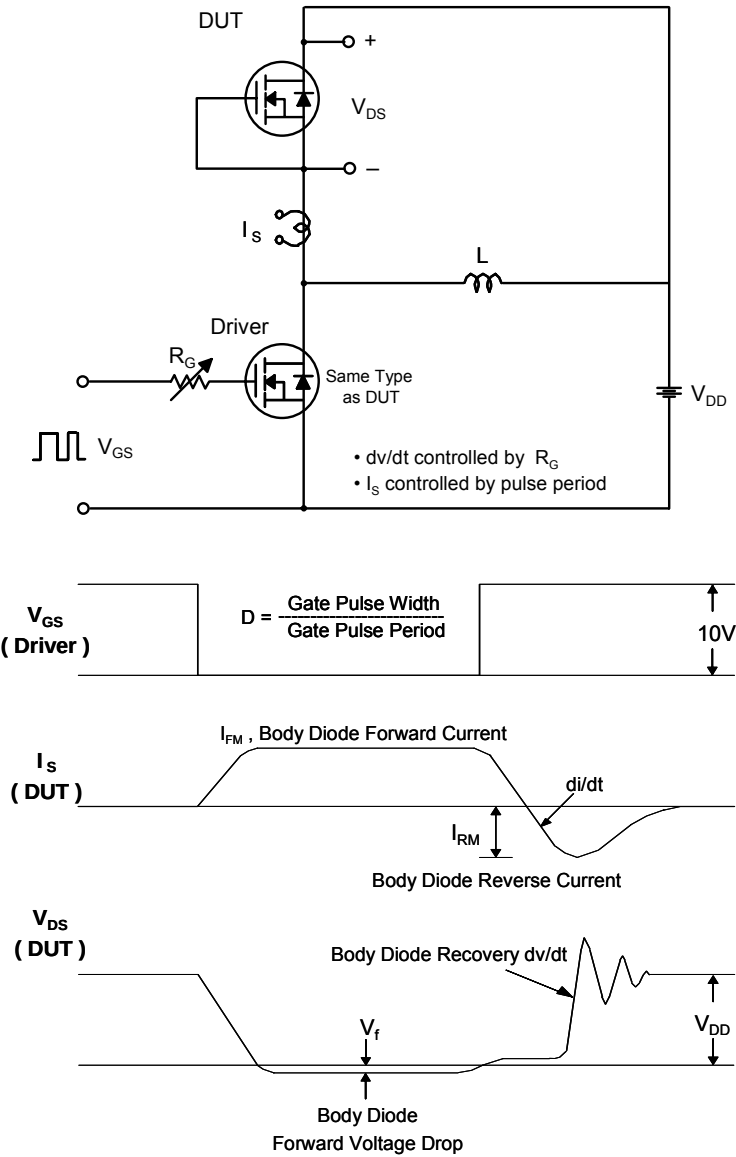


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms



SFH054

Fig. 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



SFH054

TO-247 Package Dimension

Dim.	mm			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.77		16.03	0.621		0.631
B	20.80		21.10	0.819		0.831
C	20.05		20.31	0.789		0.800
D	4.48		4.58	0.176		0.180
E	4.27		4.37	0.168		0.172
F	5.32		5.58	0.209		0.220
G	4.90		5.16	0.193		0.203
H	1.90		2.06	0.075		0.081
I	2.35		2.45	0.093		0.096
J		0.6			0.024	
K	1.2		1.33	0.047		0.052
L	1.07		1.33	0.042		0.052
M	2.99		3.25	0.118		0.128
ϕ	3.56		3.66	0.140		0.144

