

# SGL50N60RUF

# **Short Circuit Rated IGBT**

# **General Description**

Fairchild's RUF series of Insulated Gate Bipolar Transistors (IGBTs) provide low conduction and switching losses as well as short circuit ruggedness. The RUF series is designed for applications such as motor control, uninterrupted power supplies (UPS) and general inverters where short circuit ruggedness is a required feature.

# **Features**

- Short circuit rated 10us @  $T_C = 100$ °C,  $V_{GE} = 15$ V
- High speed switching
- Low saturation voltage :  $V_{CE(sat)} = 2.2 \text{ V} @ I_C = 50 \text{A}$
- High input impedance

# **Applications**

AC & DC motor controls, general purpose inverters, robotics, and servo controls.



TO-264



# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

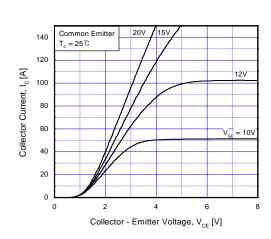
| Symbol              | Description  | 1900                     | SGL50N60RUF | Units |
|---------------------|--|--------------------------|-------------|-------|
| V <sub>CES</sub>    | Collector-Emitter Voltage  | GN//61 7                 | 600         | V     |
| V <sub>GES</sub>    | Gate-Emitter Voltage   | 11111                    | ± 20        | V     |
| I <sub>C</sub>      | Collector Current  | $@ T_C = 25^{\circ}C$    | 80          | Α     |
|                     | Collector Current  | @ T <sub>C</sub> = 100°C | 50          | Α     |
| I <sub>CM (1)</sub> | Pulsed Collector Current   |                          | 150         | А     |
| T <sub>SC</sub>     | Short Circuit Withstand Time   | @ T <sub>C</sub> = 100°C | 10          | us    |
| T <sub>SC</sub>     | Maximum Power Dissipation  | $@ T_C = 25^{\circ}C$    | 250         | W     |
|                     | Maximum Power Dissipation  | @ T <sub>C</sub> = 100°C | 100         | W     |
| T <sub>J</sub>      | Operating Junction Temperature   |                          | -55 to +150 | °C    |
| T <sub>stg</sub>    | Storage Temperature Range  |                          | -55 to +150 | °C    |
| T <sub>L</sub>      | Maximum Lead Temp. for Soldering Purposes,1/8" from Case for 5 Seconds |                          | 300         | °C    |

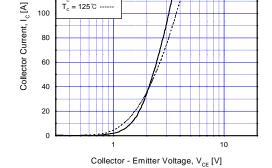
Notes:
(1) Repetitive rating: Pulse width limited by max. junction temperature

# Thermal Characteristics

| Symbol          | Parameter                               | Тур. | Max. | Units |
|-----------------|---|------|------|-------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case    |      | 0.5  | °C/W  |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient |      | 25   | °C/W  |

| Symbol   | Parameter                                    | Test Conditions   | Min. | Тур.     | Max.      | Units    |
|--|--|---|------|----------|-----------|----------|
| Off Cha  | racteristics                                 |   |      |          |           |          |
| BV <sub>CES</sub>                                    | Collector-Emitter Breakdown Voltage          | $V_{GE} = 0V, I_{C} = 250uA$  | 600  |          |           | V        |
| $\Delta B_{VCES}/$ $\Delta T_J$                      | Temperature Coefficient of Breakdown Voltage | V <sub>GE</sub> = 0V, I <sub>C</sub> = 1mA                                  |      | 0.6      |           | V/°C     |
| I <sub>CES</sub>                                     | Collector Cut-Off Current                    | $V_{CE} = V_{CES}, V_{GE} = 0V$   |      |          | 250       | uA       |
| I <sub>GES</sub>                                     | G-E Leakage Current                          | $V_{GE} = V_{GES}, V_{CE} = 0V$   |      |          | ± 100     | nA       |
| On Char  | racteristics                                 |   |      |          |           |          |
| V <sub>GE(th)</sub>                                  | G-E Threshold Voltage                        | Ic = 50mA, V <sub>CE</sub> = V <sub>GE</sub>                                | 5.0  | 6.0      | 8.5       | V        |
|  | Collector to Emitter                         | $I_C = 50A$ , $V_{GE} = 15V$  |      | 2.2      | 2.8       | V        |
| $V_{CE(sat)}$  | Saturation Voltage                           | $I_C = 80A$ , $V_{GE} = 15V$  |      | 2.5      |           | V        |
|  | c Characteristics                            |   |      |          |           |          |
| C <sub>ies</sub>                                     | Input Capacitance                            | $V_{CE} = 30 V_{VGE} = 0 V_{OE}$  |      | 3311     |           | pF       |
| C <sub>oes</sub>                                     | Output Capacitance                           | f = 1MHz  |      | 399      |           | pF       |
| C <sub>res</sub>                                     | Reverse Transfer Capacitance                 | . –2  |      | 139      |           | pF       |
| t <sub>d(on)</sub>                                   | Turn-On Delay Time                           |   |      | 26       |           | ns       |
|  | Rise Time                                    | -   |      | 89       |           | ns       |
| t <sub>r</sub>                                       | Turn-Off Delay Time                          | $V_{CC} = 300 \text{ V}, I_{C} = 50\text{A},$                               |      | 66       | 100       | ns       |
| t <sub>d(off)</sub><br>t <sub>f</sub>                | Fall Time                                    | $R_{G} = 5.9\Omega, V_{GE} = 15V,$  |      | 118      | 200       | ns       |
| Կ<br>E <sub>on</sub>                                 | Turn-On Switching Loss                       | Inductive Load, $T_C = 25^{\circ}C$   |      | 1.68     |           | mJ       |
| _  | Turn-Off Switching Loss                      |   |      | 1.03     |           | mJ       |
| E <sub>off</sub><br>E <sub>ts</sub>                  | Total Switching Loss                         | -   |      | 2.71     | 3.8       | mJ       |
|  | Turn-On Delay Time                           |   |      | 28       |           | ns       |
| t <sub>d(on)</sub>                                   | Rise Time                                    | -   |      | 91       |           | ns       |
| t <sub>r</sub>                                       | Turn-Off Delay Time                          | $V_{CC} = 300 \text{ V}, I_{C} = 50\text{A},$                               |      | 68       | 110       | ns       |
| t <sub>d(off)</sub><br>t.                            | Fall Time                                    | $R_{G} = 5.9\Omega, V_{GE} = 15V,$  |      | 261      | 400       | ns       |
| t <sub>f</sub><br>E <sub>on</sub>                    | Turn-On Switching Loss                       | Inductive Load, T <sub>C</sub> = 125°C                                      |      | 1.7      |           | mJ       |
| ∟ <sub>on</sub><br>E <sub>off</sub>                  | Turn-Off Switching Loss                      |   |      | 2.31     |           | mJ       |
| E <sub>ts</sub>                                      | Total Switching Loss                         | -   |      | 4.01     | 5.62      | mJ       |
| T <sub>sc</sub>                                      | Short Circuit Withstand Time                 | V <sub>CC</sub> = 300 V, V <sub>GE</sub> = 15V,<br>@ T <sub>C</sub> = 100°C | 10   |          |           | us       |
| ^  | Total Gate Charge                            |   |      | 145      | 210       | nC       |
| U~   |  | $V_{CE} = 300 \text{ V}, I_{C} = 50 \text{A},$                              |      | _        | -         |          |
| Q <sub>g</sub>                                       | Gate-Emitter Charge                          |   |      | 25       | 35        | n(:      |
| Q <sub>g</sub><br>Q <sub>ge</sub><br>Q <sub>gc</sub> | Gate-Emitter Charge Gate-Collector Charge    | $V_{GE} = 350 \text{ V}, 10 = 307 \text{ V}, 10 = 307 \text{ V}$            |      | 25<br>70 | 35<br>100 | nC<br>nC |





140

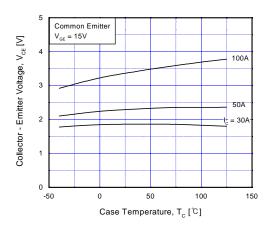
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Common Emitter

V<sub>GE</sub> = 15V T<sub>C</sub> = 25℃

Fig 1. Typical Output Characteristics

Fig 2. Typical Saturation Voltage Characteristics



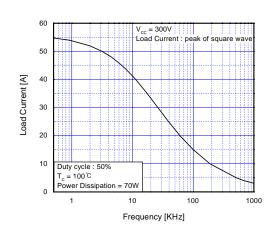
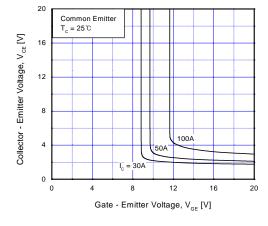


Fig 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

Fig 4. Load Current vs. Frequency



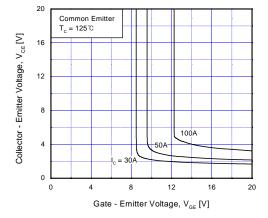
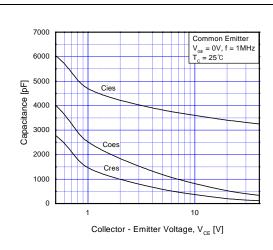


Fig 5. Saturation Voltage vs.  $V_{\text{GE}}$ 

Fig 6. Saturation Voltage vs. V<sub>GE</sub>

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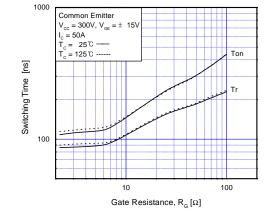
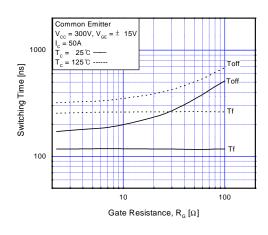


Fig 7. Capacitance Characteristics

Fig 8. Turn-On Characteristics vs.
Gate Resistance



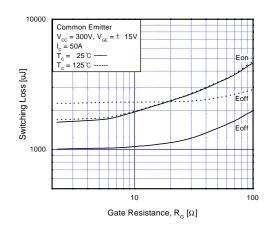
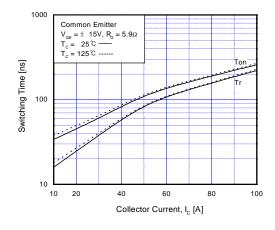


Fig 9. Turn-Off Characteristics vs.
Gate Resistance

Fig 10. Switching Loss vs. Gate Resistance



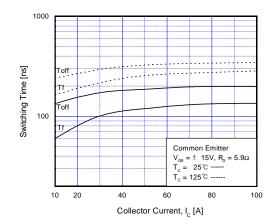
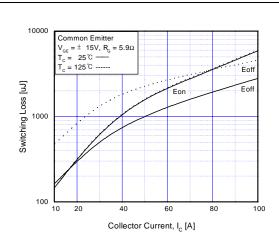


Fig 11. Turn-On Characteristics vs.
Collector Current

Fig 12. Turn-Off Characteristics vs.
Collector Current

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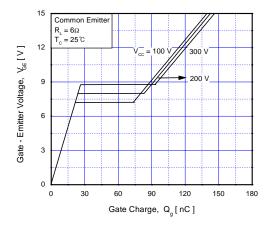
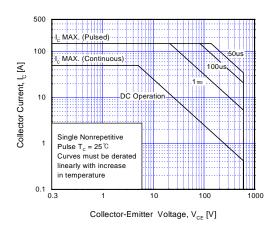


Fig 13. Switching Loss vs. Collector Current

Fig 14. Gate Charge Characteristics



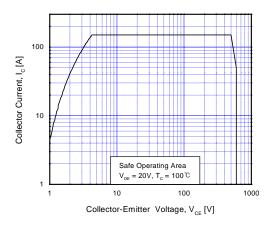


Fig 15. SOA Characteristics

Fig 16. Turn-Off SOA Characteristics

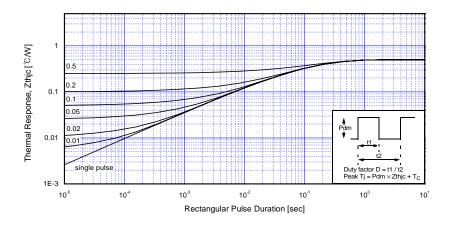
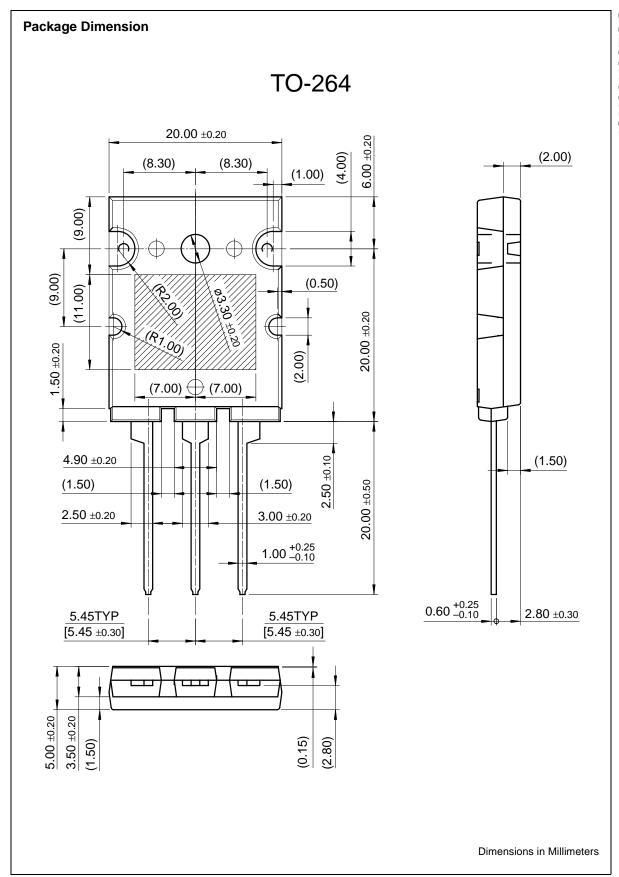


Fig 17. Transient Thermal Impedance of IGBT



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