

SGR20N40L / SGU20N40L

August 2001

IGBT



SGR20N40L / SGU20N40L

General Description

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

Features

- High input impedance
- High peak current capability (150A)
- Easy gate drive
- Surface Mount : SGR20N40L
- Straight Lead : SGU20N40L

Application

Strobe flash.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description	SGR / SGU20N40L	Units
V _{CES}	Collector - Emitter Voltage	400	V
V _{GES}	Gate - Emitter Voltage	± 6	V
I _{CM (1)}	Pulsed Collector Current	150	A
P _C	Maximum Power Dissipation $@ T_C = 25^{\circ}C$	45	W
TJ	Operating Junction Temperature	-40 to +150	°C
T _{stg}	Storage Temperature Range	-40 to +150	°C
TL	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C

Notes : (1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
R _{θJC}	Thermal Resistance, Junction-to-Case		3.0	°C/W
R _{0JA} (D-PAK)	() Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R _{0JA} (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

Notes : (2) Mounted on 1" square PCB (FR4 or G-10 Material)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	450			V
ICES	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			10	μΑ
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			±0.1	μΑ
On Cha	racteristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 1 \text{mA}, V_{CE} = V_{GE}$	0.5	1.0	1.4	V
V _{CE(sat)}	C-E Saturation Current	$I_{C} = 150A, V_{GF} = 4.5V$	2.0	4.5	8.0	V

Dynamic Characteristics

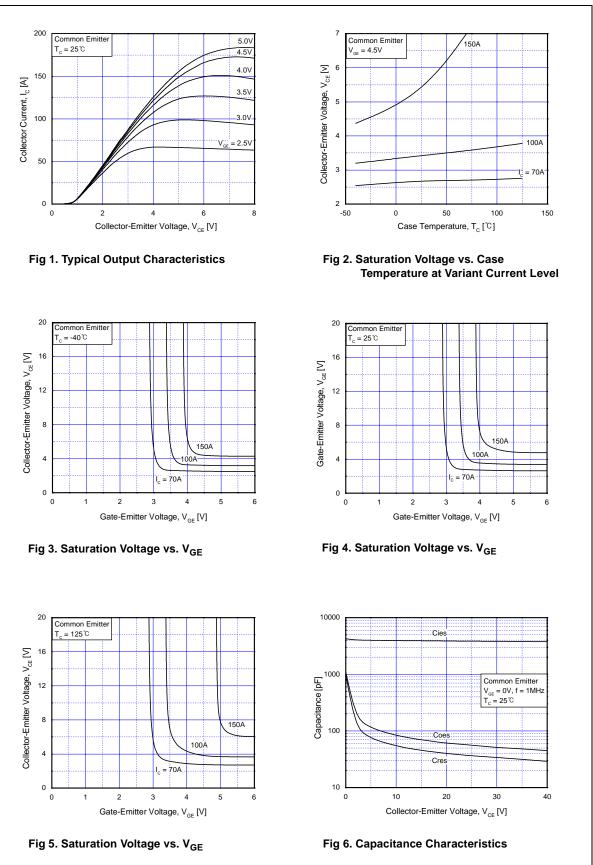
Cies	Input Capacitance	V _{GE} = 0V, V _{CE} = 30V, f = 1MHz	 3800	 pF
C _{oes}	Output Capacitance		 50	 pF
C _{res}	Reverse Transfer Capacitance		 35	 pF

Switching Characteristics

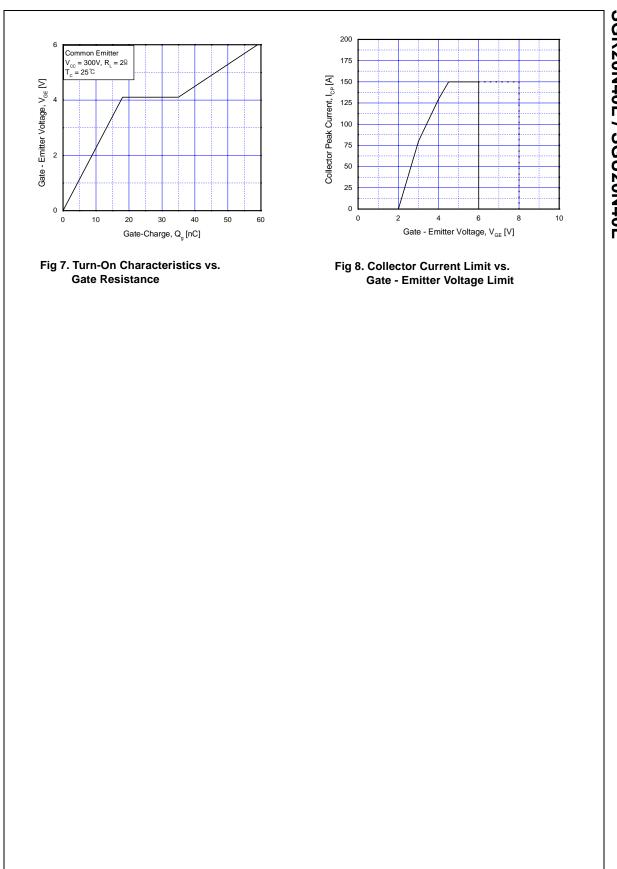
t _{d(on)}	Turn-On Delay Time	- V _{CC} = 300V, I _C = 150A, - V _{GE} = 4.5V, R _G = 15Ω* - Resistive Load	 0.2		μs
t _r	Rise Time		 1.7		μs
t _{d(off)}	Turn-Off Delay Time		 0.3	0.5	μs
t _f	Fall Time		 1.5	2.0	μs

* Notes : Recommendation of R_G Value : $\mathsf{R}_G{\geq}15\Omega$

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