# SI-3033ZD



sceed a junction temperature of  $+125^{\circ}$ C.

# High-Current, Low-Dropout, 3.3 V Regulator

Designed to meet the high-current requirements in industrial and consumer applications; embedded core, memory, or logic supplies; TVs, VCRs, and office equipment, the SI-3033ZD voltage regulator offers the reduced dropout voltage and low quiescent current essential for improved efficiency. This device delivers a regulated output at up to 3 A. Integrated thermal and overcurrent protection enhance overall system reliability. Devices with an adjustable 1.2 ~ 5 V output or a 2.5 V fixed output are also available.

Quiescent current does not increase significantly as the dropout voltage is approached, an ideal feature in standby/resume power systems where data integrity is crucial. Regulator accuracy and excellent temperature characteristics are provided by a bandgap reference. An LS-TTL/CMOS-compatible input gives the designer complete control over power up, standby, or power down. A pnp pass element provides a dropout voltage of less than 600 mV at 3 A of load current. Low output voltages eliminate the need for expensive PWM buck converters. The low dropout voltage permits more efficient regulation before output regulation is lost.

This device is supplied in a 5-lead surface-mount plastic package (TO-263) with ground tab to provide a low-resistance path for maximum heat dissipation. A similar device in a flange-mounted (TO-220-style) high-power package is the SI-3025ZF.

## FEATURES

- 3 A Output Current at 3.3 V
- 0.6 V Maximum Dropout Voltage at  $I_0 = 3 A$
- 1 μA Maximum Standby Current
- Remote Voltage Sensing
- Foldback Current Limiting
- Ground Tab for Superior Heat Dissipation
- Thermal Protection

## APPLICATIONS

- TVs, VCRs, Electronic Games
- Embedded Core, Memory, or Logic Supplies
- Printers and Other Office Equipment
- Industrial Machinery
- Secondary-Side Stabilization of Multi-Output SMPS

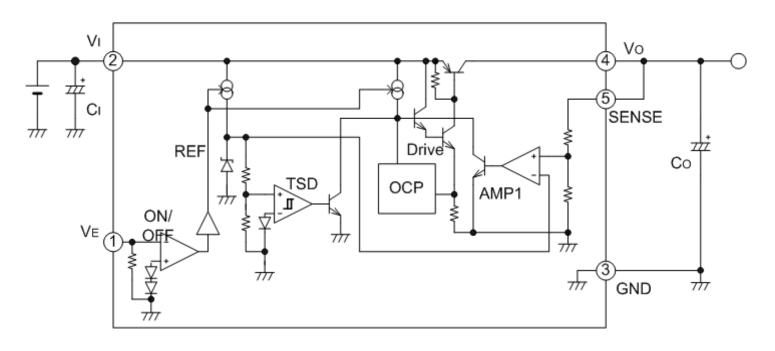
Always order by complete part number, e.g., **SI-3033ZD-TL**, where "-TL" indicates tape and reel.

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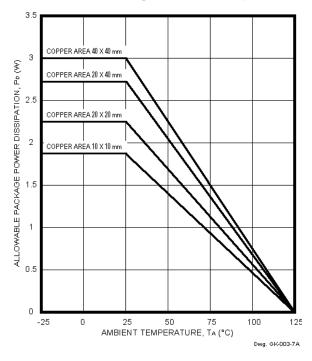




## FUNCTIONAL BLOCK DIAGRAM



#### Allowable Package Power Dissipation



### **Recommended Operating Conditions**

	Min	Max	Units
DC Input Voltage	—	6	V
DC Output Current	0	3	А
Operating Junction Temp.	-20	+100	°C

For the availability of parts meeting -40°C requirements, contact Allegro's Sales Representative.

This data sheet is based on Sanken data sheet SSJ-02590







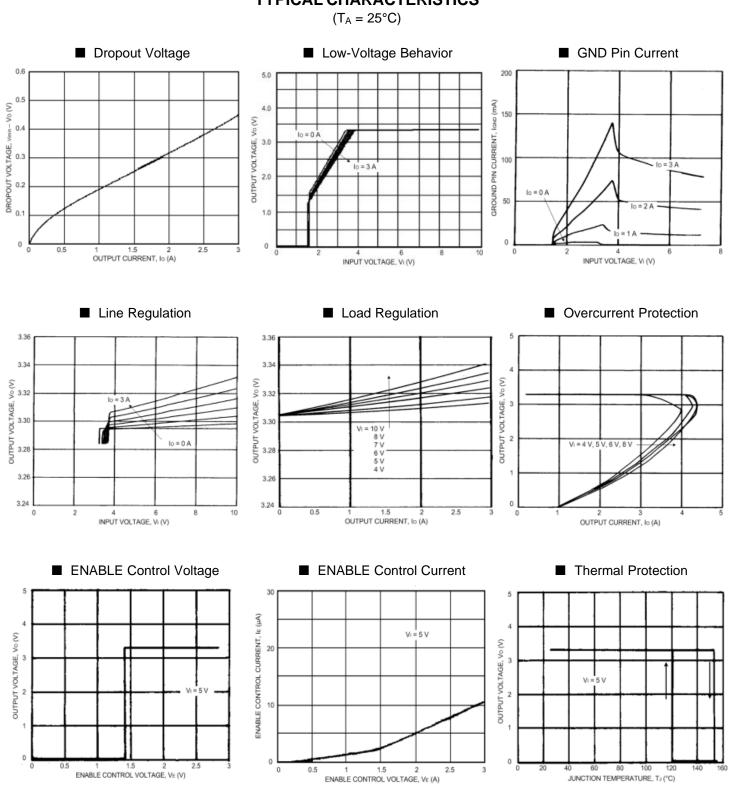
# **ELECTRICAL CHARACTERISTICS** at $T_A = +25^{\circ}$ C, $V_E = 2$ V (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Voltage	Vo	$V_{I} = 5 V, I_{O} = 10 mA$	3.234	3.300	3.366	V
	V <sub>O(off)</sub>	$V_{E} = 0 V$		—	0.5	V
Output Volt. Temp. Coeff.	a <sub>vo</sub>	$0^{\circ}C \leq T_{J} \leq 100^{\circ}C$		±0.3		mV/°C
Output Short-Circuit Current	I <sub>OM</sub>	$V_{I} = 5 V$ , see note	3.2	—	_	А
Line Regulation	$\Delta V_{O(\Delta VI)}$	$V_{I} = 4.5 \sim 5.5 \text{ V}, \text{ I}_{O} = 10 \text{ mA}$		_	10	mV
Load Regulation	$\Delta V_{O(\Delta IO)}$	$V_{I} = 5 V, I_{O} = 0 A \sim 3.0 A$		_	40	mV
Dropout Voltage	V <sub>Imin</sub> - V <sub>O</sub>	I <sub>O</sub> = 3.0 A		_	0.6	V
Ground Terminal Current	I <sub>GND</sub>	$V_{I} = 5 \text{ V}, \text{ I}_{O} = 0 \text{ mA}, \text{ V}_{E} = 2.0 \text{ V}$	_	1.0	1.5	mA
		V <sub>I</sub> = 5 V, V <sub>E</sub> = 0 V			1.0	μA
Enable Input Voltage	V <sub>EH</sub>	Output ON	2.0	_	_	V
	V <sub>EL</sub>	Output OFF			0.8	V
Enable Input Current	I <sub>EH</sub>	V <sub>E</sub> = 2.7 V	_	_	100	μA
	I <sub>EL</sub>	$V_{\rm E} = 0 V$		0	-5.0	μA
Ripple Rejection Ratio	PSRR	$V_{I} = 5 \text{ V}, 100 \text{ Hz} \le f \le 120 \text{ Hz}$		60		dB
Thermal Shutdown	Τ <sub>J</sub>		135	152		°C

Typical values are given for circuit design information only.

Note: Output short-circuit current is at point where output voltage has decreased 5% below  $V_{O(nom)}$ .

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# **TYPICAL CHARACTERISTICS**

Lineal ators Regulators

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JUNCTION TEMPERATURE, TJ (\*C)



## **APPLICATIONS INFORMATION**

**Input Capacitor** ( $C_I$ , 0.1  $\mu$ F to 10  $\mu$ F). This is necessary either when the input line includes inductance or when the wiring is long.

**Output Capacitor** ( $C_0$ , > 47  $\mu$ F). This device is not designed for a use with a very low ESR output capacitor such as a ceramic capacitor. Output oscillation may occur with that kind of capacitor.

**Determination of DC Input Voltage.** The minimum input voltage  $V_I(min)$  should be higher than the sum of the fixed output voltage and the maximum rated dropout voltage.

**Increased Output Voltage.** The output voltage (V<sub>O</sub>) may be increased by inserting a resistor (R<sub>EXT</sub>) between SENSE and OUTPUT. The current flowing into SENSE is typically 90  $\mu$ A ±30%. To minimize the effect of I<sub>SENSE</sub> and temperature on R<sub>EXT</sub>, it is recommended that a 9.1 k $\Omega$  resistor be added between SENSE and GND to increase the current in R<sub>EXT</sub>. The value of R<sub>EXT</sub> is then

$$R_{EXT} = (V_O - 3.3) \times 10^6 / 453$$

**Overcurrent Protection.** The SI-3000ZD series has a built-in fold-back type overcurrent protection circuit, which limits the output current at a start-up mode. It thus cannot be used in applications that require current at the start-up mode such as:

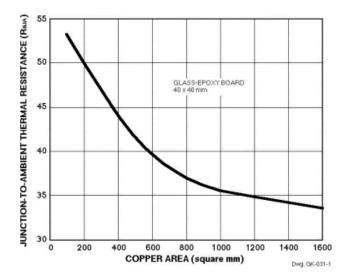
(1) constant-current load,

(2) power supply with positive and negative outputs to common load (a center-tap type power supply), or(3) raising the output voltage by putting a diode or a resistor between the device ground and system ground.

**Thermal Protection.** Circuitry turns off the pass transistor when the junction temperature rises above 135°C. It is intended only to protect the device from failures due to excessive junction temperatures and should not imply that output short circuits or continuous overloads are permitted.

**Heat Radiation and Reliability.** The reliability of the IC is directly related to the junction temperature  $(T_J)$  in its operation. Accordingly, careful consideration should be given to heat dissipation.

The inner frame on which the integrated circuit is mounted is connected to the GND terminal (pin 3). Therefore, it is very effective for heat radiation to enlarge the copper area that is connected to the GND terminal. The graph illustrates the effect of the copper area on the junction-toambient thermal resistance ( $R_{\theta IA}$ ).



The junction temperature  $(T_J)$  can be determined from either of the following equations:

or

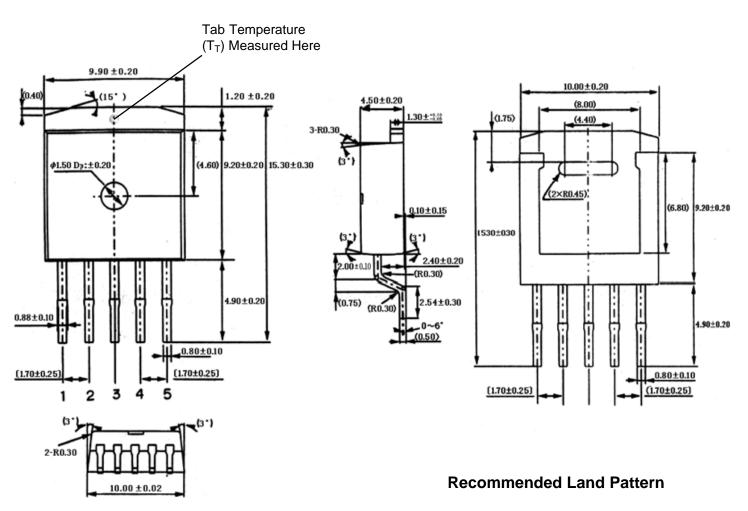
$$\mathbf{T}_{\mathbf{J}} = (\mathbf{P}_{\mathbf{D}} \times \mathbf{R}_{\mathbf{\theta} \mathbf{J} \mathbf{A}}) + \mathbf{T}_{\mathbf{A}}$$

$$\begin{split} T_{J} &= (P_{D} \times R_{\theta JT}) + T_{T} \\ \text{where} \quad P_{D} &= I_{O} \times (V_{I} - V_{O}) \text{ and} \\ R_{\theta JT} &= 3^{\circ} C/W. \end{split}$$

**Parallel Operation.** Parallel operation to increase load current is not permitted.



## **Dimensions in Millimeters**

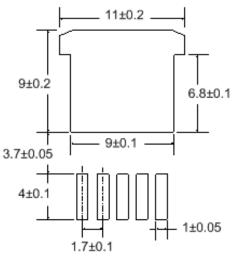


## Notes:

- 1. Dimensions do not include mold protrusions.
- 2. () Is reference.
- 3. [] Is assembly out quality.
- 4. Heat sink side flash: 0.8 mm max.
- 5. Terminal finish: pure Sn, 2<sup>nd</sup> level interconnect category (e3)

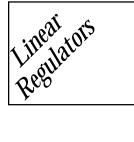
(11) areas

6. Product weight: approximately 1.48 g

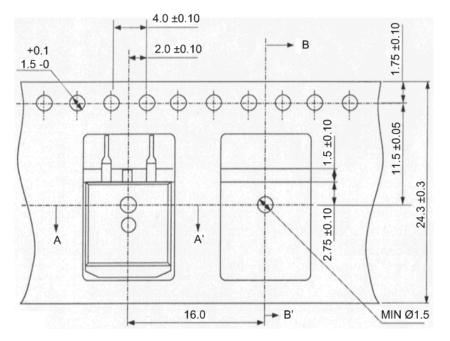


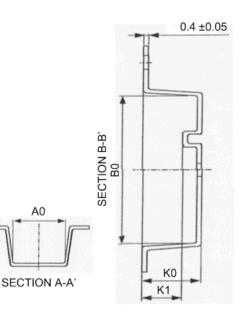


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## **Tape and Reel Dimensions in Millimeters**





## **Tape Specifications**

1. Material: conductive polystyrene.

2. Allowable camber: 1 mm/100 mm maximum.

3. A0 (10.6 mm) and B0 (15.7 mm) measured on a plane 0.3 mm above the bottom of the pocket.

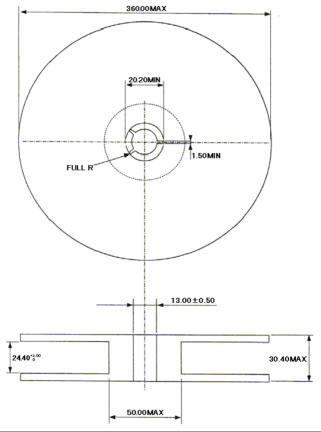
4. K0 (4.9 mm) is measured from a plane on the inside bottom of the pocket to the surface of the carrier.

5. Ten pitches cumulative tolerance on tape  $\pm 0.2$  mm.

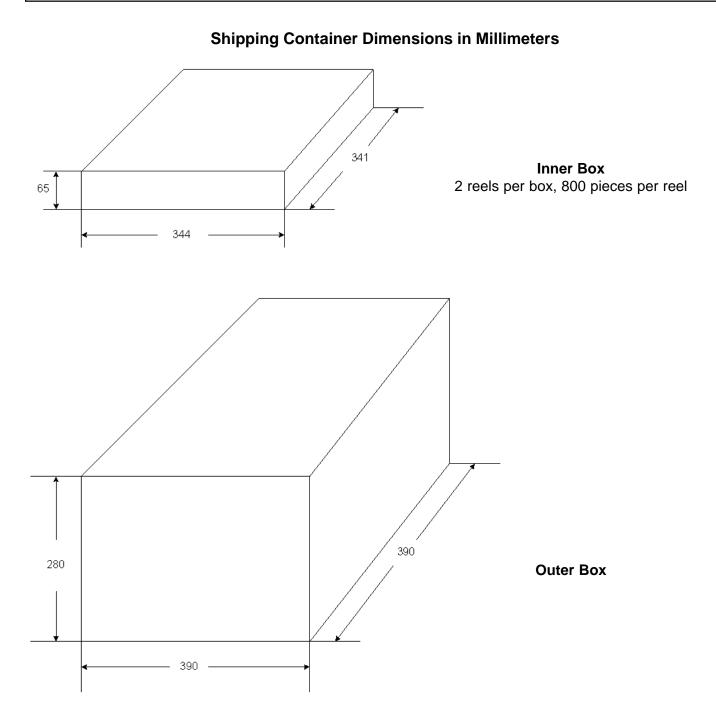
6. The space between parts and cavity must not exceed  $\pm 0.3$  mm from its normal position in any direction.

7. Surface resistivity of embossment:  $10^7$  ohm/cm<sup>2</sup> max.

8. K1 = 3.9 mm.













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