

SI-3000B Series

5-Terminal, Multi-Function, Full-Mold, Low Dropout Voltage Dropper Type

■Features

- Compact full-mold package (equivalent to TO220)
- Output current: 0.27A
- Low dropout voltage: $V_{DIF} \leq 0.5V$ (at $I_o = 0.27A$)
- Output ON/OFF control terminal is compatible with LS-TTL. (It may be directly driven by LS-TTL or standard CMOS logic.)
- Built-in foldback overcurrent, thermal protection circuits
- Highly accurate overcurrent protection starting current
 SI-3157B : 0.3 to 0.7A ($V_{IN} = 18V$)
 SI-3025B : 0.3 to 0.7A (When $V_{IN} = 18V$, setting $V_O = 15.7V$)
 0.3 to 0.75A (When $V_{IN} = 18V$, setting $V_O = 11.7V$)
- Variable output voltage type (SI-3025B) also available



■Applications

- For BS and CS antenna power supplies
- Electronic equipment

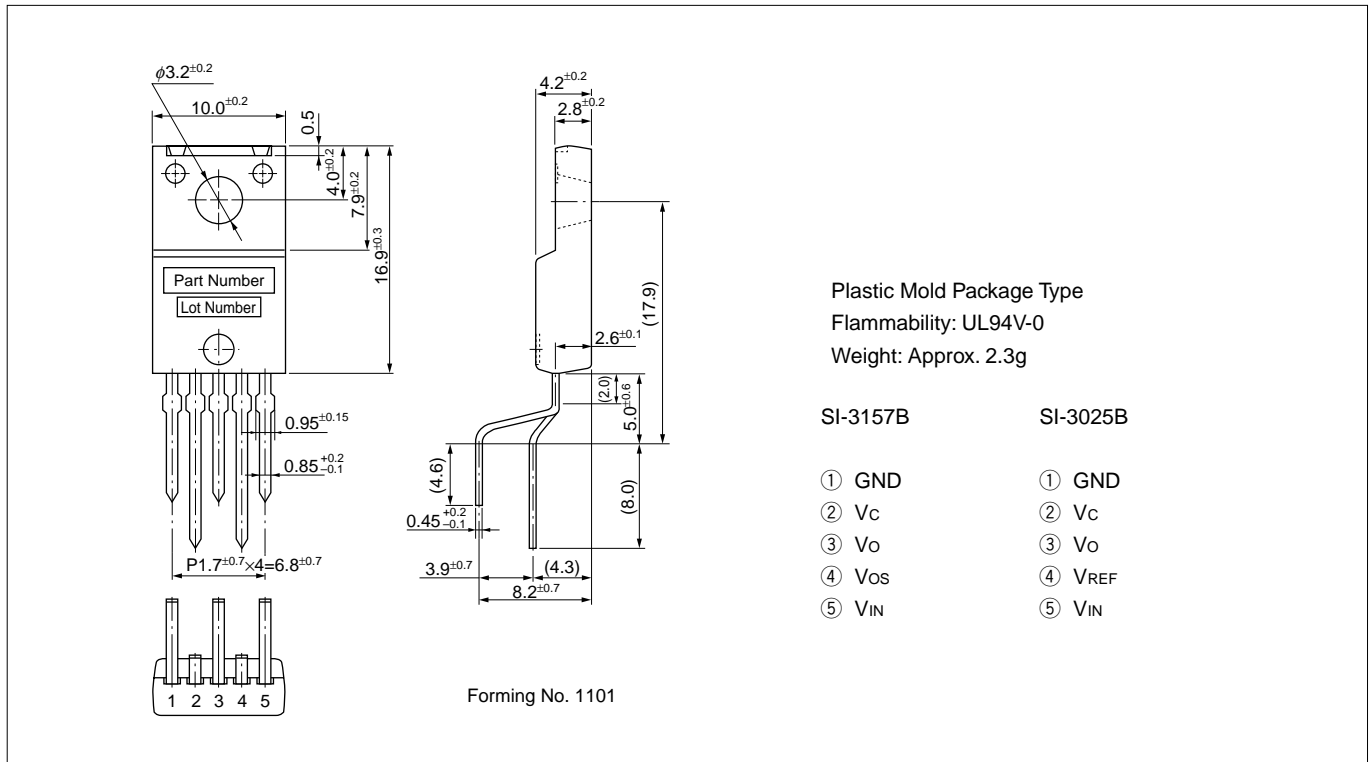
■Absolute Maximum Ratings

($T_a = 25^\circ C$)

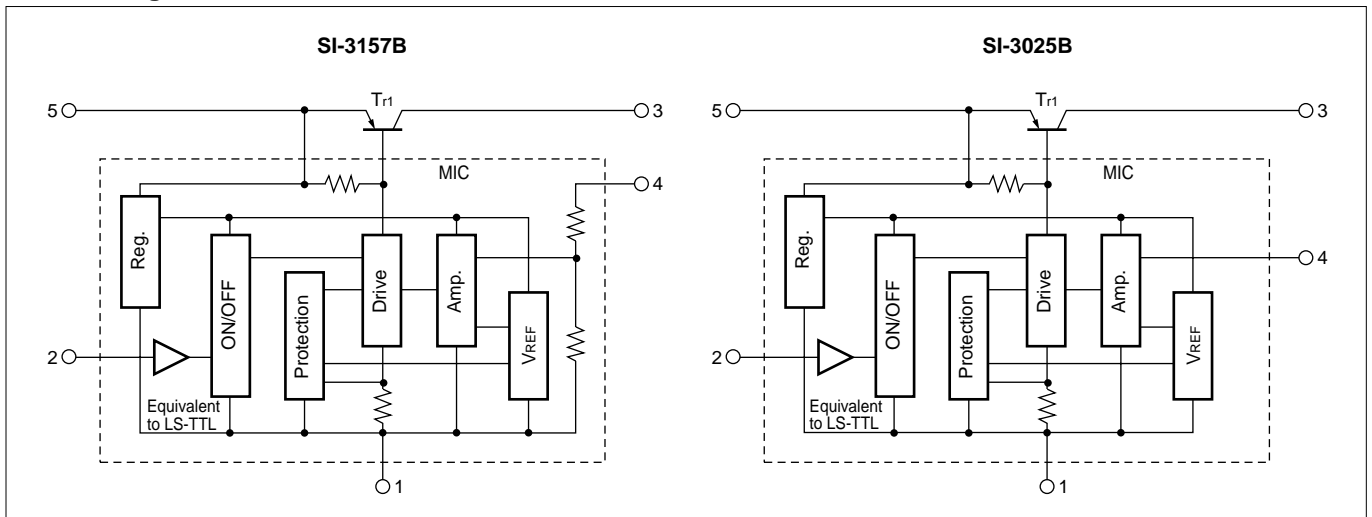
Parameter	Symbol	Ratings	Unit
DC Input Voltage	V_{IN}	35	V
Voltage of Output Control Terminal	V_C	V_{IN}	V
DC Output Current	I_o	0.27 ^{*1}	A
Power Dissipation	P_{D1}	14(With infinite heatsink)	W
	P_{D2}	1.5(Without heatsink, stand-alone operation)	W
Junction Temperature	T_j	-40 to +125	$^\circ C$
Ambient Operating Temperature	T_{op}	-30 to +100	$^\circ C$
Storage Temperature	T_{stg}	-40 to +125	$^\circ C$
Thermal Resistance (junction to case)	$R_{th(j-c)}$	7.0	$^\circ C/W$
Thermal Resistance (junction to ambient air)	$R_{th(j-a)}$	66.7(Without heatsink, stand-alone operation)	$^\circ C/W$

■Outline Drawing

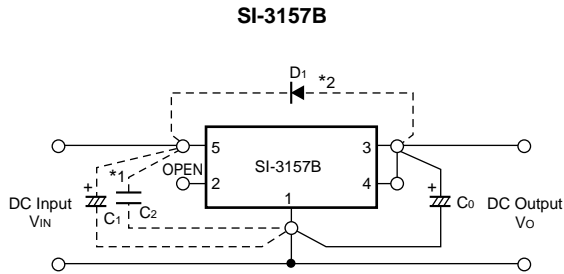
(unit:mm)



■Block Diagram



■Standard External Circuit



- C₀ : Output capacitor (47 to 100μF)
- *1 C₁ } Oscillation prevention capacitor
- C₂ } (C₁: Approx. 47μF, C₂: 0.33μF)

These capacitors are required if the input line is inductive and in the case of long wiring. Tantalum capacitors are recommended for C₁ and C₀, particularly at low temperatures.

- *2 D₁ : Protection diode

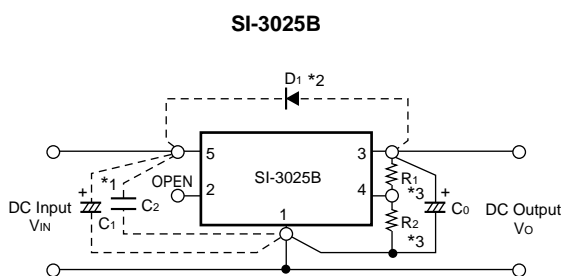
This diode is required for protection against reverse biasing of the input and output. Sanken EU2Z is recommended.

- *3 R₁ } External resistor for setting output voltage

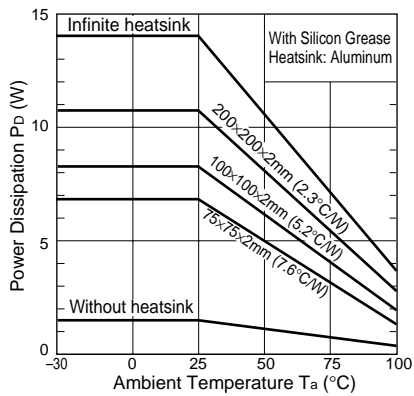
R₂ } Relationship between output voltage V_o and external resistors R₁ and R₂ is as follows.

$$V_o = V_{REF} \cdot \left(1 + \frac{R_1}{R_2} \right) \quad (V_{REF} = 2.55V(\text{typ.}))$$

R₂ must be 2.55kΩ for stable operation.



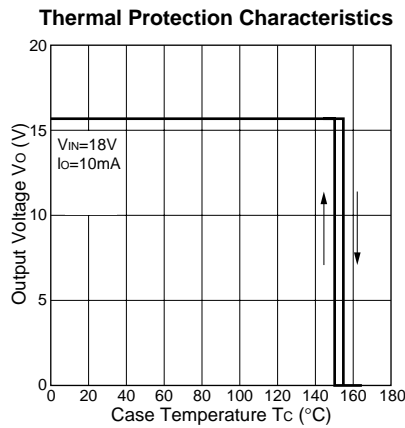
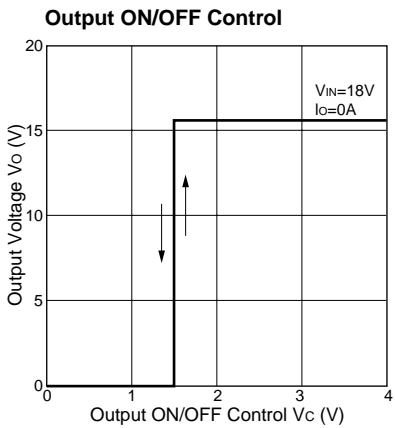
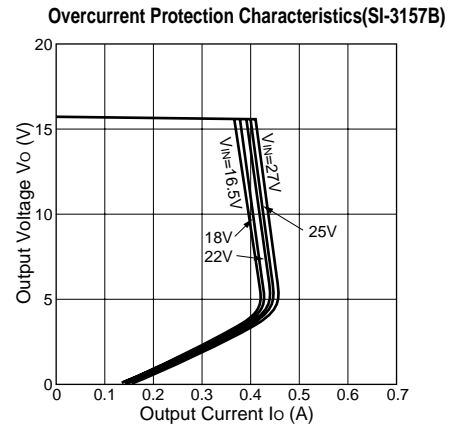
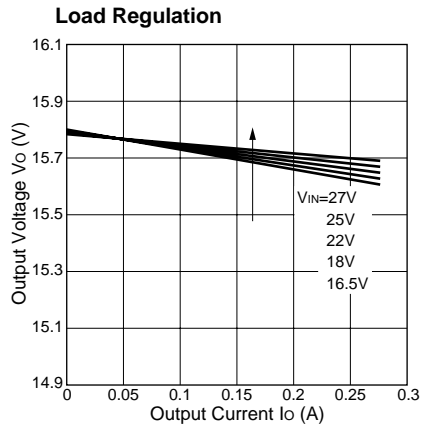
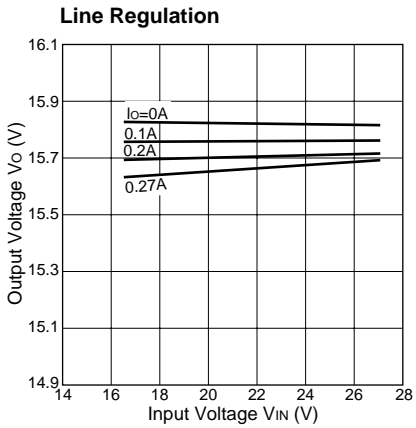
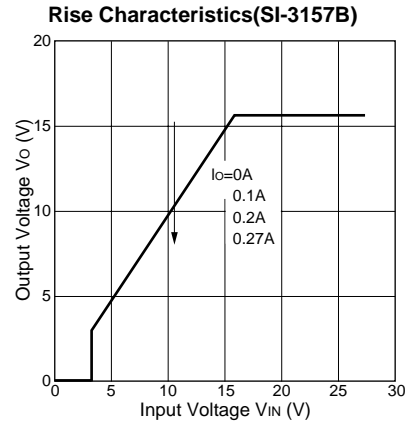
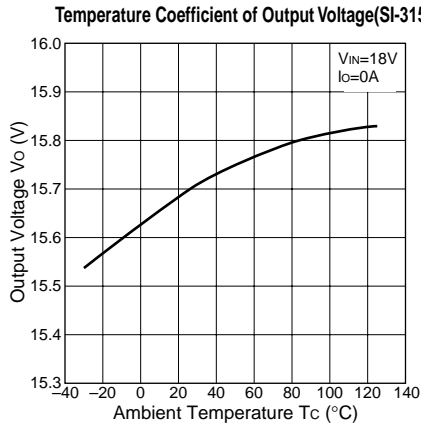
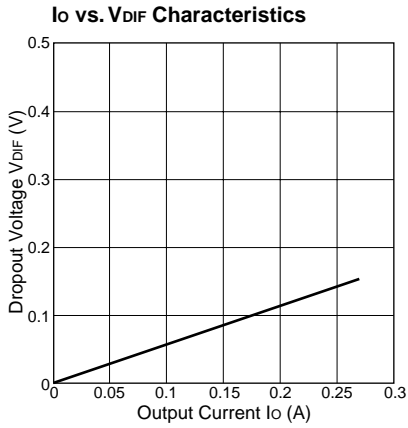
■T_a-P_d Characteristics



$$P_d = I_o \cdot [V_{IN}(\text{mean}) - V_o]$$

■Typical Characteristics (When setting $V_o=15.7V$ for SI3025B)

($T_a=25^\circ C$)



Note on Thermal Protection:

The thermal protection circuit is intended for protection against heat during instantaneous short-circuiting. Its operation is not guaranteed for short-circuiting over extended periods of time.