

RoHS

COMPLIANT

Vishay Siliconix

Load Switch with Level-Shift

PRODUCT SUMMARY				
V _{DS2} (V)	r _{DS(on)} (Ω)	I _D (A)		
1.8 to 20	0.165 at V _{IN} = 4.5 V	± 1.2		
	0.222 at V _{IN} = 2.5 V	± 1.0		
	0.303 at V _{IN} = 1.8 V	± 0.7		

FEATURES

- TrenchFET[®] Power MOSFETS: 1.8 V Rated
- ESD Protected: 2000 V On Input Switch, V_{ON/OFF}
- 165 m Ω Low r_{DS(on)}
- 1.8 to 20 V Input
- 1.5 to 8 V Logic Level Control
- Low Profile, Small Footprint SC70-6 Package
- Adjustable Slew-Rate

APPLICATIONS

Level Shift for Portable Devices

DESCRIPTION

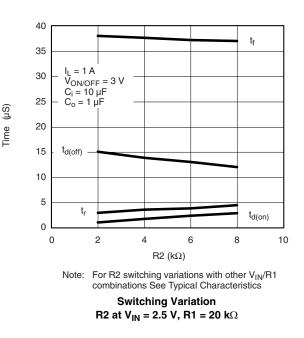
The Si1869DH includes a P- and N-Channel MOSFET in a single SC70-6 package. The low on-resistance P-Channel TrenchFET is tailored for use as a load switch. The N-Channel, with an external resistor, can be used as a level-shift to

Si1869DH 2.3 VOUT VIN 0 O Q2 R1 C1 6 6 ON/OFF O LOAD Co Q1 C_i 1 R2 GND R2 0

COMPONENTS					
R1	R1 Pull-Up Resistor Typical 10 kΩ to 1 Meg.				
R2	Optional Slew-Rate Control	Typical 0 to 100 k Ω^*			
C1	Optional Slew-Rate Control	Typical 1000 pF			

*Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on.

drive the P-Channel load-switch. The N-Channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si1869DH operates on supply lines from 1.8 to 20 V, and can drive loads up to 1.2 A.



The Si1869DH is ideally suited for high-side load switching in portable applications. The integrated N-Channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

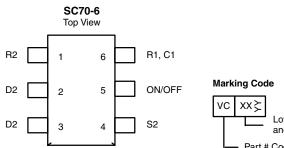
APPLICATION CIRCUITS

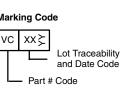
Si1869DH

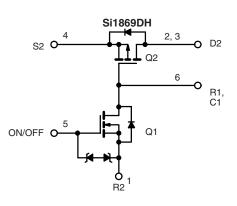
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FUNCTIONAL BLOCK DIAGRAM







Ordering Information: Si1869DH-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \degree C$, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage (D2-S2)		V _{DS}	- 20	V		
Input Voltage		V _{IN}	20			
ON/OFF Voltage		V _{ON/OFF}	8			
Load Current	Continuous ^{a, b}	IL.	± 1.2	A		
Load Current	Pulsed ^{b, c}		± 3			
Continuous Intrinsic Diode Conduction ^a		۱ _S	- 0.4			
Maximum Power Dissipation ^a		P _D	1.0	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		
ESD Rating, MIL-STD-883D Human Body Model (100 pF, 1500 Ω)		ESD	2	kV		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient (continuous current) ^a	R _{thJA}	100	125	°C/W	
Maximum Junction-to-Foot (Q2)	R _{thJF}	44	55	0/11	

SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
OFF Characteristics							
Reverse Leakage Current	I _{FL}	$V_{IN} = 8 V, V_{ON/OFF} = 0 V$			1	μA	
Diode Forward Voltage	V_{SD}	I _S = - 0.4 A	0.4	0.6	1.1	V	
ON Characteristics							
Input Voltage Range	V _{IN}		1.8		20		
Drain to Source Breakdown Voltage (P-Channel)	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V	
		$V_{ON/OFF}$ = 1.5 V, V_{IN} = 4.5 V, I_D = 1.2 A		0.132	0.165		
On-Resistance (P-Channel) at 1 A	r _{DS(on)}	$V_{ON/OFF}$ = 1.5 V, V_{IN} = 2.5 V, I_D = 1.0 A		0.177	0.222	Ω	
		$V_{ON/OFF}$ = 1.5 V, V_{IN} = 1.8 V, I_D = 0.7 A		0.242	0.303		
On-State (P-Channel) Drain-Current		$V_{\text{IN-OUT}} \leq 0.2$ V, V_{IN} = 5 V, $V_{\text{ON/OFF}}$ = 1.5 V	1			А	
On-State (F-Channel) Drain-Current	ID(on)	$V_{IN\text{-}OUT} \leq 0.3$ V, V_{IN} = 3 V, $V_{ON/OFF}$ = 1.5 V	1			A	

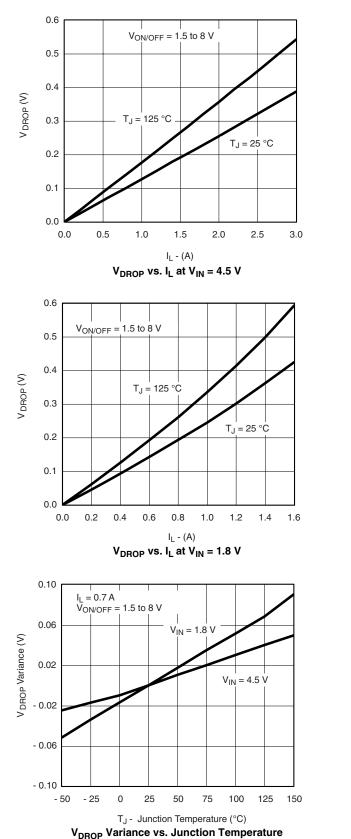
Notes:

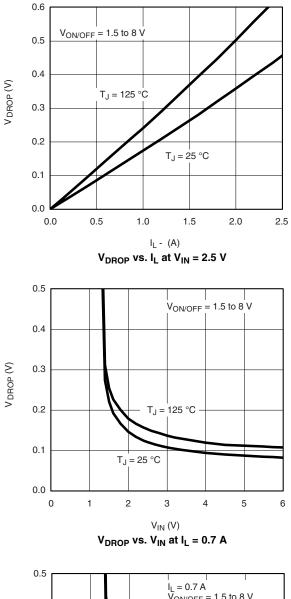
a. Surface Mounted on FR4 Board.

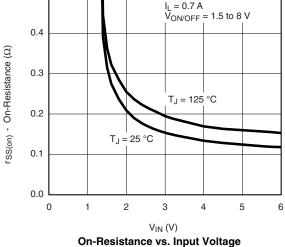
b. V_{IN} = 20 V, $V_{ON/OFF}$ = 8 V, T_A = 25 °C.

c. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







Document Number: 73449 S-61965-Rev. B, 09-Oct-06 www.vishay.com 3

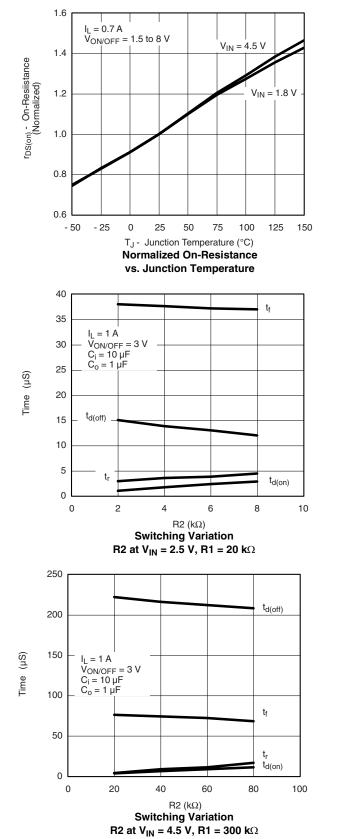
TYPICAL CHARACTERISTICS 25 °C, unless noted

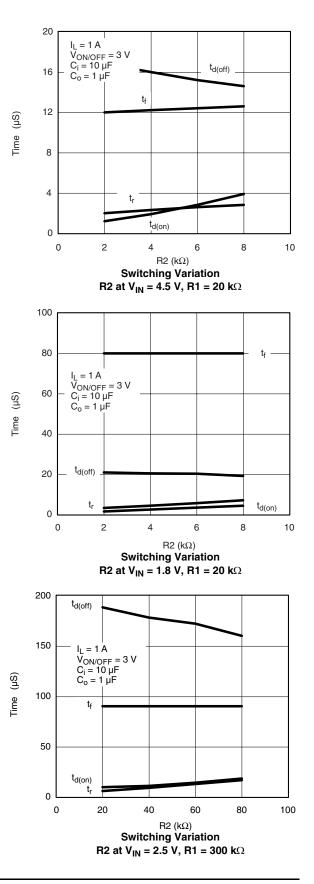
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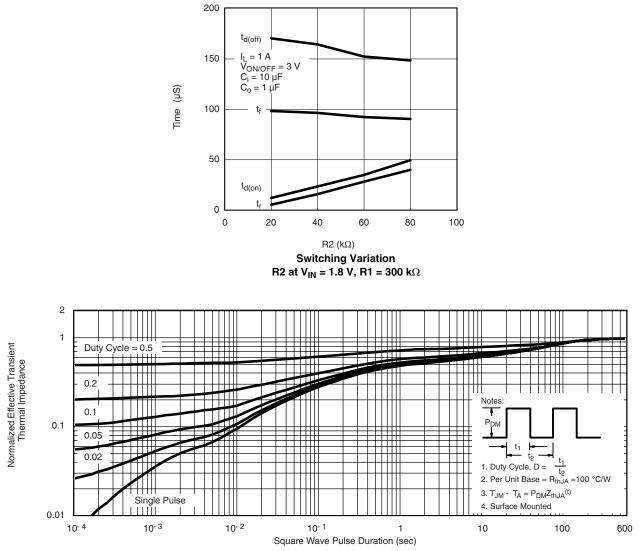








TYPICAL CHARACTERISTICS 25 °C, unless noted



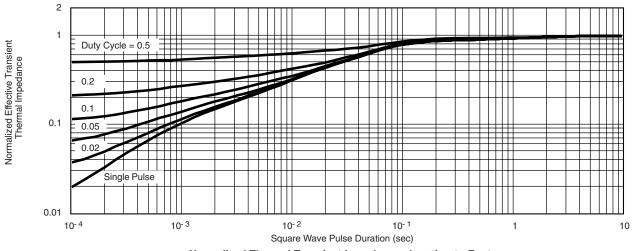
Normalized Thermal Transient Impedance, Junction-to-Ambient

Si1869DH



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TYPICAL CHARACTERISTICS 25 °C, unless noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73449.



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