

## **Dual N-Channel 40-V (D-S) MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$ $I_{D}\left(\Omega\right)$			
40	0.021 at V <sub>GS</sub> = 10 V	7.4		
	0.028 at V <sub>GS</sub> = 4.5 V	6.4		

# SO-8 S<sub>1</sub> 1 8 D<sub>1</sub> G<sub>1</sub> 2 7 D<sub>1</sub> S<sub>2</sub> 3 6 D<sub>2</sub> G<sub>2</sub> 4 5 D<sub>2</sub>

Ordering Information: Si4942DY-T1-E3 (Lead (Pb)-free)

Top View

Si4942DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

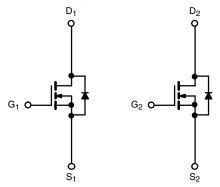
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

#### ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- · Low Power Synchronous Rectifier
- Automotive 12 V Systems



N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	40		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Operation of the Company of the Comp	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	7.4	5.3	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		5.8	4.3	
Pulsed Drain Current		I <sub>DM</sub>	30		Α
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	25		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.8	0.9	
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.1	1.1	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manifestor Locality to Austrianta	t ≤ 10 s	- R <sub>thJA</sub>	50	60	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	28	34		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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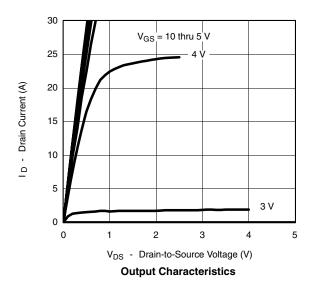
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u> </u>		1				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valtana Busin Comunit		V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance <sup>a</sup>	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.4 A	0.017		0.021		
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6.4 \text{ A}$		0.023	0.028	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 7.4 A		25		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 1.8 A, V <sub>GS</sub> = 0 V		0.75	1.1	V	
Dynamic <sup>b</sup>			1				
Total Gate Charge	$Q_g$			21	32	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.7 \text{ A}$		3.3			
Gate-Drain Charge	$Q_{gd}$			5.8			
Gate Resistance	$R_{g}$		0.5	1.1	1.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	20		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 20 V, $R_L$ = 20 $\Omega$		10	15	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 6 \Omega$		31	50		
Fall Time	t <sub>f</sub>			11	20		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.8 A, dI/dt = 100 A/μs		30	60		

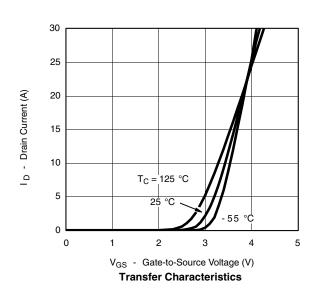
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



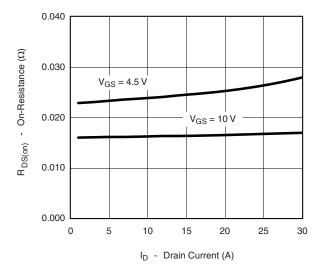




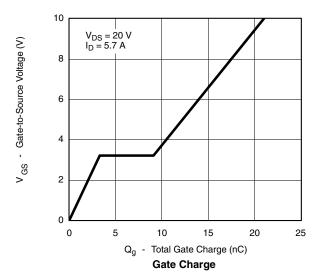


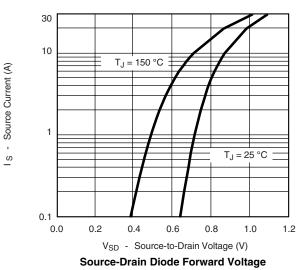


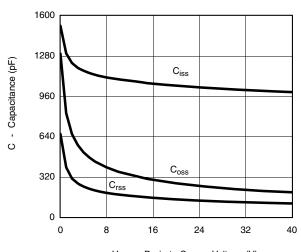
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#### On-Resistance vs. Drain Current

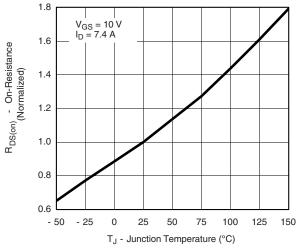




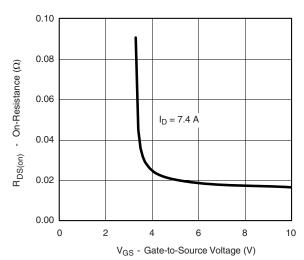


V<sub>DS</sub> - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature

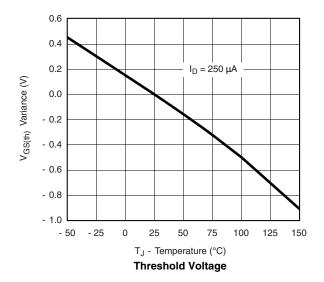


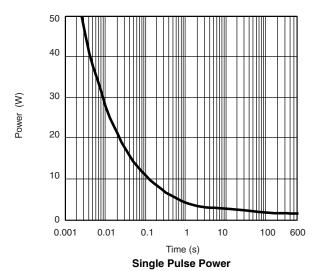
On-Resistance vs. Gate-to-Source Voltage

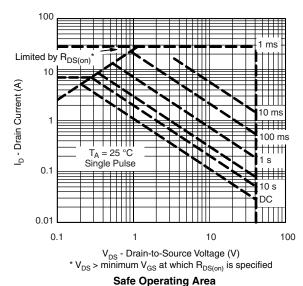
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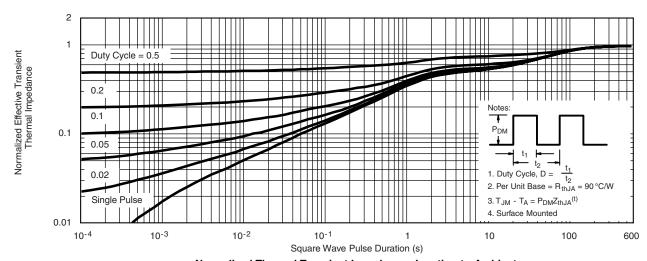
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#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted





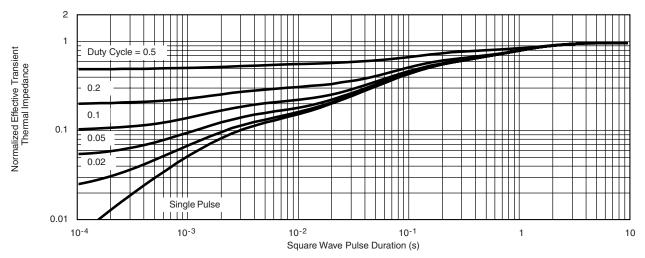




Normalized Thermal Transient Impedance, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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